

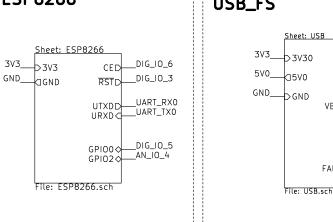
External Clocks

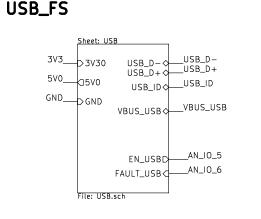
ka GND

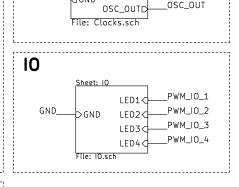
Sheet: Clocks

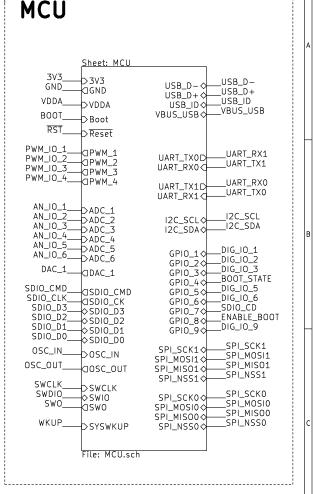
OSC_INC

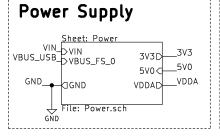
_OSC_IN

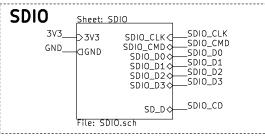




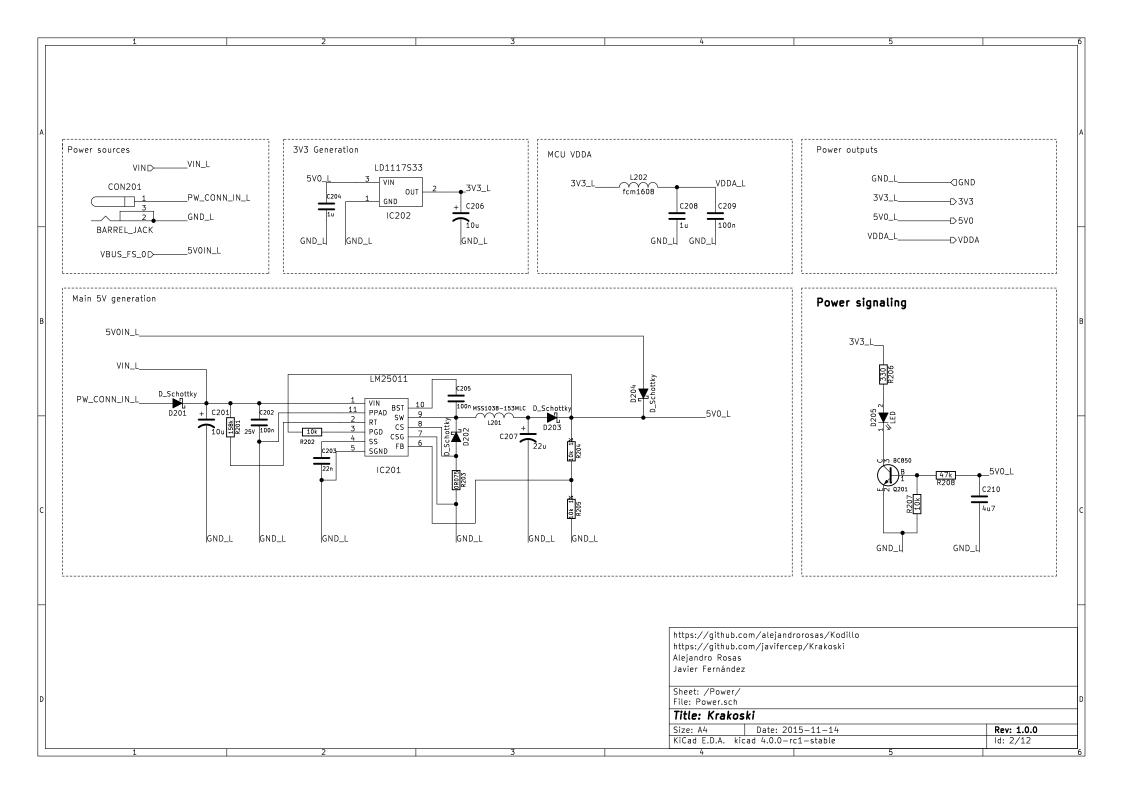


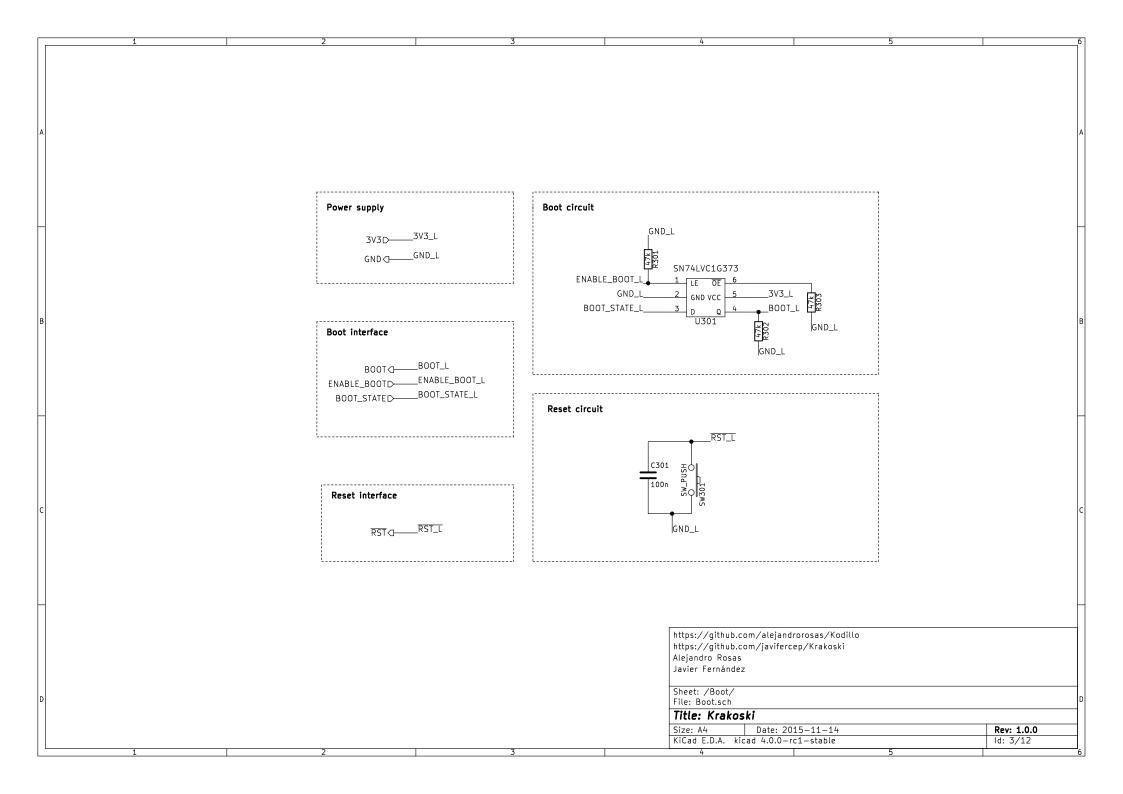


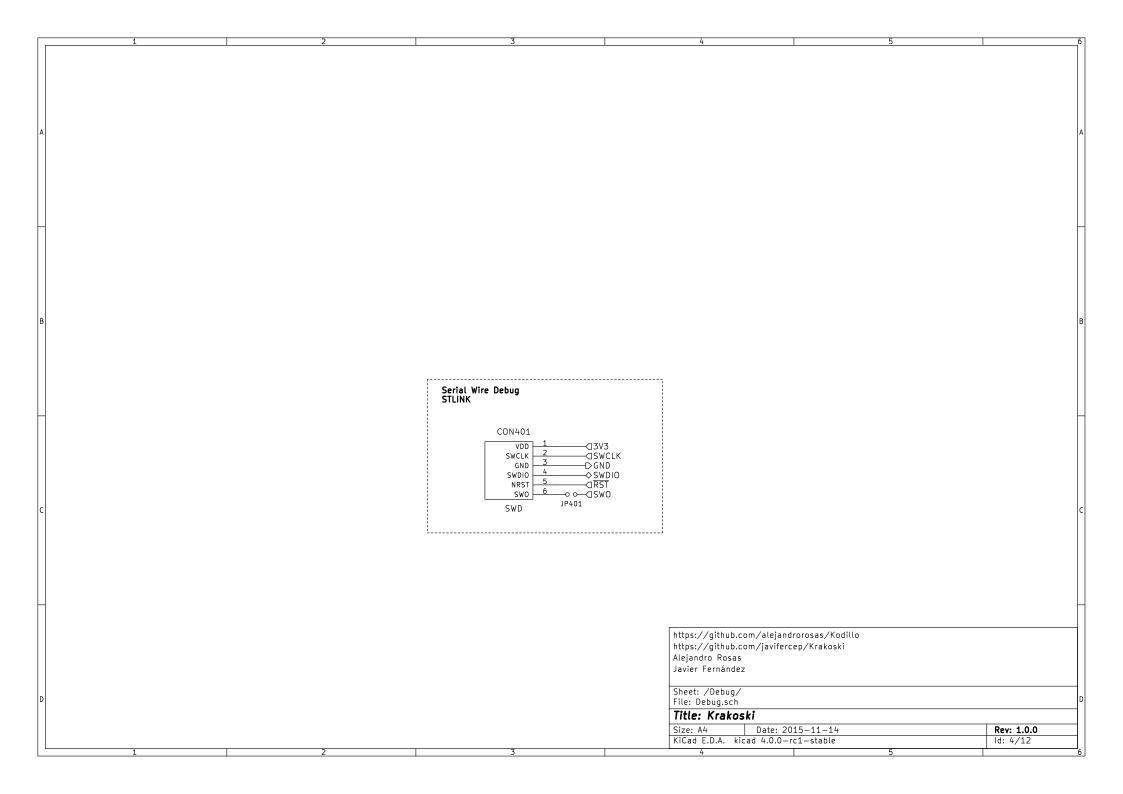


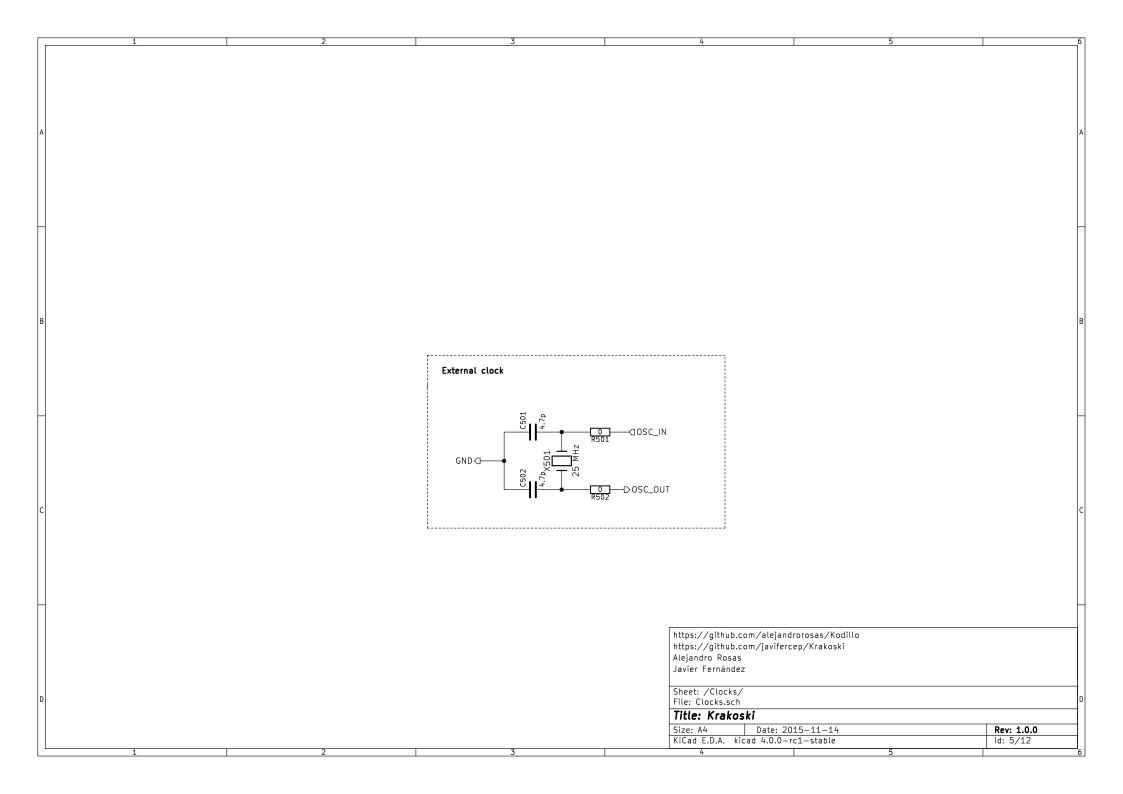


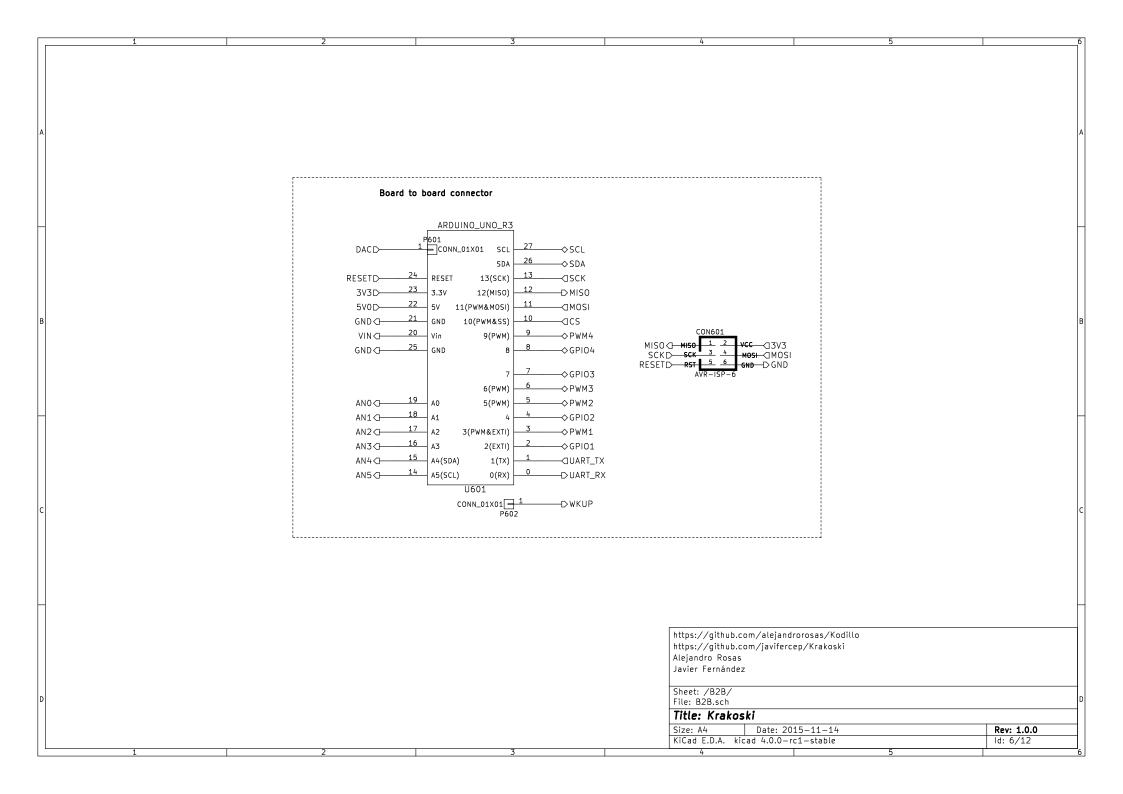
https://github.com/alejandrorosas/Kodillo https://github.com/javifercep/Krakoski Alejandro Rosas Javier Fernández Sheet: / File: Krakoski.sch Title: Krakoski Size: A4 Date: 2015-11-14 Rev: 1.0.0 KiCad E.D.A. kicad 4.0.0-rc1-stable ld: 1/12









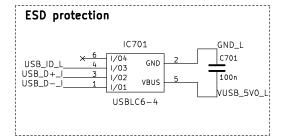


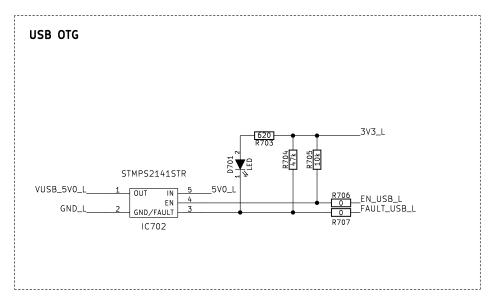
USB_Full_Speed

Power supply

3V30D—3V3_L GNDD—GND_L 5V0 (1—5V0_L

USB MCU Interface





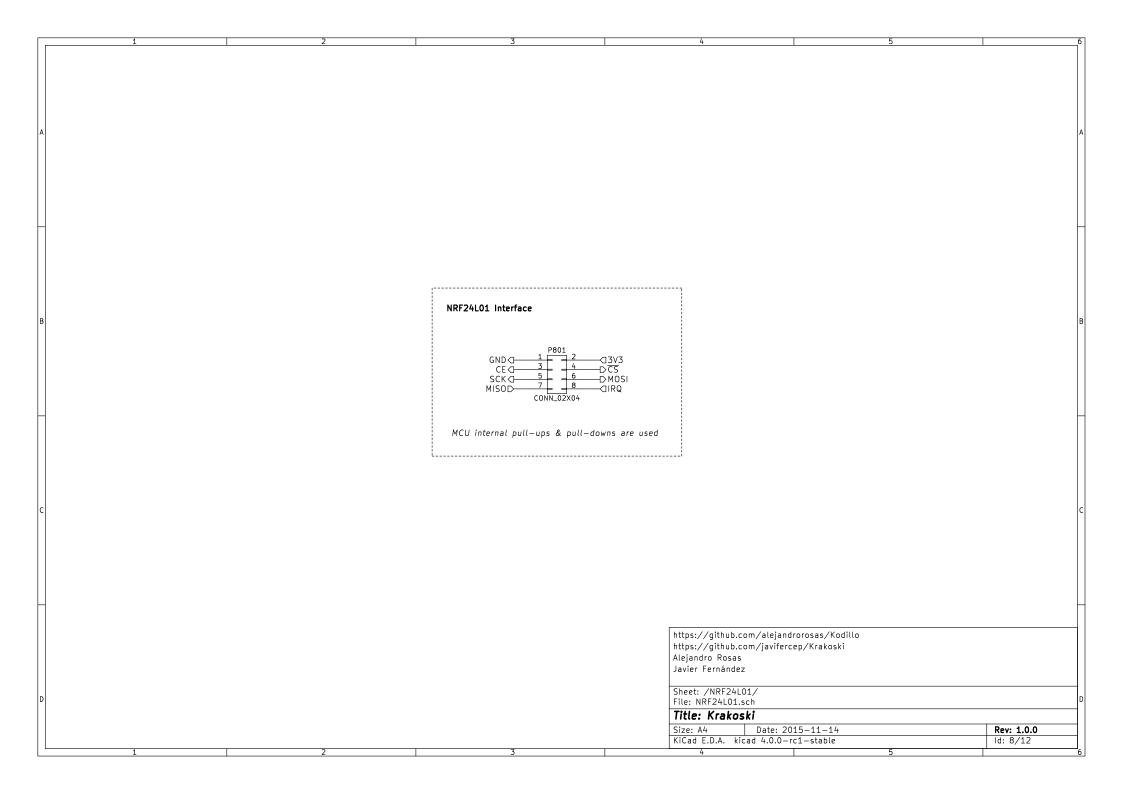
https://github.com/alejandrorosas/Kodillo https://github.com/javifercep/Krakoski Alejandro Rosas Javier Fernández

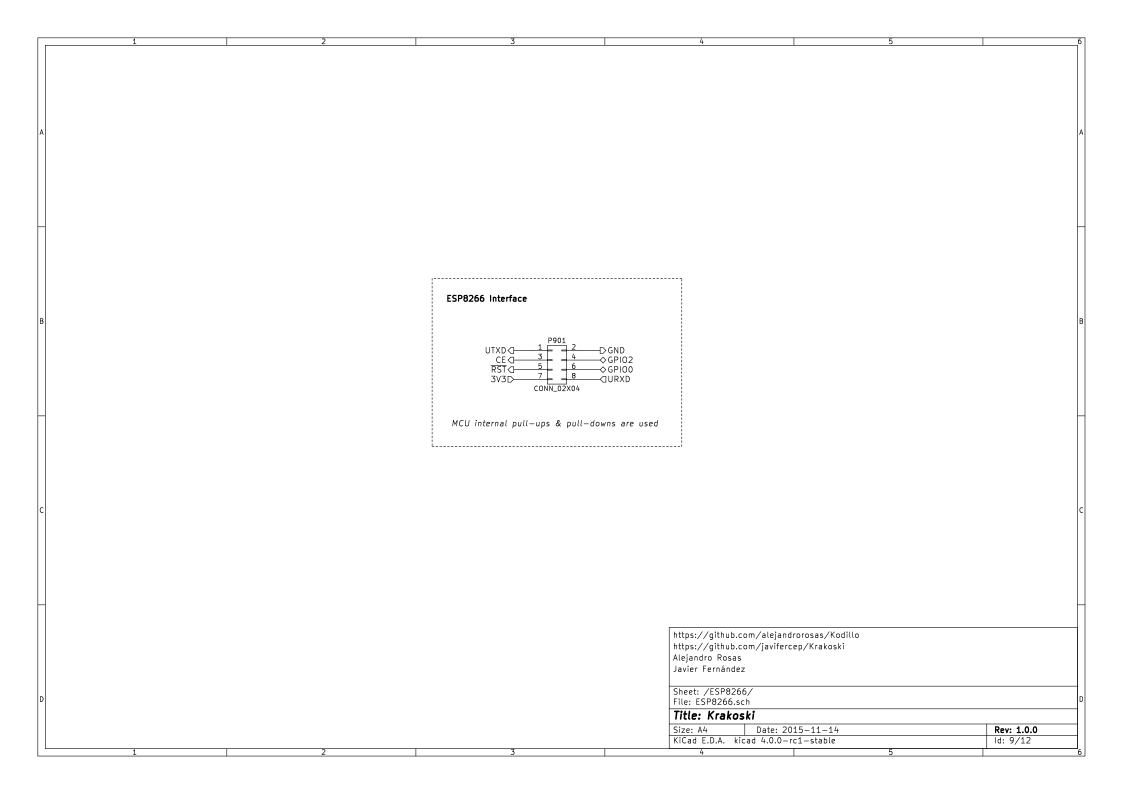
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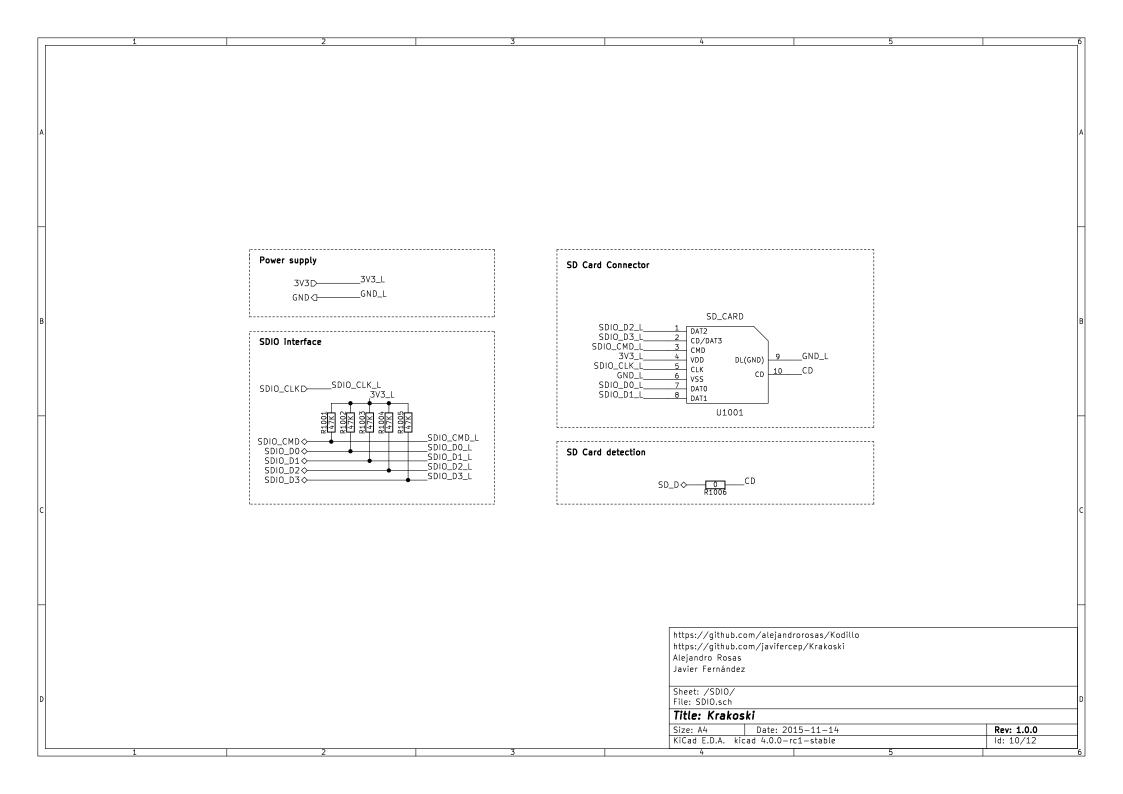
Title: Krakoski

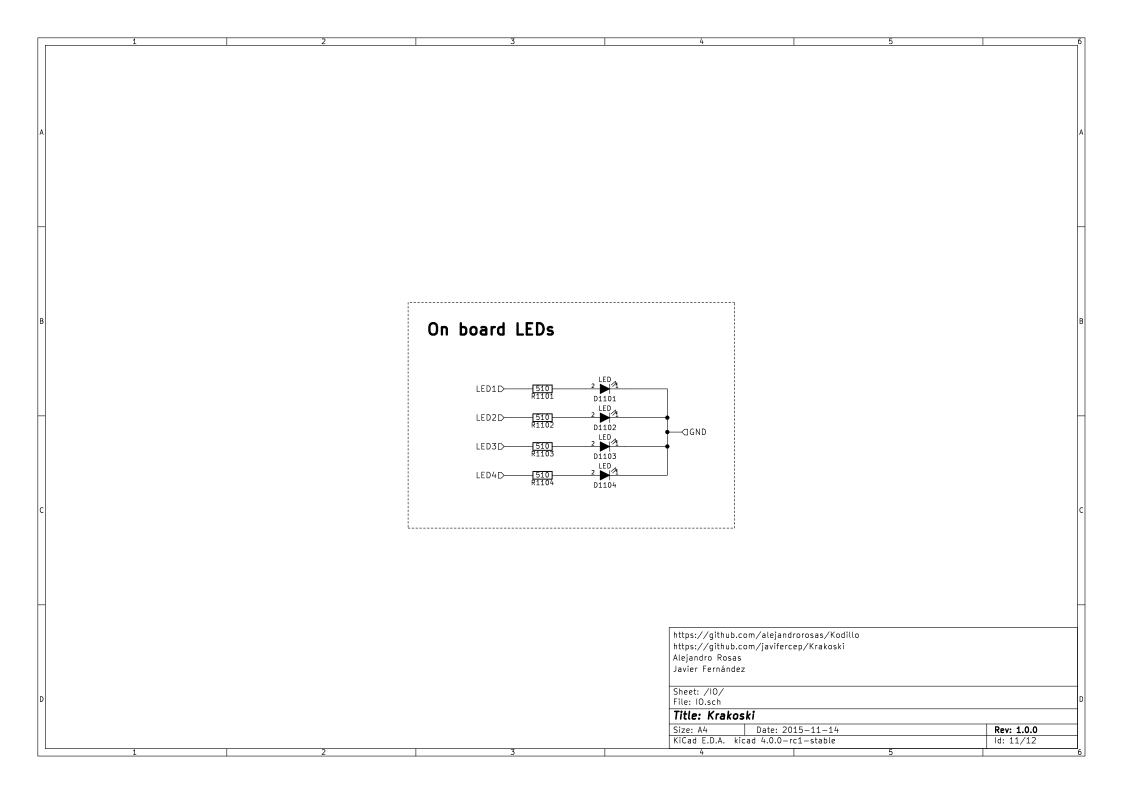
 Size: A4
 Date: 2015-11-14
 Rev: 1.0.0

 KiCad E.D.A. kicad 4.0.0-rc1-stable
 Id: 7/12

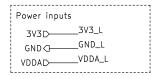


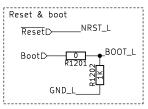






MCU: STM32F405 / STM32F205 / STM32F105

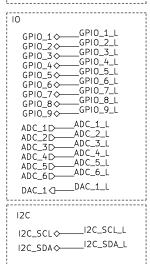




External Clock

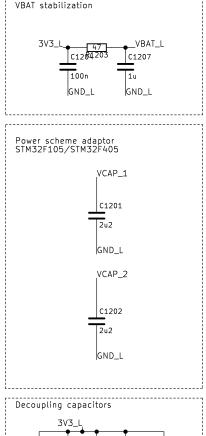
OSC_IND—OSC_IN_L

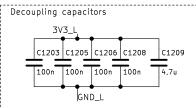
OSC_OUT (__OSC_OUT_L

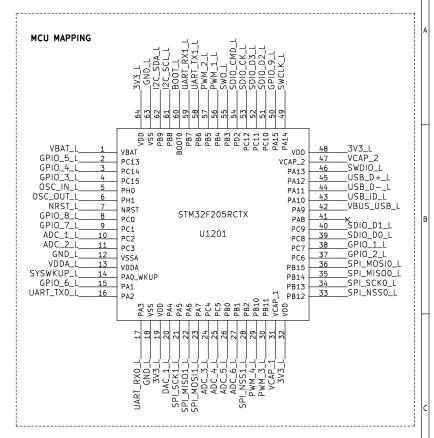


```
UART & USART
 UART_TX0 - UART_TX0_L
 UART_RXO_L
 UART_TX1 C UART_TX1_L
UART_RX1 D UART_RX1_L
 UART RX1D-
            SPI_SCK0_L
 SPL SCK0♦
            SPI_MOSIO_L
 SPI MOSIO ♦
           SPI_MISOO_L
 SPI_MISO0 ♦-
           SPI_NSSO_L
 SPI_NSS0 ♦-
            SPI_SCK1_L
           SPI_MOSI1_L
 SPI_MOSI1 ♦-
            SPI_MISO1_L
 SPI_MISO1♦
           _SPI_NSS1_L
 SPI NSS1 ♦
USB_FS
 SDIO
  SDIO_D2 SDIO_D2_L
   SDIO_D1 $ SDIO_D1_L
   SDIO_DO SDIO_DO_L
Debug & SYS
```









PA10, PA9 & PA8 reserved for future improvements — USB OTG

https://github.com/alejandrorosas/Kodillo
https://github.com/javifercep/Krakoski
Alejandro Rosas
Javier Fernández

Sheet: /MCU/
File: MCU.sch

Title: Krakoski

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 Rev: 1.0.0

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 Id: 12/12