

ENCE461 Schematic Review

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Bring your schematics, printed on A3 paper

1 Common

1. Student names and group number in title block
2. Battery fusing (this is mandatory)
3. Use serial wire debug interface for programming
4. 3.3 V MCU regulator can be back driven (the CAT6219 can). Others may require a diode
5. Short circuit protection for MCU pio pins going to external headers
6. Battery voltage monitoring
7. The analogue inputs to the MCU do not exceed 3.3 V
8. LEDs for debugging
9. Jumpers for mode configuration (radio channel select)
10. Test points
11. Ground test points
12. USB signals are not swapped
13. USB has series termination resistors (27 ohm)
14. Power supply filtering for radio (recommend ferrite bead or resistor in series with power rail with parallel capacitor)
15. The radio needs to be connected to SPI pins (MISO/PA12, MOSI/PA13, SCK/PA14)
16. Radio IRQ connected to MCU PIO pin
17. Radio uses SPI clock (SCK) not the TWI clock (TWCK)
18. TWI uses TWCK0/PA4 and TWD0/PA3 or TWCK1/PB5 and TWD1/PB4.
19. SAM4S erase pin on testpoint
20. SAM4S has 12 MHz crystal

21. Reset button connected to NRST pin
22. Power on/off button connected to WKUPn pin
23. Avoid PB4–PB5 for general I/O (they default to JTAG pins on reset but can be reconfigured in software)
24. Have external pull-down resistors to ensure chips are disabled on power-up
25. Have a few spare PIO pins connected to pads for last minute mods.

2 Hat board

1. Drive circuit for piezo tweeter
2. Accelerometer nCS pin connected high to enable I2C operation
3. Pullup resistors on TWI (I2C) bus

3 Racer board

1. Can isolate H-bridge and buck regulator with jumper or zero ohm resistors
2. MOSFET(s) for actuator (if use p-channel MOSFET need transistor to provide sufficient gate voltage to turn MOSFET off)
3. H-bridge driven by four PWM signals (it is best to use PWMHx, note PWMLx and PWMHx are complementary)
4. H-bridge AISEN and BISEN pins connected to ground (unless using current control)