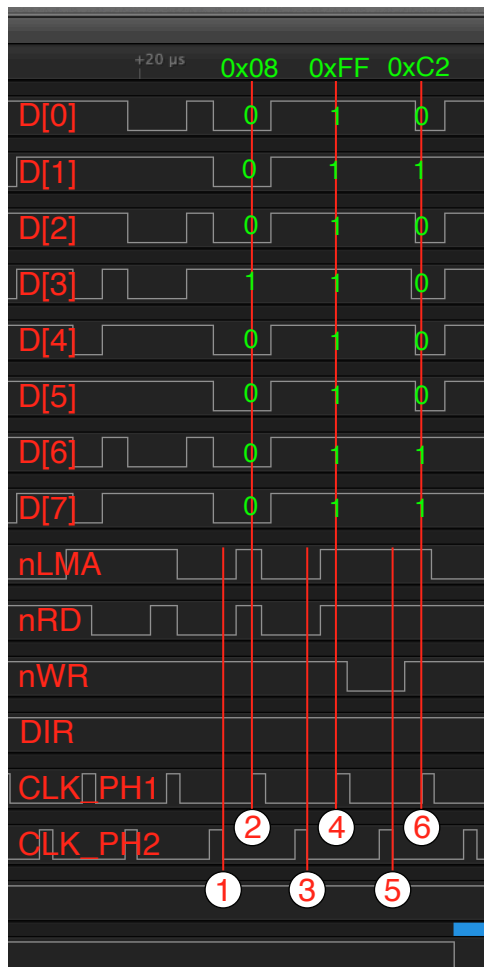


HP-85 Write of 0xC2 to Tape Control Register

- ① nLMA read at falling edge of CLK_PH2
- ② Address LSB latched on rising edge of CLK_PH1
- ③ nLMA read at falling edge of CLK_PH2
- ④ Address MSB latched on rising edge of CLK_PH1
- ⑤ nWR read at falling edge of CLK_PH2
- ⑥ DATA latched on rising edge of CLK_PH1



HP-85 Read of 0x11 from Tape Status Register

- ① nLMA read at falling edge of CLK_PH2
- ② Address LSB latched on rising edge of CLK_PH1
- ③ nLMA read at falling edge of CLK_PH2
- ④ Address MSB latched on rising edge of CLK_PH1
- ⑤ nRD read at falling edge of CLK_PH2
- ⑥ DIR is asserted from falling edge of CLK_PH2 until rising edge of CLK_PH1.

