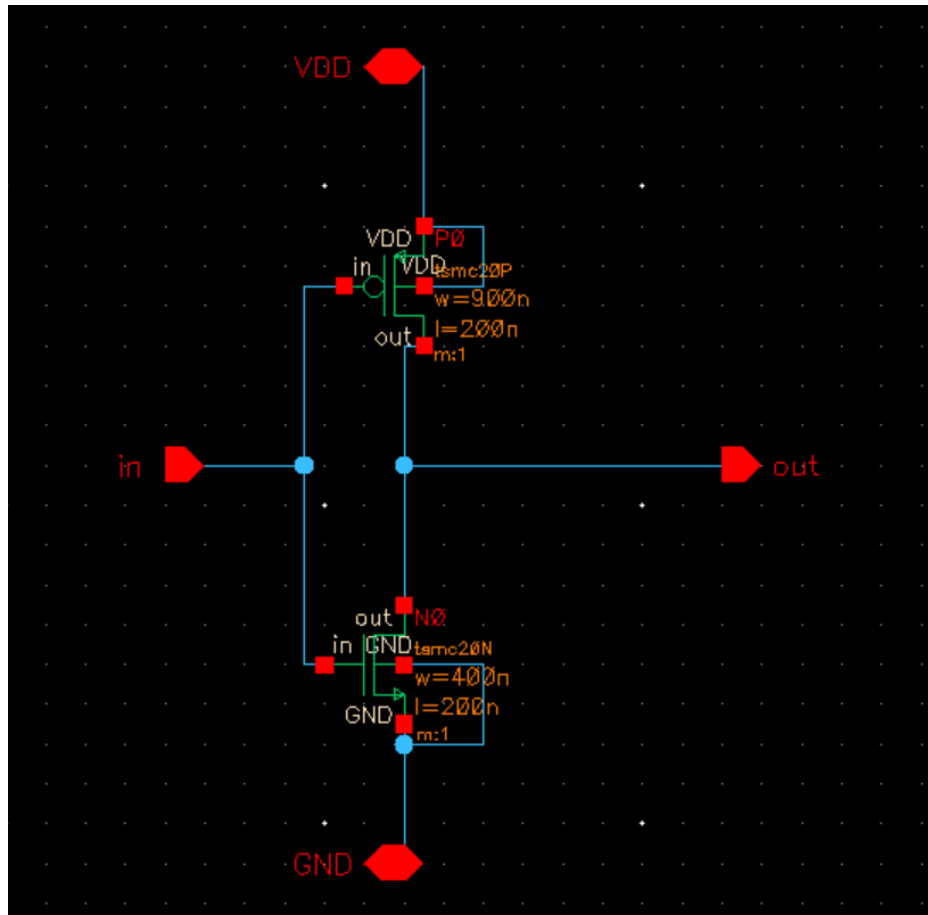


Joshua Clapp
Ta: Neha Gupta
Section: 506/606

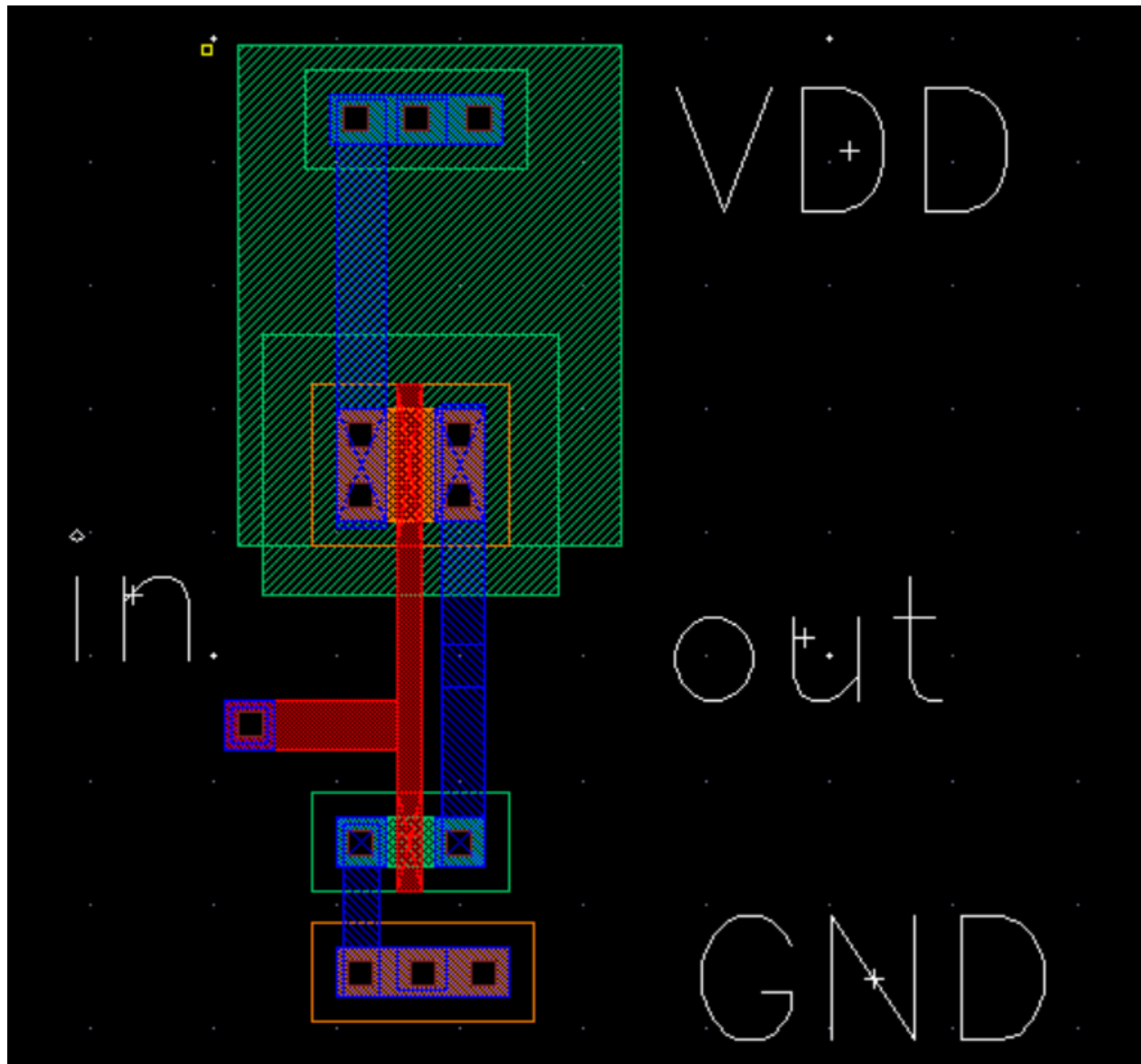
Lab 2

Schematic

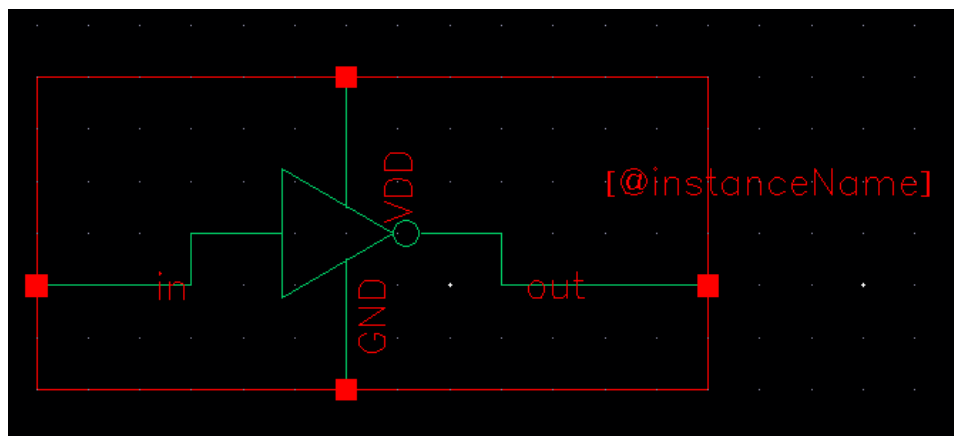
Inverter:



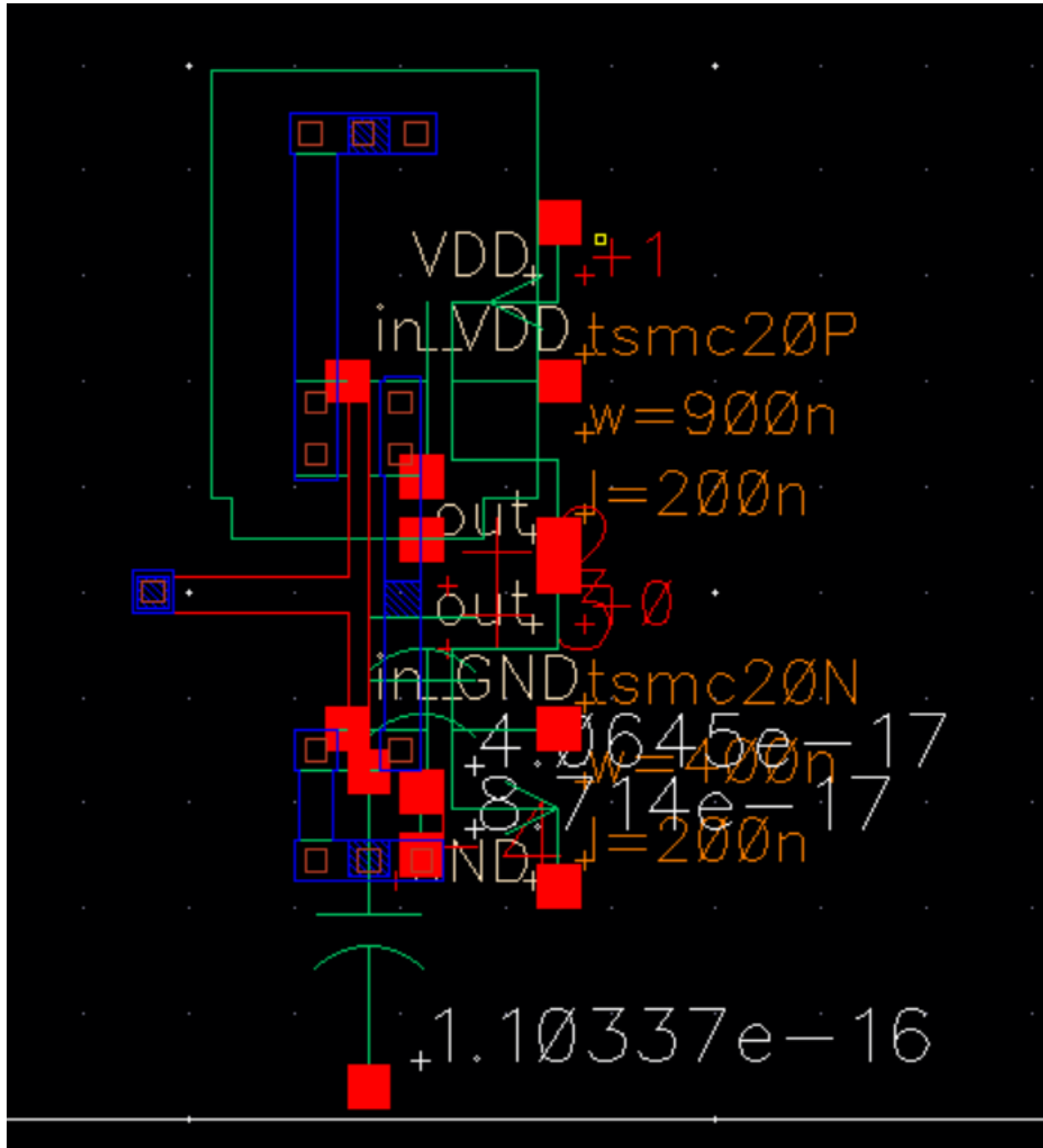
Layout:



Symbol:



Extracted:



Output:

@(#) \$CDS: LVS version 6.1.8-64b 08/04/2020 19:19 (cpgsrv11) \$

Command line: /opt/coe/cadence/IC618/tools.lnx86/dfl/bin/64bit/LVS -dir
/home/ugrads/j/jay123q/cadence/LVS -l -s -t /home/ugrads/j/jay123q/cadence/LVS/layout
/home/ugrads/j/jay123q/cadence/LVS/schematic
Like matching is enabled.
Net swapping is enabled.
Using terminal names as correspondence points.
Compiling Diva LVS rules...

Net-list summary for /home/ugrads/j/jay123q/cadence/LVS/layout/netlist

count	
4	nets
4	terminals
1	pmos
1	nmos

Net-list summary for /home/ugrads/j/jay123q/cadence/LVS/schematic/netlist

count	
4	nets
4	terminals
1	pmos
1	nmos

Terminal correspondence points

N0	N3	GND
N3	N0	VDD
N2	N1	in
N1	N2	out

Devices in the netlist but not in the rules:

pcapacitor

Devices in the rules but not in the netlist:

cap nfet pfet nmos4 pmos4

The net-lists match.

	layout	schematic
	instances	
un-matched	0	0
rewired	0	0
size errors	0	0
pruned	0	0
active	2	2
total	2	2

	nets	
un-matched	0	0
merged	0	0
pruned	0	0

active	4	4
total	4	4

	terminals	
un-matched	0	0
matched but		
different type	2	2
total	4	4

Probe files from /home/ugrads/j/jay123q/cadence/LVS/schematic

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

? Terminal in's type in the schematic: input, in the layout: inputOutput

? Terminal out's type in the schematic: output, in the layout: inputOutput

prunenet.out:

prunedev.out:

audit.out:

Probe files from /home/ugrads/j/jay123q/cadence/LVS/layout

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

? Terminal in's type in the layout: inputOutput, in the schematic: input

? Terminal out's type in the layout: inputOutput, in the schematic: output

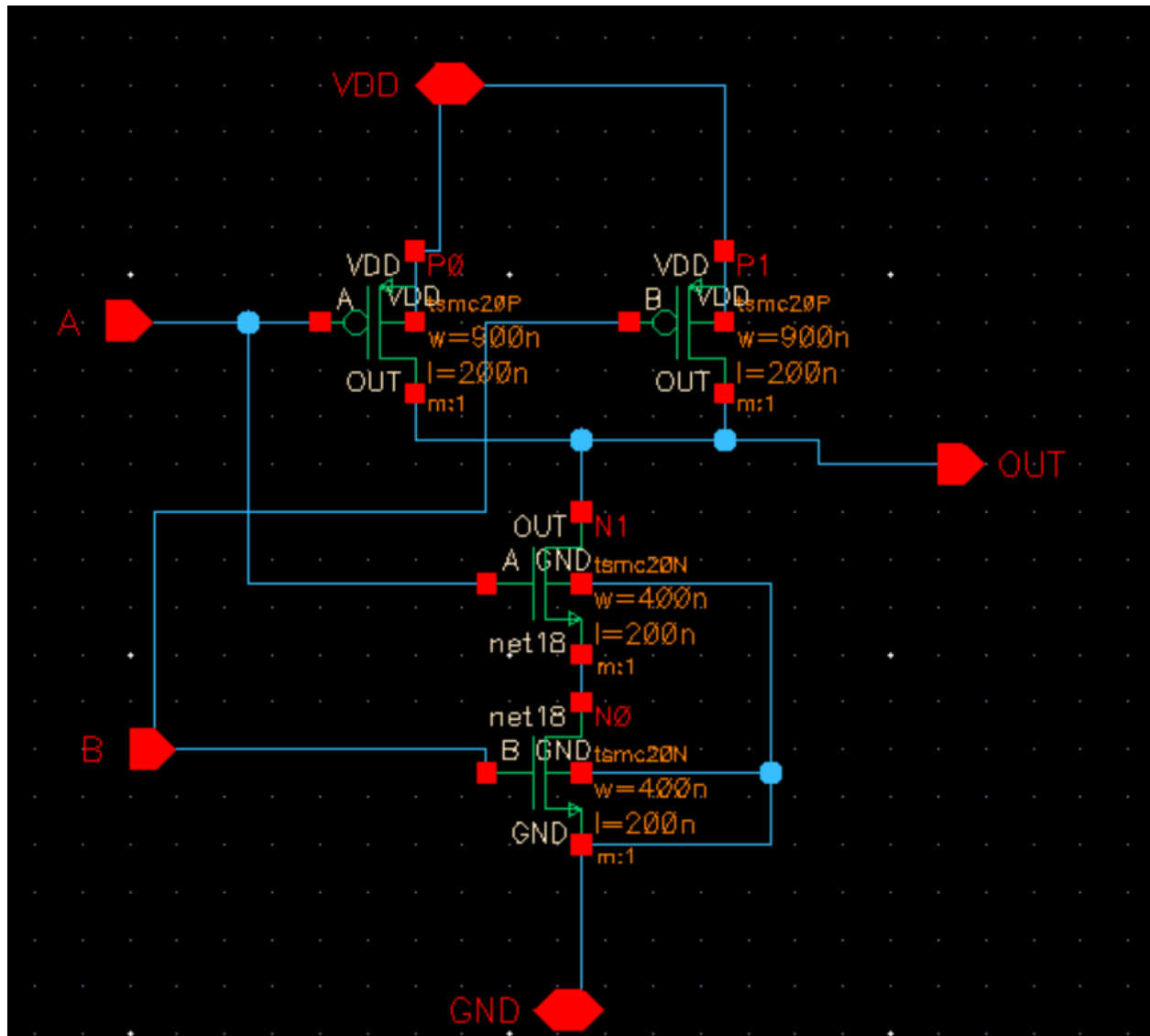
prunenet.out:

prunedev.out:

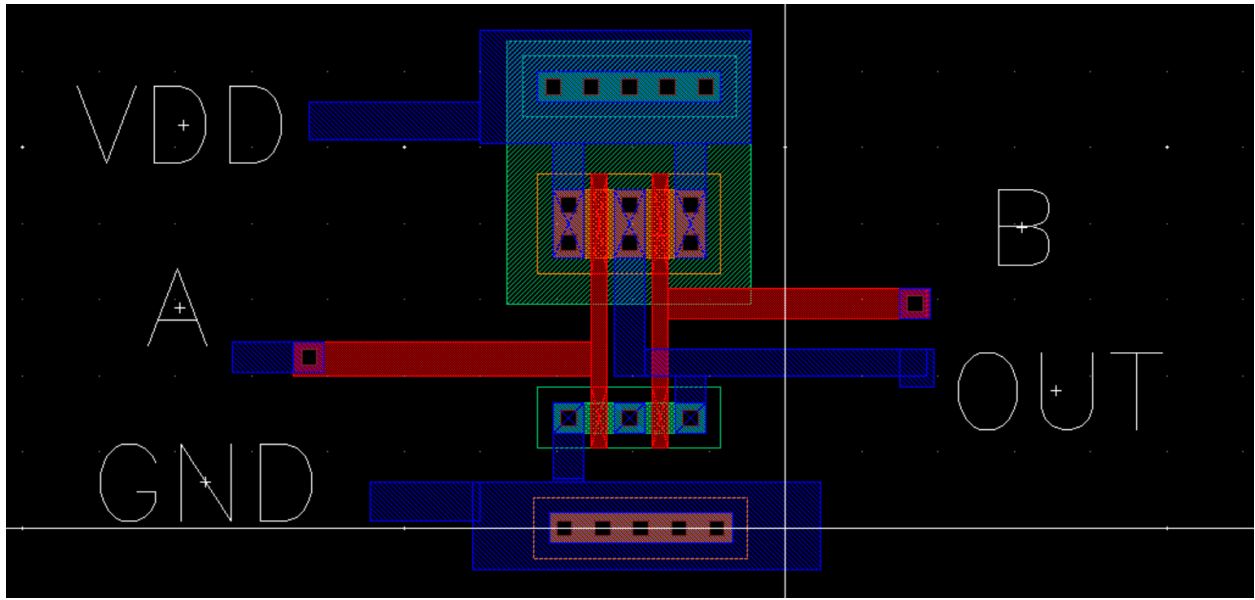
audit.out:

Schematic

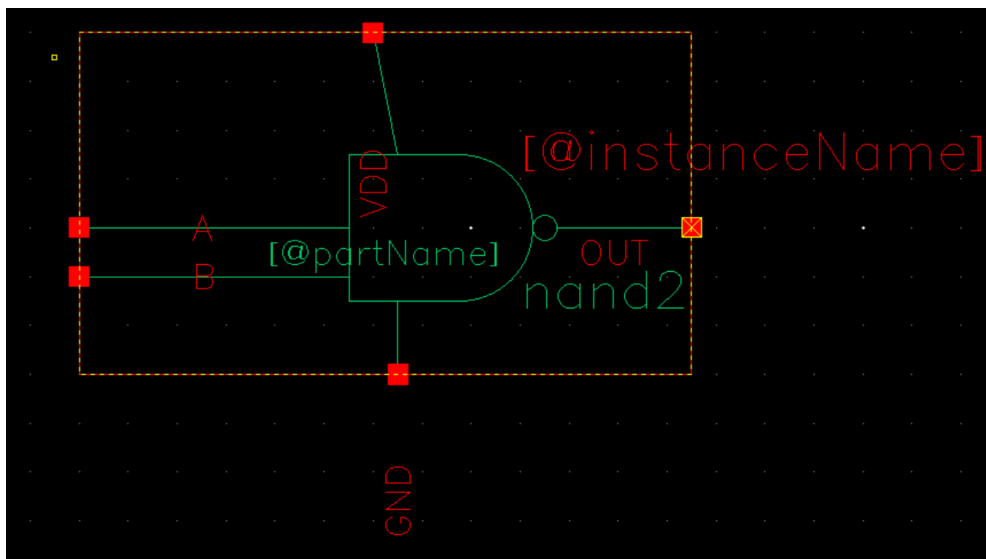
NAND2:



Layout:



Symbol:



Extracted:

count	
6	nets
5	terminals
2	pmos
2	nmos

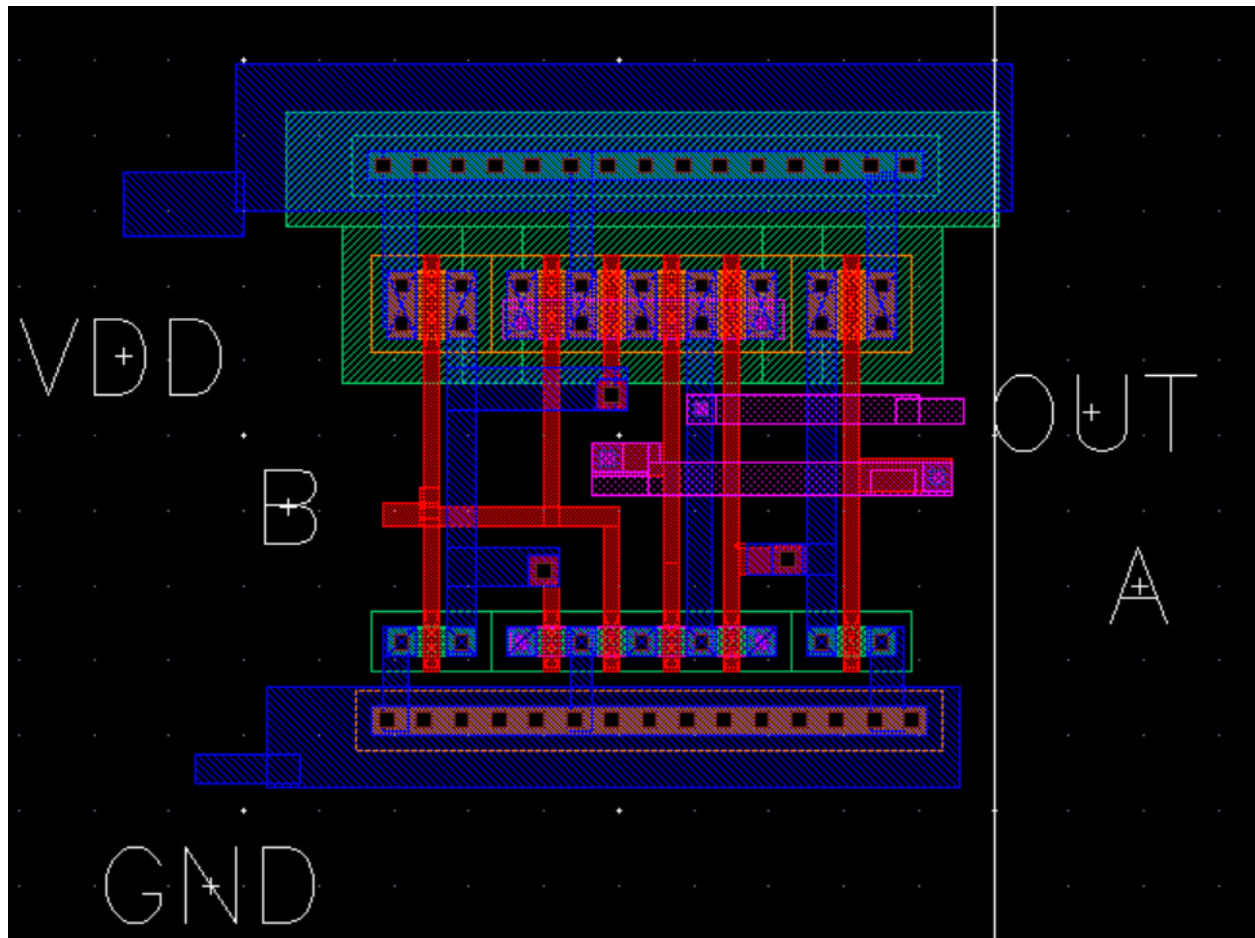
N4	N3	A
N3	N0	B
N1	N5	GND
N2	N4	OUT
N5	N1	VDD

pcapacitor

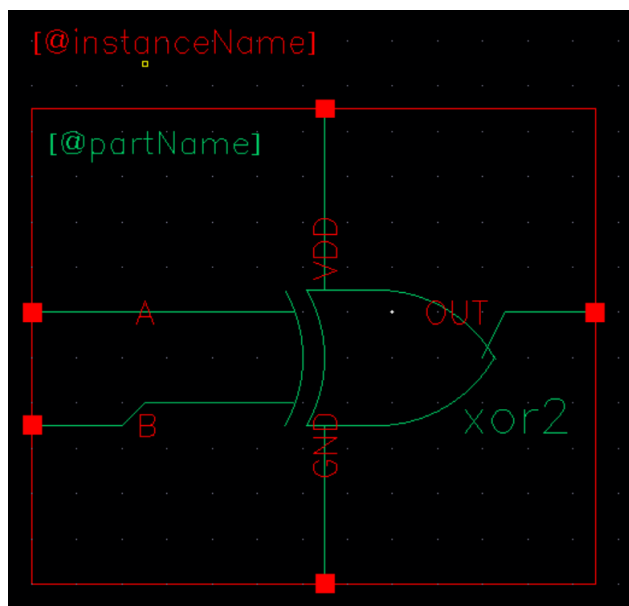
```
cap nfet pfet nmos4 pmos4
```

Schematic

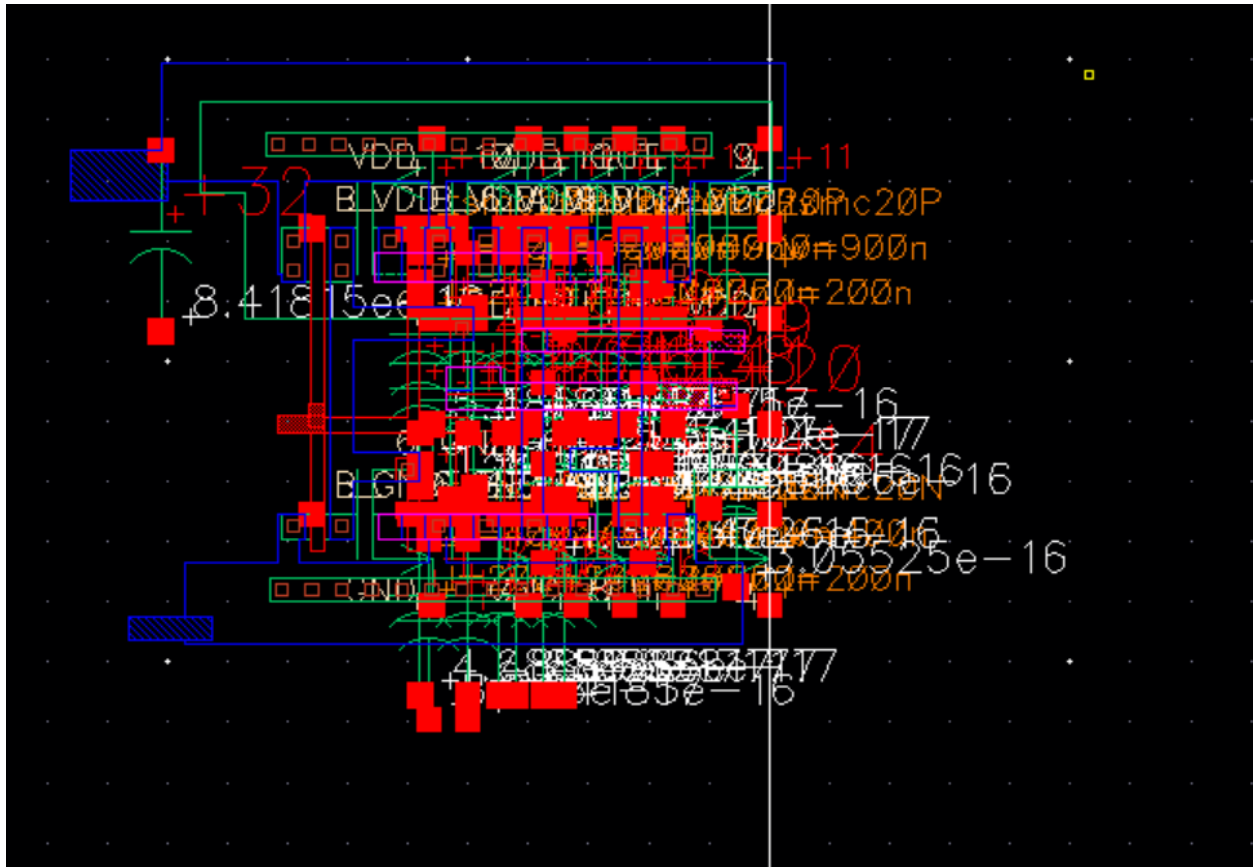
Layout:



Symbol:



Extracted:



Output:

@(#)\$CDS: LVS version 6.1.8-64b 08/04/2020 19:19 (cpgsrv11) \$

Command line: /opt/coe/cadence/IC618/tools.lnx86/dfl/bin/64bit/LVS -dir
/home/ugrads/j/jay123q/cadence/LVS -l -s -t /home/ugrads/j/jay123q/cadence/LVS/layout
/home/ugrads/j/jay123q/cadence/LVS/schematic
Like matching is enabled.
Net swapping is enabled.
Using terminal names as correspondence points.
Compiling Diva LVS rules...

Net-list summary for /home/ugrads/j/jay123q/cadence/LVS/layout/netlist

count	
11	nets
5	terminals
6	pmos
6	nmos

Net-list summary for /home/ugrads/j/jay123q/cadence/LVS/schematic/netlist

count	
11	nets
5	terminals

6	pmos
6	nmos

Terminal correspondence points

N9	N1	A
N8	N6	B
N6	N3	GND
N7	N5	OUT
N10	N9	VDD

Devices in the netlist but not in the rules:

pcapacitor

Devices in the rules but not in the netlist:

cap nfet pfet nmos4 pmos4

The net-lists match.

	layout	schematic
	instances	
un-matched	0	0
rewired	0	0
size errors	0	0
pruned	0	0
active	12	12
total	12	12

	nets	
un-matched	0	0
merged	0	0
pruned	0	0
active	11	11
total	11	11

	terminals	
un-matched	0	0
matched but different type	0	0
total	5	5

Probe files from /home/ugrads/j/jay123q/cadence/LVS/schematic

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out:

Probe files from /home/ugrads/j/jay123q/cadence/LVS/layout

devbad.out:

netbad.out:

mergenet.out:

termbad.out:

prunenet.out:

prunedev.out:

audit.out: