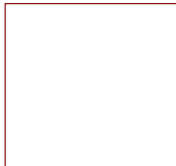


MCU



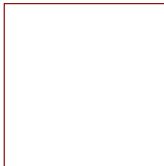
File: MCU.kicad\_sch

TFT\_ST7735



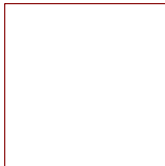
File: TFT\_ST7735.kicad\_sch

KEY\_ARRAY



File: KEY\_ARRAY.kicad\_sch

MACRO\_FN

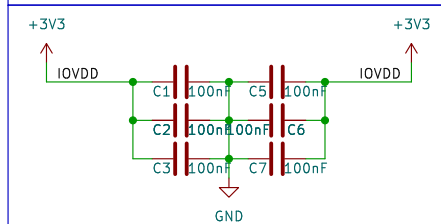
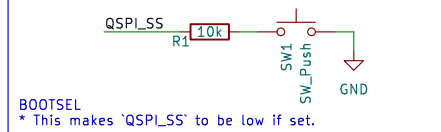
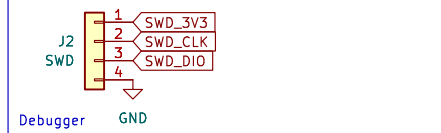
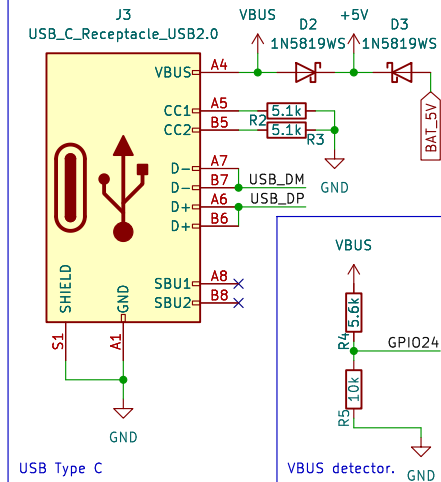


File: MACRO\_FN.kicad\_sch

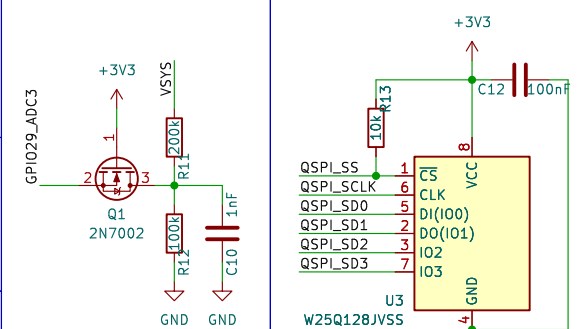
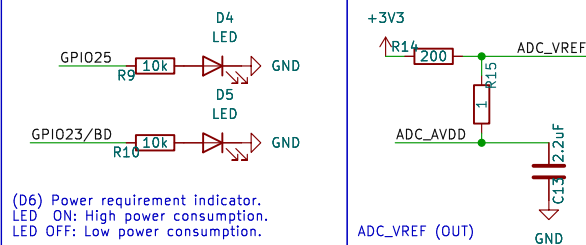
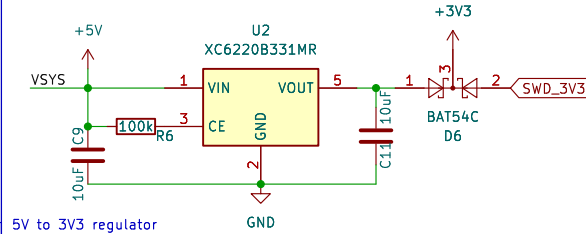
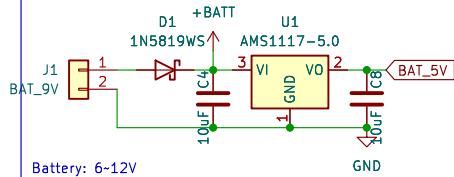
POWER\_CAP



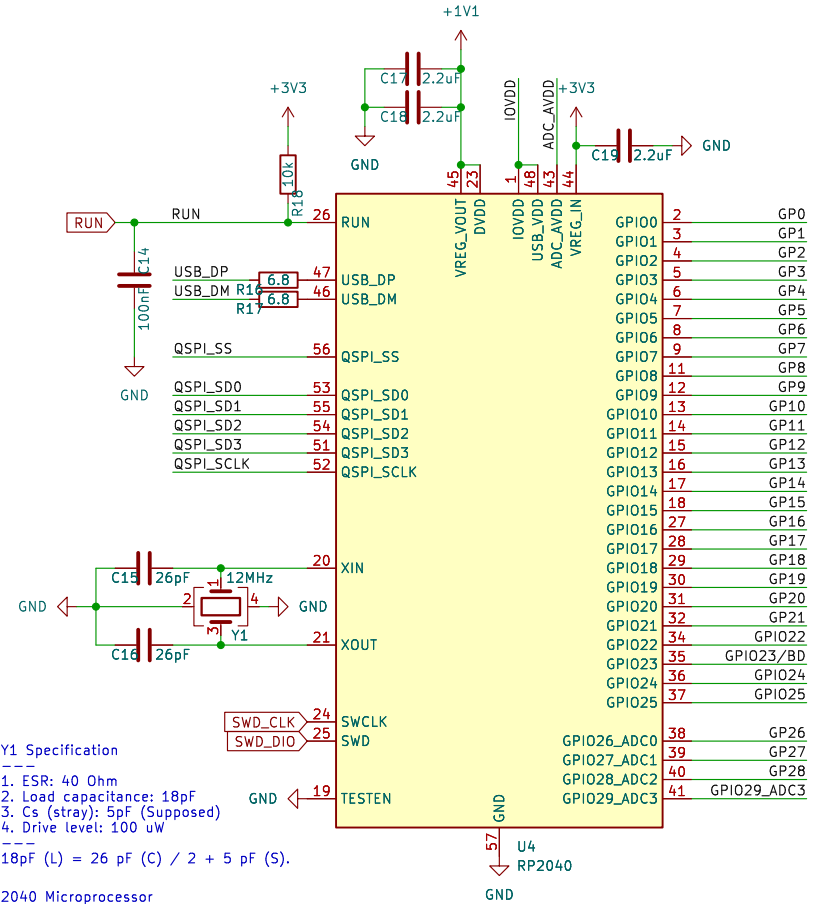
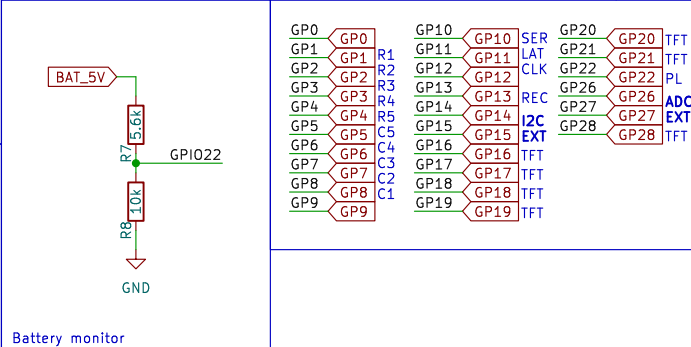
File: POWER\_CAP.kicad\_sch



\* place capacitors to IOVDD pins as near as possible.



VBUS monitor  
\* this monitors VBUS voltage.



Y1 Specification

- 1. ESR: 40 Ohm
  - 2. Load capacitance: 18pF
  - 3. Cs (stray): 5pF (Supposed)
  - 4. Drive level: 100 uV
- 18pF (L) = 26 pF (C) / 2 + 5 pF (S).

# TFT ST7735 Pin-mapping

1. GND - GND
2. VCC - 3V3 / 5V switch
3. SCL - GP18, SPI CLK
4. SDA - GP19, SPI MOSI
5. RES - GP21, Reset, active low.
6. DC - GP20, Data command, 0: register addr, 1: data.
7. CS - GP17, Chip select, active low.
8. BLK - GP28, PWM or 3V3 with 150R.

