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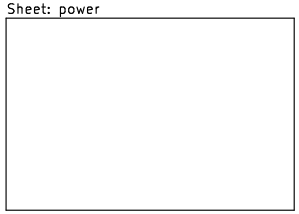

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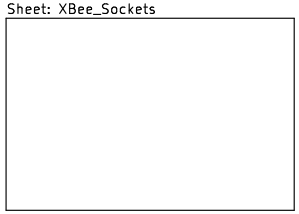

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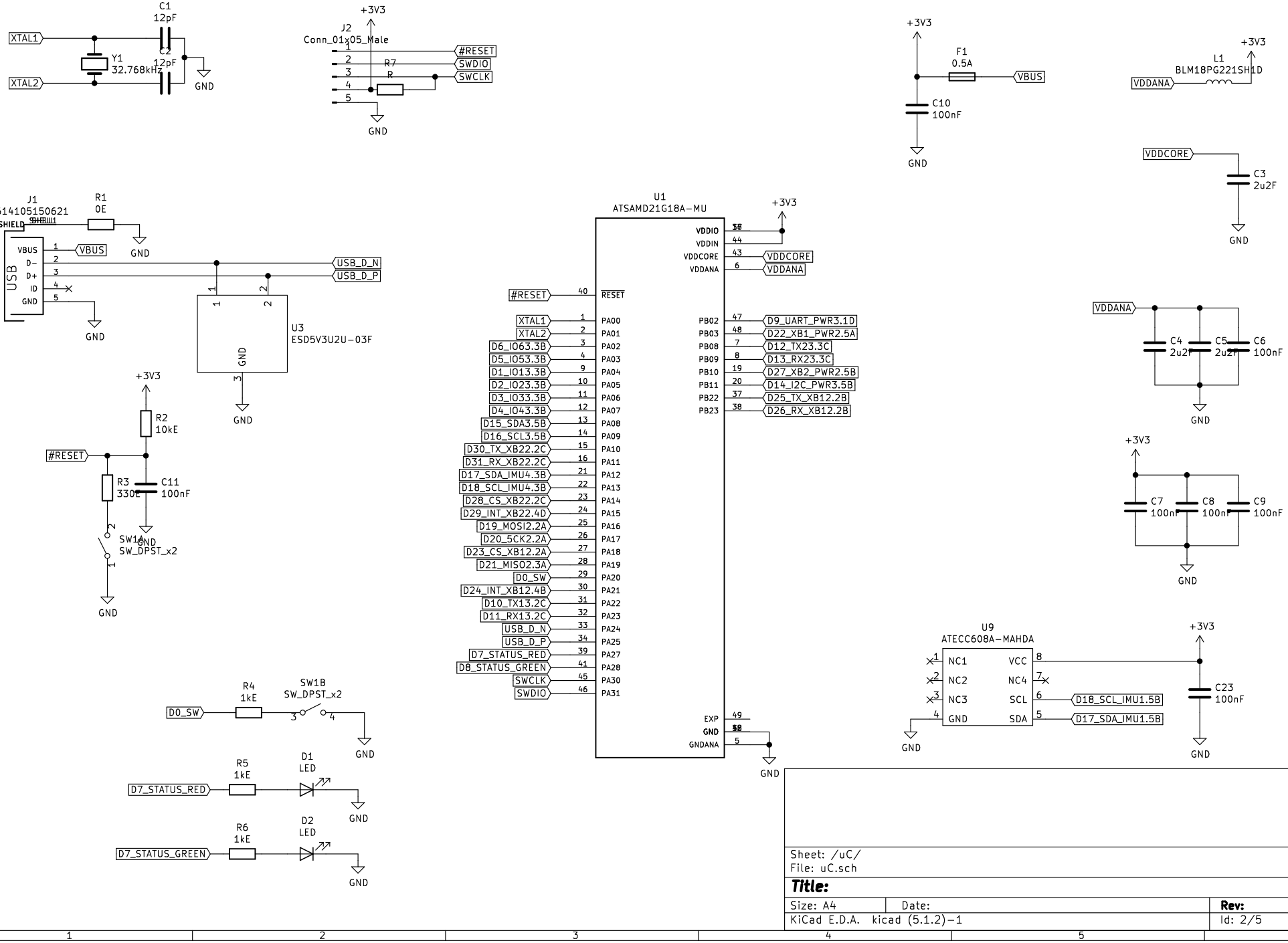

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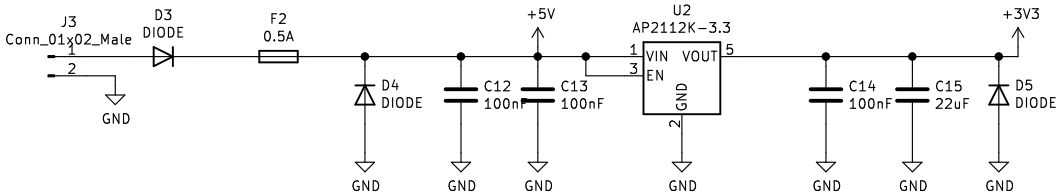

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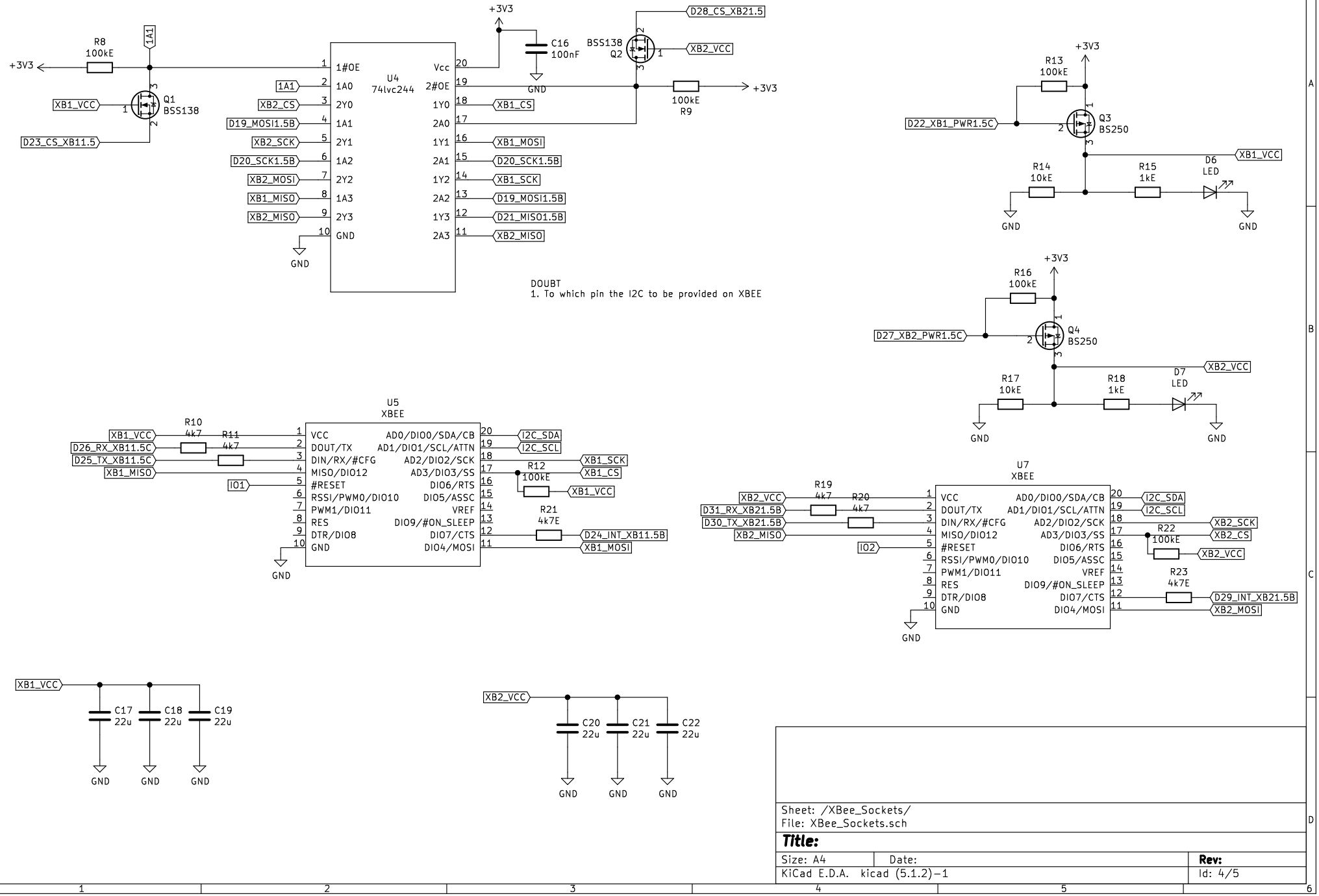

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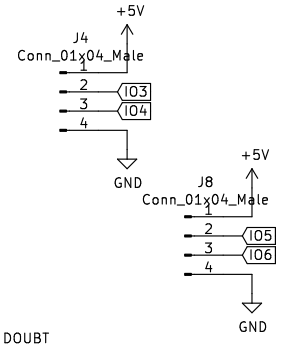
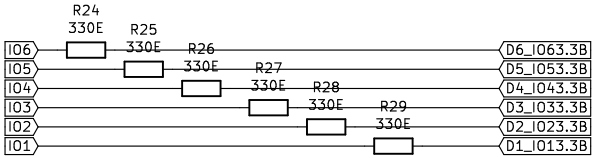
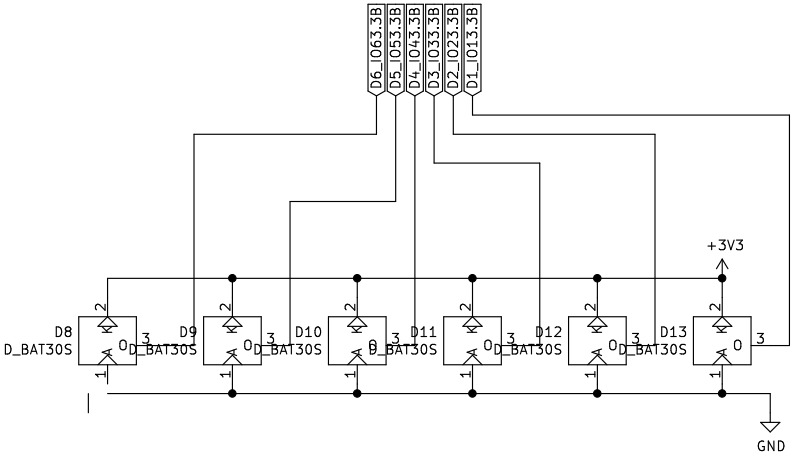
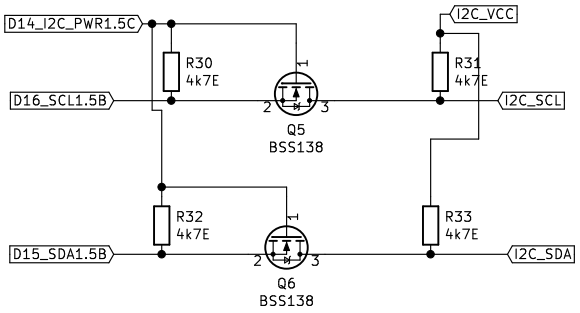
GPIO

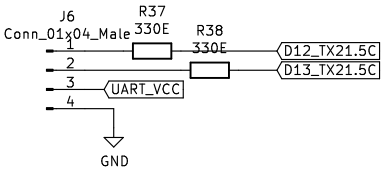




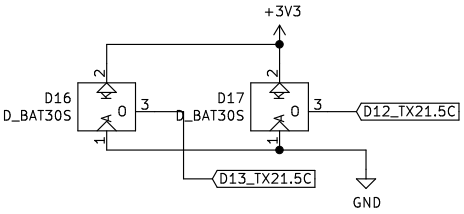


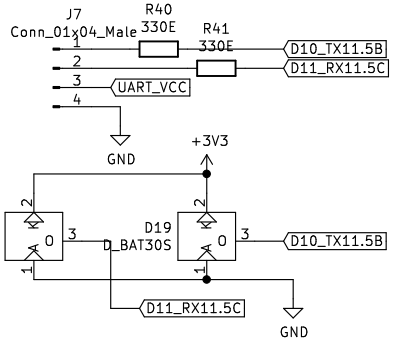
DOUBT
1. Should the IO1 and IO2 be exposed via headers (Since they are the reset pins for XBEE)

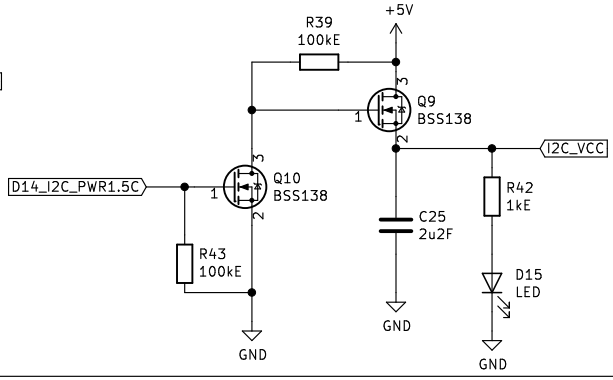


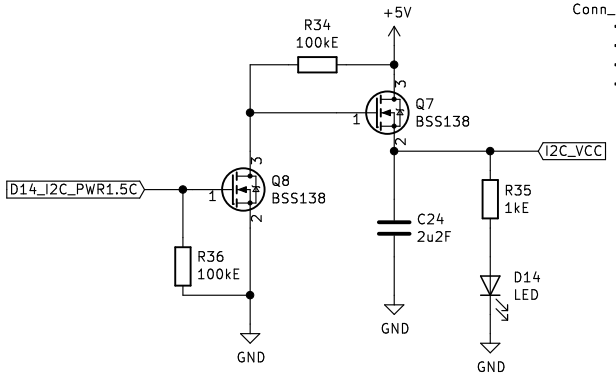


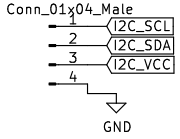
UART











NOTE:
1. If this protection circuit can be removed BOM can be reduced

Sheet: /Ports/ File: Ports.sch		
Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.2)-1		Id: 5/5