Coverage Report for FBFunctions

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Analysis Information

Model Information

Model version 1.67

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Harness information

Harness model(s) FBFunctions Harness NOT

Harness model owner FBFunctions

Simulation Optimization Options

Default parameter behavior tunable

Block reduction forced off

Conditional branch optimization on

Coverage Options

Analyzed model FBFunctions_Harness_NOT/NOT_DUPLEX

Logic block short circuiting off

MCDC mode masking

Tests

Test# Started execution Ended execution

Test 1 03-Feb-2019 15:27:36 03-Feb-2019 15:28:04

Summary

Model Hierarchy/Complexity		Test 1			
		Decision	Condition	MCDC	Execution
1. NOT_DUPLEX	18	94%	98%	90%	99%
2 <u>NOT</u>	8	100%	100%	100%	100%
3 <u>1_val_shift_register</u>	2	NA	NA	NA	100%
4 <u>Data 1</u>	1	NA	NA	NA	100%
5 <u>Unit Delay Enabled</u> Resettable	1	NA	NA	NA	100%
6 <u>Type 1</u>	1	NA	NA	NA	100%
7 <u>Unit Delay Enabled</u> Resettable	1	NA	NA	NA	100%
8 <u>TypeCheck1T2D</u>	1	100%	100%	100%	100%
9 <u>code_type_bit</u>		NA	NA	NA	100%
10 <u>cond_generate_error_code</u>	1	100%	NA NA	NA	100%
11 <u>code_no_error</u>		NA	NA	NA	100%
12 <u>isType2</u>		NA	100%	NA NA	100%
13 <u>isType</u>		NA	100%	NA NA	100%
14 <u>typeMask</u>		NA	NA	NA	100%
15 <u>isType1</u>		NA	100%	NA NA	100%
16 <u>typeMask</u>		NA	NA	NA	100%
17 <u>Unit Delay Enabled Resettable</u> <u>Synchronous8</u>	1	NA	NA	NA	100%
18 <u>Unit Delay Enabled Resettable</u>	1	NA	NA	NA	100%
19 <u>Unit Delay Enabled Resettable</u> <u>Synchronous9</u>	1	NA	NA	NA	100%
20 <u>Unit Delay Enabled Resettable</u>	1	NA	NA	NA	100%
21 <u>bool type</u>		NA	NA	NA	100%
22 <u>isType</u>		NA	100%	NA NA	100%
23 <u>typeMask</u>		NA	NA	NA	100%
24 <u>safebool type</u>		NA	NA	NA	100%
25 <u>safebool type1</u>		NA	NA	NA	100%
26 <u>NOT1</u>	8	100%	100%	100%	100%
27 <u>1_val_shift_register</u>	2	NA	NA	NA	100%
28 <u>Data 1</u>	1	NA	NA	NA	100%
29 <u>Unit Delay Enabled</u> Resettable	1	NA	NA	NA	100%
30 <u>Type 1</u>	1	NA	NA	NA	100%

31 <u>Unit Delay Enabled</u> Resettable	1	NA	NA	NA	100%
32 <u>TypeCheck1T2D</u>	1	100%	100%	100%	100%
33 <u>code_type_bit</u>		NA	NA	NA	100%
34 <u>cond_generate_error_code</u>	1	100%	NA	NA	100%
35 <u>code_no_error</u>		NA	NA	NA	100%
36 <u>isType2</u>		NA	100%	NA	100%
37 <u>isType</u>		NA	100%	NA	100%
38 <u>typeMask</u>		NA	NA	NA	100%
39 <u>isType1</u>		NA	100%	NA	100%
40 <u>typeMask</u>		NA	NA	NA	100%
41 <u>Unit Delay Enabled Resettable</u> <u>Synchronous8</u>	1	NA	NA	NA	100%
42 <u>Unit Delay Enabled Resettable</u>	1	NA	NA	NA	100%
43 <u>Unit Delay Enabled Resettable</u> <u>Synchronous9</u>	1	NA	NA	NA	100%
44 <u>Unit Delay Enabled Resettable</u>	1	NA	NA	NA	100%
45 <u>bool type</u>		NA	NA	NA	100%
46 <u>isType</u>		NA	100%	NA	100%
47 <u>typeMask</u>		NA	NA	NA	100%
48 <u>safebool type</u>		NA	NA	NA	100%
49 <u>safebool type1</u>		NA	NA	NA	100%
50 <u>State Comparator</u>	1	50%	100%	NA	80%
51 <u>Compare To Zero</u>		NA	100%	NA	100%
52 <u>combine_error_codes</u>		NA	NA	NA	100%

Details

1. SubSystem block "NOT_DUPLEX"

Child Systems: NOT, NOT1, State Comparator, combine_error_codes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	18
Condition	NA	98% (78/80) condition outcomes
Decision	NA	94% (17/18) decision outcomes
MCDC	NA	90% (18/20) conditions reversed the outcome
Execution	NA	99% (70/71) objective outcomes

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX

Uncovered Links:

Metric Coverage

Cyclomatic Complexity 0

Condition 50% (2/4) condition outcomes

MCDC 0% (0/2) conditions reversed the outcome

Execution 100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input port 1	88	0
input port 2	88	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	TT	(FT)
input port 2	TT	(T F)

Full Coverage

Model Object Metric

Relational Operator block "Relational Operator" Condition, Execution

Constant block "Num Ins" Execution

Constant block "Num Outs" Execution

Constant block "fb num constant" Execution

2. SubSystem block "NOT"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX

1 val shift register, TypeCheck1T2D, Unit Delay Enabled Resettable

Child Systems: Synchronous8, Unit Delay Enabled Resettable Synchronous9, bool type,

isType, safebool type, safebool type1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	8
Condition	NA	100% (32/32) condition outcomes
Decision	NA	100% (8/8) decision outcomes
MCDC	NA	100% (9/9) conditions reversed the outcome
Execution	NA	100% (30/30) objective outcomes

Logic block "Logical Operator3"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT

Metric Coverage

Cyclomatic Complexity 0

Condition 100% (4/4) condition outcomes

MCDC <u>see Logical Operator</u>

Execution 100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Logic block "Logical Operator"	Condition, MCDC, Execution
Logic block "Logical Operator1"	Condition, MCDC, Execution
Logic block "Logical Operator2"	Condition, MCDC, Execution
Logic block "Logical Operator4"	Condition, Execution
Logic block "Logical Operator6"	Condition, Execution
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution
Switch block "Switch2"	Decision, Execution
S-Function block "Bitwise Operator"	Execution
Constant block "Constant"	Execution
Constant block "Constant1"	Execution

3. SubSystem block "1 val shift register"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT

Child Systems: <u>Data 1</u>, <u>Type 1</u>

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0

Execution NA 100% (2/2) objective outcomes

4. SubSystem block "Data 1"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/1 val shift register

Child Systems: Unit Delay Enabled Resettable

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0

Execution NA 100% (1/1) objective outcomes

5. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/1 val shift register/Data

1

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 1

Execution NA 100% (1/1) objective outcomes

Full Coverage

Model Object Metric

Delay block "Enabled Resettable Delay" Execution

6. SubSystem block "Type 1"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/1 val shift register

Child Systems: Unit Delay Enabled Resettable

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0

Execution NA 100% (1/1) objective outcomes

7. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/1 val shift register/Type

1

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 1

Execution NA 100% (1/1) objective outcomes

Full Coverage

Model Object Metric

Delay block "Enabled Resettable Delay" Execution

8. SubSystem block "TypeCheck1T2D"

Justify or Exclude

Parent: <u>FBFunctions Harness NOT/NOT DUPLEX/NOT</u>
Child Systems: <u>code type bit, cond generate error code, isType2</u>

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Condition	NA	100% (10/10) condition outcomes
Decision	NA	100% (2/2) decision outcomes
MCDC	NA	100% (2/2) conditions reversed the outcome
Execution	NA	100% (9/9) objective outcomes

Model Object Metric

Logic block "Logical Operator" Condition, MCDC, Execution

9. SubSystem block "code type bit"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/TypeCheck1T2D

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0

Execution NA 100% (1/1) objective outcomes

Full Coverage

Model Object Metric

Constant block "type1" Execution

10. SubSystem block "cond generate error code"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/TypeCheck1T2D

Child Systems: <u>code no error</u>

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0

Decision NA 100% (2/2) decision outcomes Execution NA 100% (2/2) objective outcomes

Full Coverage

Model Object Metric

Switch block "Switch" Decision, Execution

11. SubSystem block "code_no_error"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/TypeCheck1T2D/cond generate error code

Metric Coverage (this object) C	Coverage (inc. descendants)
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Cyclomatic Complexity 0

Execution NA 100% (1/1) objective outcomes

Full Coverage

Model Object Metric

Constant block "type1" Execution

12. SubSystem block "isType2"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/TypeCheck1T2D

Child Systems: <u>isType</u>, <u>isType1</u>

Metric Coverage (this object) Coverage (inc. descendants)

Cyclomatic Complexity 0 0

Condition NA 100% (8/8) condition outcomes Execution NA 100% (5/5) objective outcomes

Logic block "Logical Operator"

Justify or Exclude

Parent: FBFunctions Harness NOT/NOT DUPLEX/NOT/TypeCheck1T2D/isType2

Metric Coverage

Cyclomatic Comple