

Report Generated by Test Manager

Title: SUB Test Report
Author: Athira Varma J
Date: 17-Jan-2019 23:26:30

Test Environment

Platform: PCWIN64
MATLAB: (R2018b)

Summary

Name

Outcome

Duration (Seconds)

[Results: 2019-Jan-17 22:54:10](#)

 [New Test Suite 1](#)

 [New Test Case 1](#)

 [Iteration1](#)

 [Iteration2](#)

 [Iteration3](#)

 [Iteration4](#)

 [Iteration5](#)

 [Iteration6](#)

 [Iteration7](#)

 [Iteration8](#)

 [Iteration9](#)

 [Iteration10](#)

 [Iteration11](#)

 [Iteration12](#)

 [Iteration13](#)

 [Iteration14](#)

 [New Test Case 2](#)

 [New Test Case 3](#)

 [New Test Case 4](#)

 [New Test Case 5](#)

 [New Test Case 6](#)

 [New Test Case 7](#)

13  10 

1092

13  10 

1092

6  8 

675



49



46



51



45



51



45



53



51



46



51



48



46



47



46



39



46



45




46



45




46

 New Test Case 8		45
 New Test Case 9		44
 New Test Case 10		47

Results: 2019-Jan-17 22:54:10

Result Type: Result Set
Parent: None
Start Time: 2019-Jan-17 22:54:10
End Time: 2019-Jan-17 23:12:22
Outcome: Total: 23, Passed: 13, Failed: 10

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Condition	MCDC	Execution
 FBFunctions/SUB DUPLEX	Normal	98	98%	96%	32%	99%

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New Test Suite 1

Test Result Information

Result Type: Test Suite Result
Parent: [Results: 2019-Jan-17 22:54:10](#)
Start Time: 2019-Jan-17 22:54:10
End Time: 2019-Jan-17 23:12:22
Outcome: Total: 23, Passed: 13, Failed: 10

Test Suite Information

Name: New Test Suite 1

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New Test Case 1

Test Result Information

Result Type: Test Case Result
Parent: [New Test Suite 1](#)
Start Time: 2019-Jan-17 22:54:10
End Time: 2019-Jan-17 23:05:25
Outcome: Failed
Cause of Failure: Test failed as iteration failed

Test Case Information

Name: New Test Case 1
Type: Baseline Test

Iteration1

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:54:10
End Time: 2019-Jan-17 22:54:59
Outcome: **Passed**

Test Case Information

Name: Iteration1
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 1

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:54:18
Simulation Stop Time: 2019-01-17 22:54:36
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 1			
TestInps	1	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration2

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:54:59

End Time: 2019-Jan-17 22:55:45
Outcome: Passed

Test Case Information

Name: Iteration2
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 2

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:55:09
Simulation Stop Time: 2019-01-17 22:55:22
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 2			
TestInps	2	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration3

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:55:45

End Time: 2019-Jan-17 22:56:36
Outcome: Passed

Test Case Information

Name: Iteration3
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 3

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:55:55
Simulation Stop Time: 2019-01-17 22:56:08
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 3			
TestInps	3	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration4

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:56:36

End Time: 2019-Jan-17 22:57:21
Outcome: Passed

Test Case Information

Name: Iteration4
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 4

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:56:45
Simulation Stop Time: 2019-01-17 22:56:57
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 4			
TestInps	4	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration5

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:57:21

End Time: 2019-Jan-17 22:58:12
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration5
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 5

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:57:32
Simulation Stop Time: 2019-01-17 22:57:45
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 5			
TestInps	5	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration6

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:58:12

End Time: 2019-Jan-17 22:58:57
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration6
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 6

Verify Result

Name	
✔	Test Sequence2/step_1:verify(numInputs == 4)
✔	Test Sequence2/step_1:verify(numOutputs == 2)
✔	Test Sequence2/step_1:verify(dataOut == 0)
✔	Test Sequence2/step_1:verify(state_error == 0)
✔	Test Sequence2/step_1:verify(executed == 1)
✔	Test Sequence2/step_42:verify(numInputs == 4)
✔	Test Sequence2/step_42:verify(numOutputs == 2)
✔	Test Sequence2/step_42:verify(dataOut == 0)
✔	Test Sequence2/step_42:verify(state_error == 0)
✔	Test Sequence2/step_42:verify(executed == 1)
✔	Test Sequence2/step_2:verify(numInputs == 4)
✔	Test Sequence2/step_2:verify(numOutputs == 2)
✔	Test Sequence2/step_2:verify(dataOut == 0)
✔	Test Sequence2/step_2:verify(state_error == 0)
✔	Test Sequence2/step_2:verify(executed == 1)
✔	Test Sequence2/step_3:verify(numInputs == 4)
✔	Test Sequence2/step_3:verify(numOutputs == 2)
✔	Test Sequence2/step_3:verify(dataOut == 0)
✔	Test Sequence2/step_3:verify(state_error == 0)
✔	Test Sequence2/step_3:verify(executed == 1)
✔	Test Sequence2/step_53:verify(numInputs == 4)
✔	Test Sequence2/step_53:verify(numOutputs == 2)
✔	Test Sequence2/step_53:verify(dataOut == 0)
✔	Test Sequence2/step_53:verify(state_error == 0)
✔	Test Sequence2/step_53:verify(executed == 1)
✔	Test Sequence2/step_54:verify(numInputs == 4)
✔	Test Sequence2/step_54:verify(numOutputs == 2)
✔	Test Sequence2/step_54:verify(dataOut == 0)
✔	Test Sequence2/step_54:verify(state_error == 0)
✔	Test Sequence2/step_54:verify(executed == 1)
✔	Test Sequence2/step_55:verify(numInputs == 4)
✔	Test Sequence2/step_55:verify(numOutputs == 2)
✔	Test Sequence2/step_55:verify(dataOut == 0)
✔	Test Sequence2/step_55:verify(state_error == 0)
✔	Test Sequence2/step_55:verify(executed == 1)
✔	Test Sequence2/step_56:verify(numInputs == 4)
✔	Test Sequence2/step_56:verify(numOutputs == 2)
✔	Test Sequence2/step_56:verify(dataOut == 0)
✔	Test Sequence2/step_56:verify(state_error == 0)
✔	Test Sequence2/step_56:verify(executed == 1)
✔	Test Sequence2/step_4:verify(numInputs == 4)
✔	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:58:22
Simulation Stop Time: 2019-01-17 22:58:34
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 6			
TestInps	6	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration7

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:58:57

End Time: 2019-Jan-17 22:59:50
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration7
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 7

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 22:59:08
Simulation Stop Time: 2019-01-17 22:59:23
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 7			
TestInps	7	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration8

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 22:59:50

End Time: 2019-Jan-17 23:00:41
Outcome: Passed

Test Case Information

Name: Iteration8
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 8

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:00:03
Simulation Stop Time: 2019-01-17 23:00:18
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 8			
TestInps	8	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration9

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:00:41

End Time: 2019-Jan-17 23:01:27
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration9
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 9

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:00:52
Simulation Stop Time: 2019-01-17 23:01:03
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 9			
TestInps	9	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration10

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:01:27

End Time: 2019-Jan-17 23:02:18
Outcome: Passed

Test Case Information

Name: Iteration10
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 10

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:01:38
Simulation Stop Time: 2019-01-17 23:01:52
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 10			
TestInps	10	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration11

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:02:18

End Time: 2019-Jan-17 23:03:06
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration11
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 11

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:02:30
Simulation Stop Time: 2019-01-17 23:02:42
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 11			
TestInps	11	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration12

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:03:06

End Time: 2019-Jan-17 23:03:52
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration12
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 12

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:03:16
Simulation Stop Time: 2019-01-17 23:03:28
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 12			
TestInps	12	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration13

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:03:52

End Time: 2019-Jan-17 23:04:39
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration13
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 13

Verify Result

Name	
✔	Test Sequence2/step_1:verify(numInputs == 4)
✔	Test Sequence2/step_1:verify(numOutputs == 2)
✔	Test Sequence2/step_1:verify(dataOut == 0)
✔	Test Sequence2/step_1:verify(state_error == 0)
✔	Test Sequence2/step_1:verify(executed == 1)
✔	Test Sequence2/step_42:verify(numInputs == 4)
✔	Test Sequence2/step_42:verify(numOutputs == 2)
✔	Test Sequence2/step_42:verify(dataOut == 0)
✔	Test Sequence2/step_42:verify(state_error == 0)
✔	Test Sequence2/step_42:verify(executed == 1)
✔	Test Sequence2/step_2:verify(numInputs == 4)
✔	Test Sequence2/step_2:verify(numOutputs == 2)
✔	Test Sequence2/step_2:verify(dataOut == 0)
✔	Test Sequence2/step_2:verify(state_error == 0)
✔	Test Sequence2/step_2:verify(executed == 1)
✔	Test Sequence2/step_3:verify(numInputs == 4)
✔	Test Sequence2/step_3:verify(numOutputs == 2)
✔	Test Sequence2/step_3:verify(dataOut == 0)
✔	Test Sequence2/step_3:verify(state_error == 0)
✔	Test Sequence2/step_3:verify(executed == 1)
✔	Test Sequence2/step_53:verify(numInputs == 4)
✔	Test Sequence2/step_53:verify(numOutputs == 2)
✔	Test Sequence2/step_53:verify(dataOut == 0)
✔	Test Sequence2/step_53:verify(state_error == 0)
✔	Test Sequence2/step_53:verify(executed == 1)
✔	Test Sequence2/step_54:verify(numInputs == 4)
✔	Test Sequence2/step_54:verify(numOutputs == 2)
✔	Test Sequence2/step_54:verify(dataOut == 0)
✔	Test Sequence2/step_54:verify(state_error == 0)
✔	Test Sequence2/step_54:verify(executed == 1)
✔	Test Sequence2/step_55:verify(numInputs == 4)
✔	Test Sequence2/step_55:verify(numOutputs == 2)
✔	Test Sequence2/step_55:verify(dataOut == 0)
✔	Test Sequence2/step_55:verify(state_error == 0)
✔	Test Sequence2/step_55:verify(executed == 1)
✔	Test Sequence2/step_56:verify(numInputs == 4)
✔	Test Sequence2/step_56:verify(numOutputs == 2)
✔	Test Sequence2/step_56:verify(dataOut == 0)
✔	Test Sequence2/step_56:verify(state_error == 0)
✔	Test Sequence2/step_56:verify(executed == 1)
✔	Test Sequence2/step_4:verify(numInputs == 4)
✔	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:04:04
Simulation Stop Time: 2019-01-17 23:04:16
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 13			
TestInps	13	base workspace	FBFunctions_Harness4/Test Sequence2

Iteration14

Test Result Information

Result Type: Test Iteration Result
Parent: [New Test Case 1](#)
Start Time: 2019-Jan-17 23:04:39

End Time: 2019-Jan-17 23:05:25
Outcome: **Failed**
Cause of Failure: **Failed criteria: Verification**

Test Case Information

Name: Iteration14
Type: Baseline Test

Iteration Settings

Test Overrides

Parameter Name	Value
ParameterSet	Parameter Set 14

Verify Result

Name	
✓	Test Sequence2/step_1:verify(numInputs == 4)
✓	Test Sequence2/step_1:verify(numOutputs == 2)
✓	Test Sequence2/step_1:verify(dataOut == 0)
✓	Test Sequence2/step_1:verify(state_error == 0)
✓	Test Sequence2/step_1:verify(executed == 1)
✓	Test Sequence2/step_42:verify(numInputs == 4)
✓	Test Sequence2/step_42:verify(numOutputs == 2)
✓	Test Sequence2/step_42:verify(dataOut == 0)
✓	Test Sequence2/step_42:verify(state_error == 0)
✓	Test Sequence2/step_42:verify(executed == 1)
✓	Test Sequence2/step_2:verify(numInputs == 4)
✓	Test Sequence2/step_2:verify(numOutputs == 2)
✓	Test Sequence2/step_2:verify(dataOut == 0)
✓	Test Sequence2/step_2:verify(state_error == 0)
✓	Test Sequence2/step_2:verify(executed == 1)
✓	Test Sequence2/step_3:verify(numInputs == 4)
✓	Test Sequence2/step_3:verify(numOutputs == 2)
✓	Test Sequence2/step_3:verify(dataOut == 0)
✓	Test Sequence2/step_3:verify(state_error == 0)
✓	Test Sequence2/step_3:verify(executed == 1)
✓	Test Sequence2/step_53:verify(numInputs == 4)
✓	Test Sequence2/step_53:verify(numOutputs == 2)
✓	Test Sequence2/step_53:verify(dataOut == 0)
✓	Test Sequence2/step_53:verify(state_error == 0)
✓	Test Sequence2/step_53:verify(executed == 1)
✓	Test Sequence2/step_54:verify(numInputs == 4)
✓	Test Sequence2/step_54:verify(numOutputs == 2)
✓	Test Sequence2/step_54:verify(dataOut == 0)
✓	Test Sequence2/step_54:verify(state_error == 0)
✓	Test Sequence2/step_54:verify(executed == 1)
✓	Test Sequence2/step_55:verify(numInputs == 4)
✓	Test Sequence2/step_55:verify(numOutputs == 2)
✓	Test Sequence2/step_55:verify(dataOut == 0)
✓	Test Sequence2/step_55:verify(state_error == 0)
✓	Test Sequence2/step_55:verify(executed == 1)
✓	Test Sequence2/step_56:verify(numInputs == 4)
✓	Test Sequence2/step_56:verify(numOutputs == 2)
✓	Test Sequence2/step_56:verify(dataOut == 0)
✓	Test Sequence2/step_56:verify(state_error == 0)
✓	Test Sequence2/step_56:verify(executed == 1)
✓	Test Sequence2/step_4:verify(numInputs == 4)
✓	Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model: FBFunctions
Harness: FBFunctions_Harness4
Harness Owner: FBFunctions/SUB_DUPLEX
Simulation Mode: normal
Configuration Set: Configuration1
Start Time: 0
Stop Time: 10
Checksum: 864294538 2310530824 2202527501 1346950290
Simulink Version: 9.2
Model Version: 1.181
Model Author: jayakumarav
Date: Thu Jan 17 14:59:38 2019
User ID: jayakumarav
Model Path: C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx

Machine Name: EGR-ELKS-05
Solver Name: VariableStepDiscrete
Solver Type: Variable-Step
Max Step Size: 0.20000000000000001
Simulation Start Time: 2019-01-17 23:04:49
Simulation Stop Time: 2019-01-17 23:05:01
Platform: PCWIN64

Parameter Overrides

Workspace Variable	Value	Source	Model Element
Parameter Set 14			
TestInps	14	base workspace	FBFunctions_Harness4/Test Sequence2

New Test Case 2

Test Result Information

Result Type: Test Case Result
Parent: [New Test Suite 1](#)
Start Time: 2019-Jan-17 23:05:38

End Time: 2019-Jan-17 23:06:17
Outcome: Passed

Test Case Information

Name: New Test Case 2
Type: Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:05:43
Simulation Stop Time:	2019-01-17 23:05:54
Platform:	PCWIN64

New Test Case 3

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:06:17
End Time:	2019-Jan-17 23:07:03
Outcome:	Passed

Test Case Information

Name:	New Test Case 3
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:06:28
Simulation Stop Time:	2019-01-17 23:06:40
Platform:	PCWIN64

New Test Case 4

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:07:03
End Time:	2019-Jan-17 23:07:48
Outcome:	Passed

Test Case Information

Name:	New Test Case 4
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:07:13
Simulation Stop Time:	2019-01-17 23:07:25
Platform:	PCWIN64

New Test Case 5

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:07:48
End Time:	2019-Jan-17 23:08:34
Outcome:	Passed

Test Case Information

Name:	New Test Case 5
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:07:59
Simulation Stop Time:	2019-01-17 23:08:10
Platform:	PCWIN64

New Test Case 6

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:08:34
End Time:	2019-Jan-17 23:09:19
Outcome:	Passed

Test Case Information

Name:	New Test Case 6
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:08:44
Simulation Stop Time:	2019-01-17 23:08:56
Platform:	PCWIN64

New Test Case 7

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:09:19
End Time:	2019-Jan-17 23:10:05
Outcome:	Failed
Cause of Failure:	Failed criteria: Verification

Test Case Information

Name:	New Test Case 7
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Type:

Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:09:29
Simulation Stop Time:	2019-01-17 23:09:42
Platform:	PCWIN64

New Test Case 8

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:10:05
End Time:	2019-Jan-17 23:10:50
Outcome:	Failed
Cause of Failure:	Failed criteria: Verification

Test Case Information

Name:	New Test Case 8
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Type:

Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:10:16
Simulation Stop Time:	2019-01-17 23:10:27
Platform:	PCWIN64

New Test Case 9

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:10:51
End Time:	2019-Jan-17 23:11:35
Outcome:	Passed

Test Case Information

Name:	New Test Case 9
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:11:01
Simulation Stop Time:	2019-01-17 23:11:12
Platform:	PCWIN64

New Test Case 10

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-17 23:11:35
End Time:	2019-Jan-17 23:12:22
Outcome:	Passed

Test Case Information

Name:	New Test Case 10
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 4)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 4)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 4)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 4)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_53:verify(numInputs == 4)
 Test Sequence2/step_53:verify(numOutputs == 2)
 Test Sequence2/step_53:verify(dataOut == 0)
 Test Sequence2/step_53:verify(state_error == 0)
 Test Sequence2/step_53:verify(executed == 1)
 Test Sequence2/step_54:verify(numInputs == 4)
 Test Sequence2/step_54:verify(numOutputs == 2)
 Test Sequence2/step_54:verify(dataOut == 0)
 Test Sequence2/step_54:verify(state_error == 0)
 Test Sequence2/step_54:verify(executed == 1)
 Test Sequence2/step_55:verify(numInputs == 4)
 Test Sequence2/step_55:verify(numOutputs == 2)
 Test Sequence2/step_55:verify(dataOut == 0)
 Test Sequence2/step_55:verify(state_error == 0)
 Test Sequence2/step_55:verify(executed == 1)
 Test Sequence2/step_56:verify(numInputs == 4)
 Test Sequence2/step_56:verify(numOutputs == 2)
 Test Sequence2/step_56:verify(dataOut == 0)
 Test Sequence2/step_56:verify(state_error == 0)
 Test Sequence2/step_56:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 4)
Test Sequence2/step_4:verify(numOutputs == 2)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness4
Harness Owner:	FBFunctions/SUB_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	864294538 2310530824 2202527501 1346950290
Simulink Version:	9.2
Model Version:	1.181
Model Author:	jayakumarav
Date:	Thu Jan 17 14:59:38 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-17 23:11:46
Simulation Stop Time:	2019-01-17 23:11:58
Platform:	PCWIN64
