


Report Generated by Test Manager

Title: Test
Author: Athira Varma J
Date: 07-Jan-2019 14:02:06

Test Environment

Platform: PCWIN64
MATLAB: (R2018b)

Summary

Name	Outcome	Duration (Seconds)
 New Test Suite 1	5  1 	85
 New Test Case 1		13
 New Test Case 2		15
 New Test Case 3		15
 New Test Case 4		15
 New Test Case 5		13
 New Test Case 6		14

New Test Suite 1


Test Result Information

Result Type: Test Suite Result
Parent: None
Start Time: 2019-Jan-07 12:59:29
End Time: 2019-Jan-07 13:00:54
Outcome: Total: 6, Passed: 5, Failed: 1

Test Suite Information

Name: New Test Suite 1

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Condition	MCDC	Execution
 FBFunctions/BSR_DUPLEX	Normal	28	71%	96%	88%	98%

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New Test Case 1

Test Result Information

Result Type: Test Case Result
Parent: [New Test Suite 1](#)
Start Time: 2019-Jan-07 12:59:29
End Time: 2019-Jan-07 12:59:42
Outcome: Passed

Test Case Information

Name: New Test Case 1
Type: Baseline Test

Verify Result

Name	
✔	Test Sequence2/step_1:verify(numInputs == 2)
✔	Test Sequence2/step_1:verify(numOutputs == 2)
✔	Test Sequence2/step_1:verify(fb_error_code == 256)
✔	Test Sequence2/step_1:verify(dataOut == 0)
✔	Test Sequence2/step_1:verify(state_error == 0)
✔	Test Sequence2/step_1:verify(executed == 1)
✔	Test Sequence2/step_42:verify(numInputs == 2)
✔	Test Sequence2/step_42:verify(numOutputs == 2)
✔	Test Sequence2/step_42:verify(fb_error_code == 256)
✔	Test Sequence2/step_42:verify(dataOut == 0)
✔	Test Sequence2/step_42:verify(state_error == 0)
✔	Test Sequence2/step_42:verify(executed == 1)
✔	Test Sequence2/step_2:verify(numInputs == 2)
✔	Test Sequence2/step_2:verify(numOutputs == 2)
✔	Test Sequence2/step_2:verify(fb_error_code == 256)
✔	Test Sequence2/step_2:verify(dataOut == 0)
✔	Test Sequence2/step_2:verify(state_error == 0)
✔	Test Sequence2/step_2:verify(executed == 1)
✔	Test Sequence2/step_3:verify(numInputs == 2)
✔	Test Sequence2/step_3:verify(numOutputs == 2)
✔	Test Sequence2/step_3:verify(fb_error_code == 256)
✔	Test Sequence2/step_3:verify(dataOut == 0)
✔	Test Sequence2/step_3:verify(state_error == 0)
✔	Test Sequence2/step_3:verify(executed == 1)
✔	Test Sequence2/step_4:verify(numInputs == 2)
✔	Test Sequence2/step_4:verify(numOutputs == 2)
✔	Test Sequence2/step_4:verify(fb_error_code == 0)
✔	Test Sequence2/step_4:verify(dataOut == 0)
✔	Test Sequence2/step_4:verify(state_error == 0)
✔	Test Sequence2/step_4:verify(executed == 1)
✔	Test Sequence2/step_5:verify(numInputs == 2)
✔	Test Sequence2/step_5:verify(numOutputs == 2)
✔	Test Sequence2/step_5:verify(fb_error_code == 0)
✔	Test Sequence2/step_5:verify(dataOut == 0)
✔	Test Sequence2/step_5:verify(state_error == 0)
✔	Test Sequence2/step_5:verify(executed == 1)
✔	Test Sequence2/step_6:verify(numInputs == 2)
✔	Test Sequence2/step_6:verify(numOutputs == 2)
✔	Test Sequence2/step_6:verify(fb_error_code == 0)
✔	Test Sequence2/step_6:verify(dataOut == 67108864)
✔	Test Sequence2/step_6:verify(state_error == 0)
✔	Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 12:59:30
Simulation Stop Time:	2019-01-07 12:59:35
Platform:	PCWIN64

New Test Case 2

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-07 12:59:42
End Time:	2019-Jan-07 12:59:57
Outcome:	Passed

Test Case Information

Name:	New Test Case 2
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 2)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(fb_error_code == 256)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 2)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(fb_error_code == 256)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 2)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(fb_error_code == 256)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 2)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(fb_error_code == 256)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 2)
 Test Sequence2/step_4:verify(numOutputs == 2)
 Test Sequence2/step_4:verify(fb_error_code == 0)
 Test Sequence2/step_4:verify(dataOut == 0)
 Test Sequence2/step_4:verify(state_error == 0)
 Test Sequence2/step_4:verify(executed == 1)
 Test Sequence2/step_5:verify(numInputs == 2)
 Test Sequence2/step_5:verify(numOutputs == 2)
 Test Sequence2/step_5:verify(fb_error_code == 0)
 Test Sequence2/step_5:verify(dataOut == 0)
 Test Sequence2/step_5:verify(state_error == 0)
 Test Sequence2/step_5:verify(executed == 1)
 Test Sequence2/step_6:verify(numInputs == 2)
 Test Sequence2/step_6:verify(numOutputs == 2)
 Test Sequence2/step_6:verify(fb_error_code == 0)
 Test Sequence2/step_6:verify(dataOut == 67108864)
 Test Sequence2/step_6:verify(state_error == 0)
Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 12:59:43
Simulation Stop Time:	2019-01-07 12:59:48
Platform:	PCWIN64

New Test Case 3

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-07 12:59:57
End Time:	2019-Jan-07 13:00:12
Outcome:	Passed

Test Case Information

Name:	New Test Case 3
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 2)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(fb_error_code == 256)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 2)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(fb_error_code == 256)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 2)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(fb_error_code == 256)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 2)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(fb_error_code == 256)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 2)
 Test Sequence2/step_4:verify(numOutputs == 2)
 Test Sequence2/step_4:verify(fb_error_code == 0)
 Test Sequence2/step_4:verify(dataOut == 0)
 Test Sequence2/step_4:verify(state_error == 0)
 Test Sequence2/step_4:verify(executed == 1)
 Test Sequence2/step_5:verify(numInputs == 2)
 Test Sequence2/step_5:verify(numOutputs == 2)
 Test Sequence2/step_5:verify(fb_error_code == 0)
 Test Sequence2/step_5:verify(dataOut == 0)
 Test Sequence2/step_5:verify(state_error == 0)
 Test Sequence2/step_5:verify(executed == 1)
 Test Sequence2/step_6:verify(numInputs == 2)
 Test Sequence2/step_6:verify(numOutputs == 2)
 Test Sequence2/step_6:verify(fb_error_code == 0)
 Test Sequence2/step_6:verify(dataOut == 67108864)
 Test Sequence2/step_6:verify(state_error == 0)
Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 12:59:58
Simulation Stop Time:	2019-01-07 13:00:04
Platform:	PCWIN64

New Test Case 4

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-07 13:00:12
End Time:	2019-Jan-07 13:00:27
Outcome:	Failed
Cause of Failure:	Failed criteria: Verification

Test Case Information

Name:	New Test Case 4
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Type:

Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 2)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(fb_error_code == 256)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 2)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(fb_error_code == 256)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 2)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(fb_error_code == 256)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 2)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(fb_error_code == 256)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 2)
 Test Sequence2/step_4:verify(numOutputs == 2)
 Test Sequence2/step_4:verify(fb_error_code == 0)
 Test Sequence2/step_4:verify(dataOut == 0)
 Test Sequence2/step_4:verify(state_error == 0)
 Test Sequence2/step_4:verify(executed == 1)
 Test Sequence2/step_5:verify(numInputs == 2)
 Test Sequence2/step_5:verify(numOutputs == 2)
 Test Sequence2/step_5:verify(fb_error_code == 0)
 Test Sequence2/step_5:verify(dataOut == 0)
 Test Sequence2/step_5:verify(state_error == 0)
 Test Sequence2/step_5:verify(executed == 1)
 Test Sequence2/step_6:verify(numInputs == 2)
 Test Sequence2/step_6:verify(numOutputs == 2)
 Test Sequence2/step_6:verify(fb_error_code == 0)
 Test Sequence2/step_6:verify(dataOut == 67108864)
 Test Sequence2/step_6:verify(state_error == 0)
Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 13:00:13
Simulation Stop Time:	2019-01-07 13:00:19
Platform:	PCWIN64

New Test Case 5


Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-07 13:00:27
End Time:	2019-Jan-07 13:00:40
Outcome:	Passed

Test Case Information

Name:	New Test Case 5
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 2)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(fb_error_code == 256)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 2)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(fb_error_code == 256)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 2)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(fb_error_code == 256)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 2)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(fb_error_code == 256)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 2)
 Test Sequence2/step_4:verify(numOutputs == 2)
 Test Sequence2/step_4:verify(fb_error_code == 0)
 Test Sequence2/step_4:verify(dataOut == 0)
 Test Sequence2/step_4:verify(state_error == 0)
 Test Sequence2/step_4:verify(executed == 1)
 Test Sequence2/step_5:verify(numInputs == 2)
 Test Sequence2/step_5:verify(numOutputs == 2)
 Test Sequence2/step_5:verify(fb_error_code == 0)
 Test Sequence2/step_5:verify(dataOut == 0)
 Test Sequence2/step_5:verify(state_error == 0)
 Test Sequence2/step_5:verify(executed == 1)
 Test Sequence2/step_6:verify(numInputs == 2)
 Test Sequence2/step_6:verify(numOutputs == 2)
 Test Sequence2/step_6:verify(fb_error_code == 0)
 Test Sequence2/step_6:verify(dataOut == 67108864)
 Test Sequence2/step_6:verify(state_error == 0)
Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\Sympl eVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 13:00:28
Simulation Stop Time:	2019-01-07 13:00:33
Platform:	PCWIN64

New Test Case 6

Test Result Information

Result Type:	Test Case Result
Parent:	New Test Suite 1
Start Time:	2019-Jan-07 13:00:40
End Time:	2019-Jan-07 13:00:54
Outcome:	Passed

Test Case Information

Name:	New Test Case 6
Type:	Baseline Test

Verify Result

Name
 Test Sequence2/step_1:verify(numInputs == 2)
 Test Sequence2/step_1:verify(numOutputs == 2)
 Test Sequence2/step_1:verify(fb_error_code == 256)
 Test Sequence2/step_1:verify(dataOut == 0)
 Test Sequence2/step_1:verify(state_error == 0)
 Test Sequence2/step_1:verify(executed == 1)
 Test Sequence2/step_42:verify(numInputs == 2)
 Test Sequence2/step_42:verify(numOutputs == 2)
 Test Sequence2/step_42:verify(fb_error_code == 256)
 Test Sequence2/step_42:verify(dataOut == 0)
 Test Sequence2/step_42:verify(state_error == 0)
 Test Sequence2/step_42:verify(executed == 1)
 Test Sequence2/step_2:verify(numInputs == 2)
 Test Sequence2/step_2:verify(numOutputs == 2)
 Test Sequence2/step_2:verify(fb_error_code == 256)
 Test Sequence2/step_2:verify(dataOut == 0)
 Test Sequence2/step_2:verify(state_error == 0)
 Test Sequence2/step_2:verify(executed == 1)
 Test Sequence2/step_3:verify(numInputs == 2)
 Test Sequence2/step_3:verify(numOutputs == 2)
 Test Sequence2/step_3:verify(fb_error_code == 256)
 Test Sequence2/step_3:verify(dataOut == 0)
 Test Sequence2/step_3:verify(state_error == 0)
 Test Sequence2/step_3:verify(executed == 1)
 Test Sequence2/step_4:verify(numInputs == 2)
 Test Sequence2/step_4:verify(numOutputs == 2)
 Test Sequence2/step_4:verify(fb_error_code == 0)
 Test Sequence2/step_4:verify(dataOut == 0)
 Test Sequence2/step_4:verify(state_error == 0)
 Test Sequence2/step_4:verify(executed == 1)
 Test Sequence2/step_5:verify(numInputs == 2)
 Test Sequence2/step_5:verify(numOutputs == 2)
 Test Sequence2/step_5:verify(fb_error_code == 0)
 Test Sequence2/step_5:verify(dataOut == 0)
 Test Sequence2/step_5:verify(state_error == 0)
 Test Sequence2/step_5:verify(executed == 1)
 Test Sequence2/step_6:verify(numInputs == 2)
 Test Sequence2/step_6:verify(numOutputs == 2)
 Test Sequence2/step_6:verify(fb_error_code == 0)
 Test Sequence2/step_6:verify(dataOut == 67108864)
 Test Sequence2/step_6:verify(state_error == 0)
Test Sequence2/step_6:verify(executed == 1)

Simulation

System Under Test Information

Model:	FBFunctions
Harness:	FBFunctions_Harness3
Harness Owner:	FBFunctions/BSR_DUPLEX
Simulation Mode:	normal
Configuration Set:	Configuration1
Start Time:	0
Stop Time:	10
Checksum:	1861970796 2481637763 3301617682 905941352
Simulink Version:	9.2
Model Version:	1.89
Model Author:	jayakumarav
Date:	Mon Jan 07 12:57:34 2019
User ID:	jayakumarav
Model Path:	C:\Users\jayakumarav\Documents\GitHub\SympleVerification\FBFunctions\FBFunctions.slx
Machine Name:	EGR-ELKS-05
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.20000000000000001
Simulation Start Time:	2019-01-07 13:00:41
Simulation Stop Time:	2019-01-07 13:00:46
Platform:	PCWIN64
