

# Coverage Report for FBFunctions

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## Analysis Information

### Model Information

Model version	1.70
Author	hiterd
Last saved	Mon Feb 04 14:13:52 2019

### Harness information

Harness model(s)	FBFunctions_Harness_BNOR
Harness model owner	FBFunctions

### Simulation Optimization Options

Default parameter behavior	tunable
Block reduction	forced off
Conditional branch optimization	on

### Coverage Options

Analyzed model	FBFunctions_Harness_BNOR/BNOR_DUPLEX
Logic block short circuiting	off
MCDC mode	masking




















































## Tests

Test#	Started execution	Ended execution
Test 1	04-Feb-2019 14:17:41	04-Feb-2019 14:18:59

# Summary

## Model Hierarchy/Complexity

## Test 1

		Decision	Condition	MCDC	Execution
1. <a href="#">BNOR_DUPLEX</a>	20	93% 	98% 	64% 	99% 
2. ... <a href="#">BNOR</a>	9	100% 	100% 	70% 	100% 
3. .... <a href="#">2_val_shift_register</a>	4	NA	NA	NA	100% 
4. .... <a href="#">Data 1</a>	1	NA	NA	NA	100% 
5. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
6. .... <a href="#">Data 2</a>	1	NA	NA	NA	100% 
7. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
8. .... <a href="#">Type 1</a>	1	NA	NA	NA	100% 
9. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
10. .... <a href="#">Type 2</a>	1	NA	NA	NA	100% 
11. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
12. .... <a href="#">TypeCheck2T1D</a>	1	100% 	100% 	0% 	100% 
13. .... <a href="#">code_type_bit</a>		NA	NA	NA	100% 
14. .... <a href="#">cond_generate_error_code</a>	1	100% 	NA	NA	100% 
15. .... <a href="#">code_no_error</a>		NA	NA	NA	100% 
16. .... <a href="#">isType</a>		NA	100% 	NA	100% 
17. .... <a href="#">typeMask</a>		NA	NA	NA	100% 
18. .... <a href="#">isType1</a>		NA	100% 	NA	100% 
19. .... <a href="#">typeMask</a>		NA	NA	NA	100% 
20. .... <a href="#">isTypeMatch2</a>		NA	100% 	NA	100% 
21. .... <a href="#">typeMask</a>		NA	NA	NA	100% 
22. .... <a href="#">typeMask1</a>		NA	NA	NA	100% 
23. .... <a href="#">Unit Delay Enabled Resettable Synchronous8</a>	1	NA	NA	NA	100% 
24. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
25. .... <a href="#">Unit Delay Enabled Resettable Synchronous9</a>	1	NA	NA	NA	100% 
26. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
27. .... <a href="#">int type</a>		NA	NA	NA	100% 
28. ... <a href="#">BNOR1</a>	9	100% 	100% 	70% 	100% 
29. .... <a href="#">2_val_shift_register</a>	4	NA	NA	NA	100% 
30. .... <a href="#">Data 1</a>	1	NA	NA	NA	100% 
31. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
32. .... <a href="#">Data 2</a>	1	NA	NA	NA	100% 
33. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 
34. .... <a href="#">Type 1</a>	1	NA	NA	NA	100% 
35. .... <a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100% 

36.....	<a href="#">Type 2</a>	1	NA	NA	NA	100%	<div></div>
37.....	<a href="#">Unit Delay Enabled Resettable</a>	1	NA	NA	NA	100%	<div></div>
38.....	<a href="#">TypeCheck2T1D</a>	1	100% <div></div>	100% <div></div>	0% <div></div>	100%	<div></div>
39.....	<a href="#">code_type_bit</a>		NA	NA	NA	100%	<div></div>
40.....	<a href="#">cond_generate_error_code</a>	1	100% <div></div>	NA	NA	100%	<div></div>
41.....	<a href="#">code_no_error</a>		NA	NA	NA	100%	<div></div>
42.....	<a href="#">isType</a>		NA	100% <div></div>	NA	100%	<div></div>
43.....	<a href="#">typeMask</a>		NA	NA	NA	100	