Coverage Report for FBFunctions

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Analysis Information

Model Information

Model version	1.71
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Last saved	Mon Jan 07 12:57:34 2019

Harness information

Harness model(s)	FBFunctions_Harness3
Harness model owner	FBFunctions

Simulation Optimization Options

Default parameter behavior	tunable
Block reduction	forced off
Conditional branch optimization	on

Coverage Options

Analyzed model	FBFunctions_Harness3/BSR_DUPLEX
Logic block short circuiting	off
MCDC mode	masking

Tests

Test#	Started execution	Ended execution

Summary

Model Hierarchy/Complexity	Test 1							
		Decision		Condition		MCDC		Execution	
1. BSR_DUPLEX	28	71%		96%		88%		98%	
2. . . . BSR	13	73%		100%		100%		100%	
3. . . . . 1_val_shift_register	2	NA		NA		NA		100%	
4. . . . . Data 1	1	NA		NA		NA		100%	
5. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
6. . . . . Type 1	1	NA		NA		NA		100%	
7. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
8. . . . . Bit Shift	6	40%		NA		NA		NA	
9. . . . . bit_shift	5	40%		NA		NA		NA	
10. . . . . TypeCheck1T1D	1	100%		100%		NA		100%	
11. . . . . code_type_bit		NA		NA		NA		100%	
12. . . . . cond_generate_error_code	1	100%		NA		NA		100%	
13. . . . . code_no_error		NA		NA		NA		100%	
14. . . . . isType		NA		100%		NA		100%	
15. . . . . typeMask		NA		NA		NA		100%	
16. . . . . Unit Delay Enabled Resettable Synchronous8	1	NA		NA		NA		100%	
17. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
18. . . . . Unit Delay Enabled Resettable Synchronous9	1	NA		NA		NA		100%	
19. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
20. . . . . int type		NA		NA		NA		100%	
21. . . . BSR1	13	73%		100%		100%		100%	
22. . . . . 1_val_shift_register	2	NA		NA		NA		100%	
23. . . . . Data 1	1	NA		NA		NA		100%	
24. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
25. . . . . Type 1	1	NA		NA		NA		100%	
26. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA		100%	
27. . . . . Bit Shift	6	40%		NA		NA		NA	

28. . . . . bit_shift	5	40%		NA		NA	NA	
29. . . . . TypeCheck1T1D	1	100%		100%		NA	100%	
30. . . . . code_type_bit		NA		NA		NA	100%	
31. . . . . cond_generate_error_code	1	100%		NA		NA	100%	
32. . . . . code_no_error		NA		NA		NA	100%	
33. . . . . isType		NA		100%		NA	100%	
34. . . . . typeMask		NA		NA		NA	100%	
35. . . . . Unit Delay Enabled Resettable Synchronous8	1	NA		NA		NA	100%	
36. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA	100%	
37. . . . . Unit Delay Enabled Resettable Synchronous9	1	NA		NA		NA	100%	
38. . . . . Unit Delay Enabled Resettable	1	NA		NA		NA	100%	
39. . . . . int type		NA		NA		NA	100%	
40. . . . State Comparator	1	50%		100%		NA	80%	
41. . . . . Compare To Zero		NA		100%		NA	100%	
42. . . . combine_error_codes		NA		NA		NA	100%	

Details

1. SubSystem block "BSR_DUPLEX"

Child Systems: BSR, BSR1, State Comparator, combine_error_codes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	28
Condition	NA	96% (54/56) condition outcomes
Decision	NA	71% (17/24) decision outcomes
MCDC	NA	88% (14/16) conditions reversed the outcome
Execution	NA	98% (48/49) objective outcomes

Logic block "Logical Operator"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX

Uncovered Links:

Metric	Coverage

Cyclomatic Complexity	0
Condition	50% (2/4) condition outcomes
MCDC	0% (0/2) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input port 1	306	0
input port 2	306	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	**T**T	(**F**T)
input port 2	T**T**	(T**F**)

Full Coverage

Model Object	Metric
RelationalOperator block "Relational Operator"	Condition, Execution
Constant block "Num Ins"	Execution
Constant block "Num Outs"	Execution
Constant block "fb_num_constant"	Execution

2. SubSystem block "BSR"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX

Child Systems: 1_val_shift_register, Bit Shift, TypeCheck1T1D, Unit Delay Enabled Resettable Synchronous8, Unit Delay Enabled Resettable Synchronous9, int type

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	13

Condition	NA	100% (20/20) condition outcomes
Decision	NA	73% (8/11) decision outcomes
MCDC	NA	100% (7/7) conditions reversed the outcome
Execution	NA	100% (19/19) objective outcomes

Logic block "Logical Operator3"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR

Metric	Coverage
Cyclomatic Complexity	0
Condition	100% (4/4) condition outcomes
MCDC	see Logical Operator
Execution	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Logic block "Logical Operator"	Condition, MCDC, Execution
Logic block "Logical Operator1"	Condition, MCDC, Execution
Logic block "Logical Operator2"	Condition, MCDC, Execution
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution
Constant block "Constant"	Execution
Constant block "Constant1"	Execution

3. SubSystem block "1_val_shift_register"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR
Child Systems: Data 1, Type 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	2

Execution	NA	100% (2/2) objective outcomes

4. SubSystem block "Data 1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/1_val_shift_register
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

5. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/1_val_shift_register/Data 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

6. SubSystem block "Type 1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/1_val_shift_register
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

7. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/1_val_shift_register/Type 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

8. SubSystem block "Bit Shift"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR
Child Systems: bit_shift

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	6
Decision	NA	40% (2/5) decision outcomes

9. MATLAB Function "bit_shift"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/Bit Shift

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	5
Decision	NA	40% (2/5) decision outcomes

MATLAB Function "fcn"

Justify or Exclude

FBFunctions_Harness3/BSR_DUPLEX/BSR/Bit Shift/bit_shift

Parent:

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	4
Decision	40% (2/5) decision outcomes

```
 1  function y = fcn(u, mode, N)
 2  %#codegen
 3
 4  switch mode
 5      case 1
 6          y = bitsll(cast_to_fi(u), N);
 7      case 2
 8          y = bitsrl(cast_to_fi(u), N);
 9      case 3
10          y = bitsra(cast_to_fi(u), N);
11      otherwise
12          y = cast_to_fi(u);
13  end
14
15
16
```

#4: switch mode

Decisions analyzed

switch mode	25%
otherwise	0/306
case 1	0/306
case 2	306/306
case 3	0/306

Full Coverage

Model Object	Metric
#1: function y = fcn(u, mode, N)	Decision

10. SubSystem block "TypeCheck1T1D"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR
Child Systems: code_type_bit, cond_generate_error_code, isType

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Condition	NA	100% (4/4) condition outcomes
Decision	NA	100% (2/2) decision outcomes
Execution	NA	100% (6/6) objective outcomes

Full Coverage

Model Object	Metric
Logic block "Logical Operator"	Condition, Execution

11. SubSystem block "code_type_bit"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/TypeCheck1T1D

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

12. SubSystem block "cond_generate_error_code"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/TypeCheck1T1D
Child Systems: code_no_error

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Decision	NA	100% (2/2) decision outcomes
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution

13. SubSystem block "code_no_error"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/TypeCheck1T1D/cond_generate_error_code

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

14. SubSystem block "isType"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/TypeCheck1T1D
Child Systems: typeMask

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Relational Operator1"	Condition, Execution

15. SubSystem block "typeMask"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/TypeCheck1T1D/isType

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
S-Function block "Apply Type Mask"	Execution

16. SubSystem block "Unit Delay Enabled Resettable Synchronou..."

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

17. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/Unit Delay Enabled Resettable Synchronous8

Metric	Coverage (this object)	Coverage (inc. descendants)

Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

18. SubSystem block "Unit Delay Enabled Resettable Synchronou..."

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

19. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR/Unit Delay Enabled Resettable Synchronous9

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

20. SubSystem block "int type"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

21. SubSystem block "BSR1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX

Child Systems: 1_val_shift_register, Bit Shift, TypeCheck1T1D, Unit Delay Enabled Resettable Synchronous8, Unit Delay Enabled Resettable Synchronous9, int type

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	13
Condition	NA	100% (20/20) condition outcomes
Decision	NA	73% (8/11) decision outcomes
MCDC	NA	100% (7/7) conditions reversed the outcome
Execution	NA	100% (19/19) objective outcomes

Logic block "Logical Operator3"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1

Metric	Coverage
Cyclomatic Complexity	0
Condition	100% (4/4) condition outcomes
MCDC	see Logical Operator
Execution	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Logic block "Logical Operator"	Condition, MCDC, Execution
Logic block "Logical Operator1"	Condition, MCDC, Execution
Logic block "Logical Operator2"	Condition, MCDC, Execution
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution
Constant block "Constant"	Execution
Constant block "Constant1"	Execution

22. SubSystem block "1_val_shift_register"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1
Child Systems: Data 1, Type 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	2
Execution	NA	100% (2/2) objective outcomes

23. SubSystem block "Data 1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/1_val_shift_register
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

24. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/1_val_shift_register/Data 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

25. SubSystem block "Type 1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/1_val_shift_register
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

26. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/1_val_shift_register/Type 1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

27. SubSystem block "Bit Shift"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1

Child Systems: bit_shift

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	6
Decision	NA	40% (2/5) decision outcomes

28. MATLAB Function "bit_shift"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/Bit Shift

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	5
Decision	NA	40% (2/5) decision outcomes

MATLAB Function "fcn"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/Bit Shift/bit_shift

Uncovered Links: 

Metric	Coverage
Cyclomatic Complexity	4
Decision	40% (2/5) decision outcomes

```
 1  function y = fcn(u, mode, N)
 2  %#codegen
 3
 4  switch mode
 5      case 1
 6          y = bitsll(cast_to_fi(u), N);
 7      case 2
 8          y = bitsrl(cast_to_fi(u), N);
 9      case 3
10          y = bitsra(cast_to_fi(u), N);
11      otherwise
12          y = cast_to_fi(u);
13  end
14
15
16
```

#4: switch mode

Decisions analyzed

switch mode	25%
otherwise	0/306
case 1	0/306
case 2	306/306
case 3	0/306

Full Coverage

Model Object	Metric
#1: function y = fcn(u, mode, N)	Decision

29. SubSystem block "TypeCheck1T1D"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1

Child Systems: code_type_bit, cond_generate_error_code, isType

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Condition	NA	100% (4/4) condition outcomes
Decision	NA	100% (2/2) decision outcomes
Execution	NA	100% (6/6) objective outcomes

Full Coverage

Model Object	Metric
Logic block "Logical Operator"	Condition, Execution

30. SubSystem block "code_type_bit"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/TypeCheck1T1D

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

31. SubSystem block "cond_generate_error_code"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/TypeCheck1T1D
Child Systems: code_no_error

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Decision	NA	100% (2/2) decision outcomes
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution

32. SubSystem block "code_no_error"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/TypeCheck1T1D/cond_generate_error_code

Metric	Coverage (this object)	Coverage (inc. descendants)

Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

33. SubSystem block "isType"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/TypeCheck1T1D
Child Systems: typeMask

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Relational Operator1"	Condition, Execution

34. SubSystem block "typeMask"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/TypeCheck1T1D/isType

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
S-Function block "Apply Type Mask"	Execution

35. SubSystem block "Unit Delay Enabled Resettable Synchronou..."

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

36. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/Unit Delay Enabled Resettable Synchronous8

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

37. SubSystem block "Unit Delay Enabled Resettable Synchronou..."

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1
Child Systems: Unit Delay Enabled Resettable

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Execution	NA	100% (1/1) objective outcomes

38. SubSystem block "Unit Delay Enabled Resettable"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1/Unit Delay Enabled Resettable Synchronous9

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	1
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Delay block "Enabled Resettable Delay"	Execution

39. SubSystem block "int type"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/BSR1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
Constant block "type1"	Execution

40. SubSystem block "State Comparator"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX

Child Systems: Compare To Zero

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Condition	NA	100% (10/10) condition outcomes
Decision	NA	50% (1/2) decision outcomes
Execution	NA	80% (4/5) objective outcomes

Switch block "Switch1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/State Comparator

Uncovered Links: 

Metric	Coverage
Cyclomatic Complexity	1
Decision	50% (1/2) decision outcomes
Execution	100% (1/1) objective outcomes

Decisions analyzed

logical trigger input	50%
false (output is from 3rd input port)	0/306
true (output is from 1st input port)	306/306

Constant block "Constant1"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/State Comparator

Uncovered Links: 

Metric	Coverage
Cyclomatic Complexity	0
Execution	0% (0/1) objective outcomes

Full Coverage

Model Object	Metric

Sum block "Sum of Elements"	Execution
RelationalOperator block "Relational Operator"	Condition, Execution

41. SubSystem block "Compare To Zero"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX/State Comparator

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

42. SubSystem block "combine_error_codes"

Justify or Exclude

Parent: FBFunctions_Harness3/BSR_DUPLEX

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
S-Function block "Bitwise Operator"	Execution