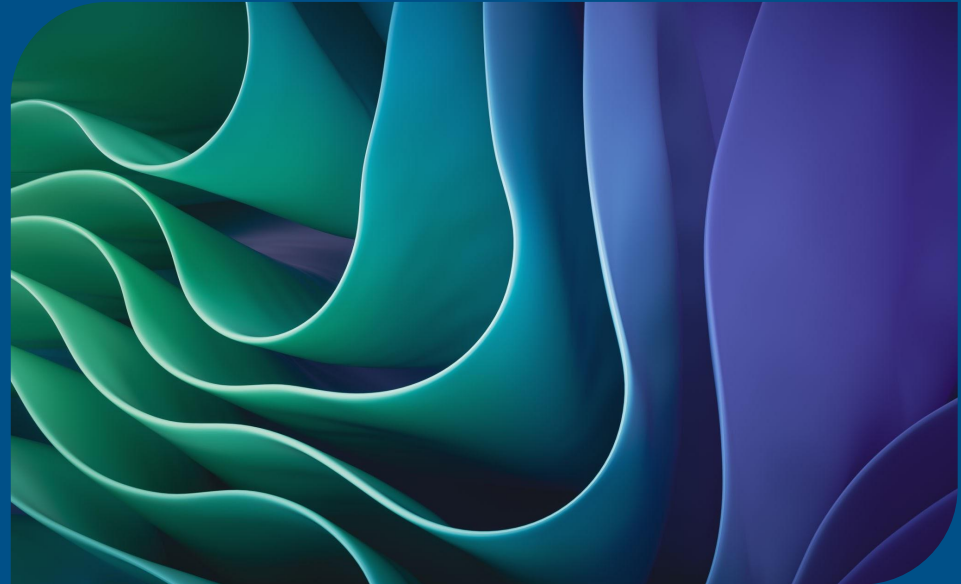


Femto-Ampereometer

F.A.M v1.0

Seamless Measurements of DC &
Pulsated DC Inputs



1. **Problem Statement**
2. **The Requirements**
3. **The Solution**
4. **Simulation Results**
5. **Conclusion & Future Scope**

Problem Statement

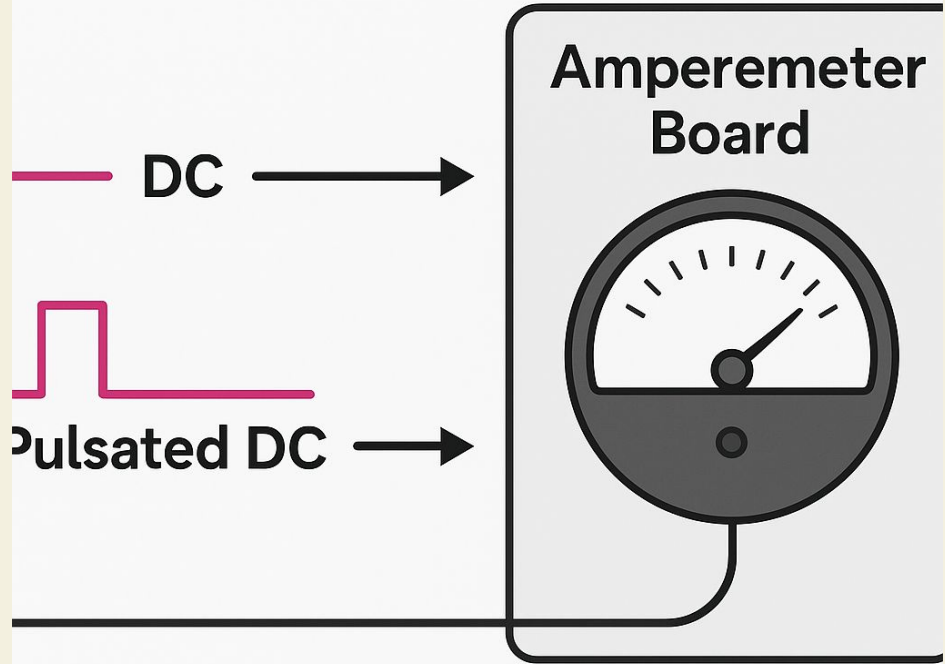
Problems with the X-Design

1. **DC Input measurement Limit: 1pA**
2. **Pulsated DC Measurement Response Time: 1.8 secs Lag.**

The Requirements

The Requirements

1. DC / Pulsated DC Measurement:
5fA to 5mA in the frequency range
of 50Hz to 250Hz.
2. Response Time: Instantaneous
3. Cost Effective Solution In The
Market



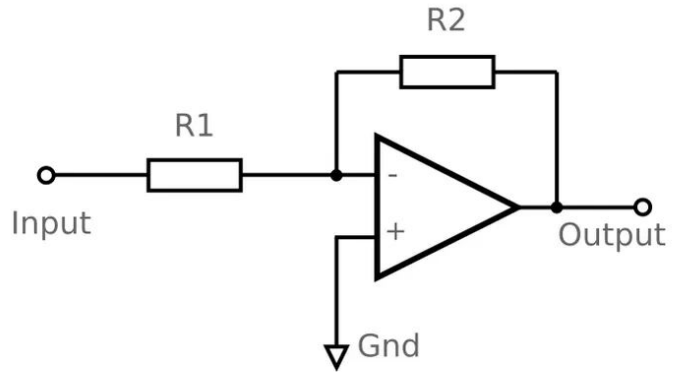
The Solution

Femto-Amperemeter

F.A.M v1.0

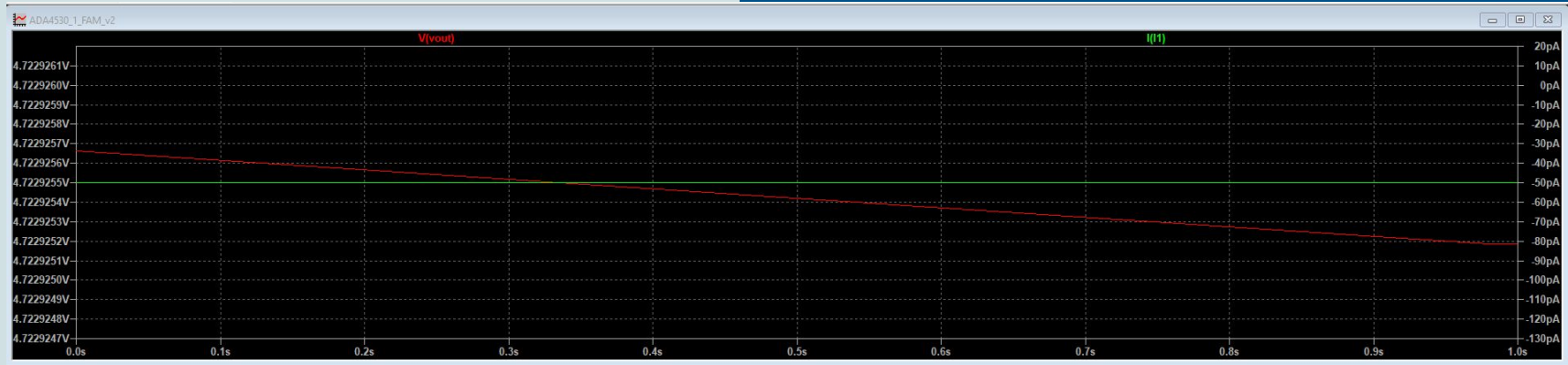
FAM - Ranges for different Modes		
Supply Voltage = +/- 5V	Rin = 1G	
Rf (Ohms)	Cf (F)	Range (A)*
1k	0.01p	500nA to 5mA
10M	0.01p	50pA to 500nA
100G	0.01p	5fA to 50pA
* Based on the reading ability from a 18-bit ADC > Inputs are both DC and Pulsated DC between 50Hz to 250Hz		

Current Amplifier

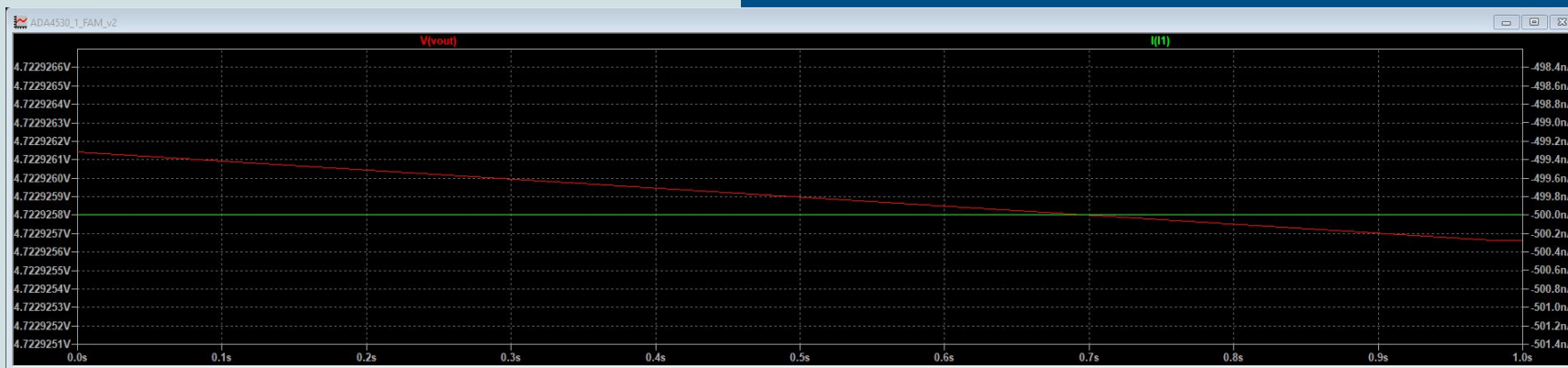
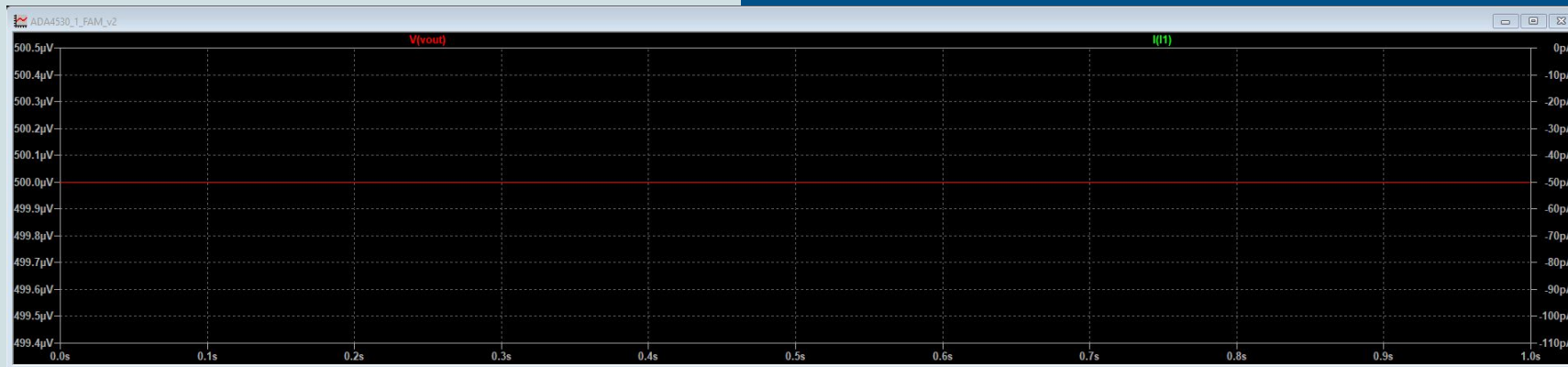


Simulation Results

DC: 5fA & 50pA



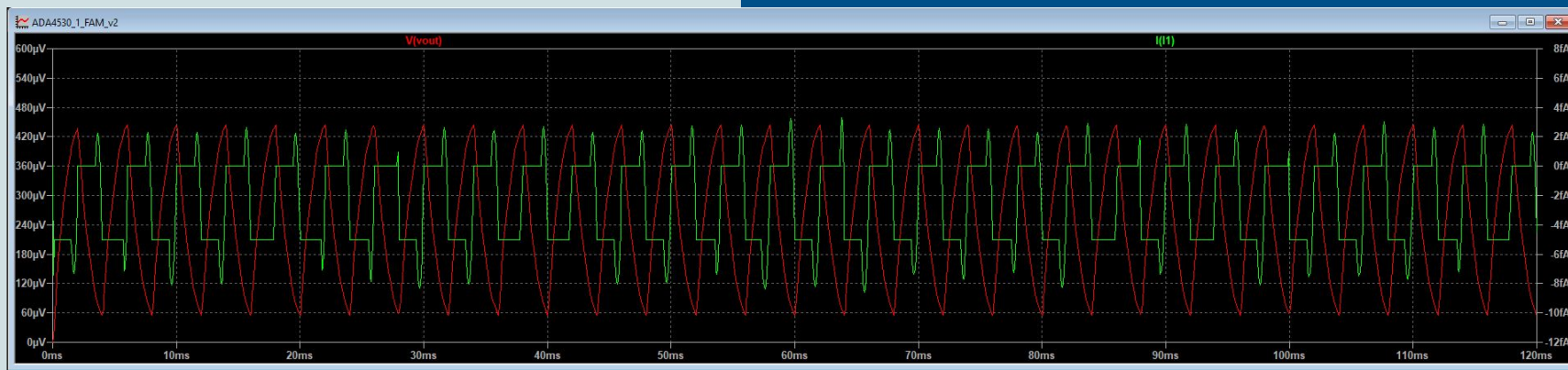
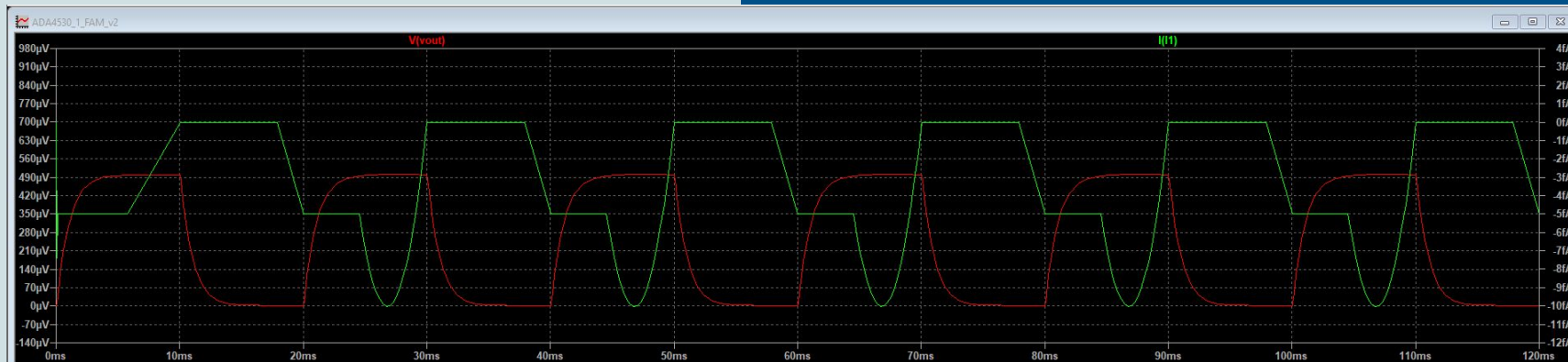
DC: 50pA & 500nA



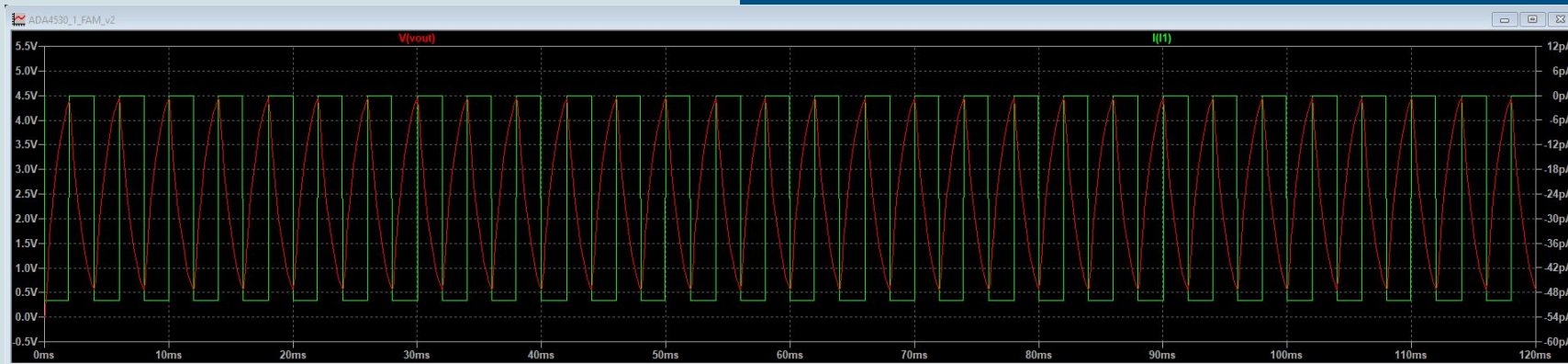
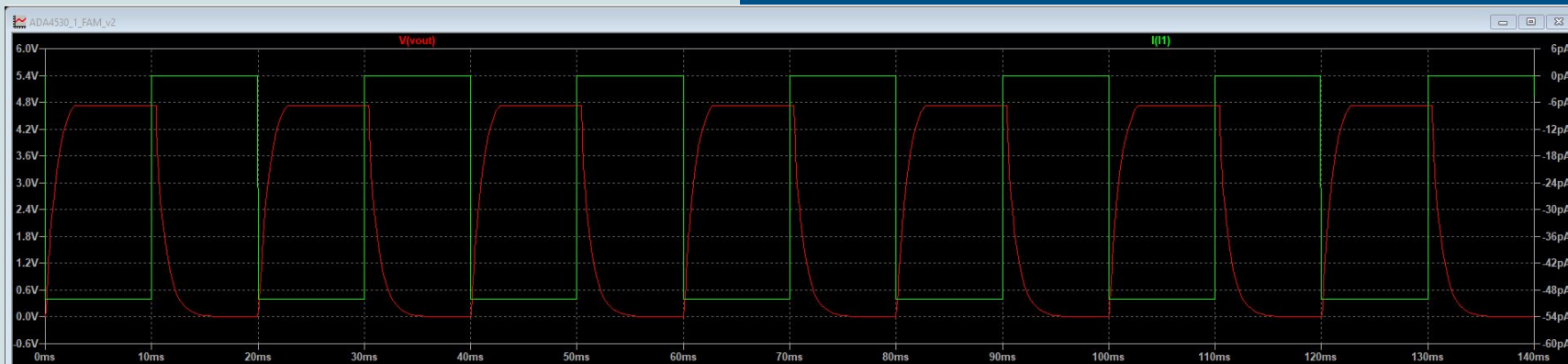
DC: 500nA & 5mA



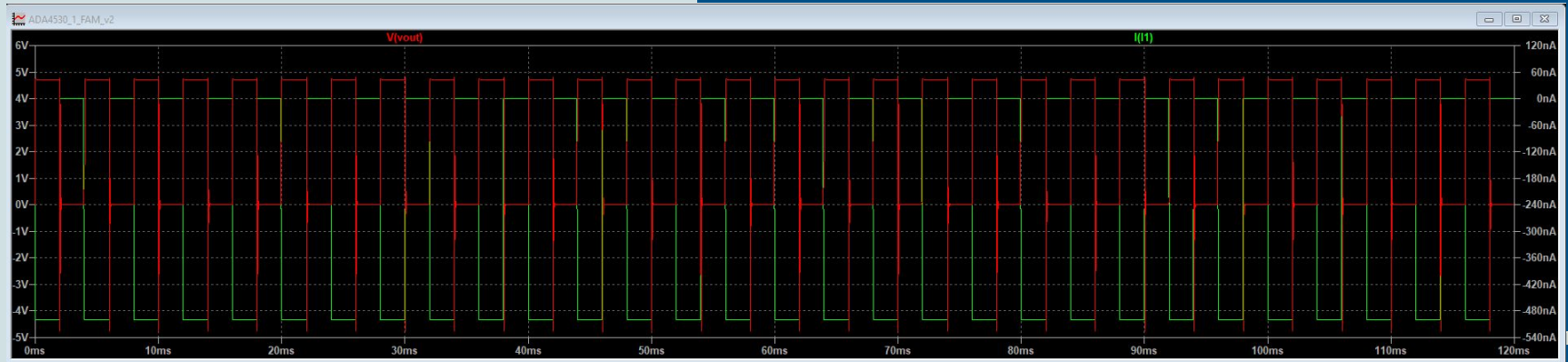
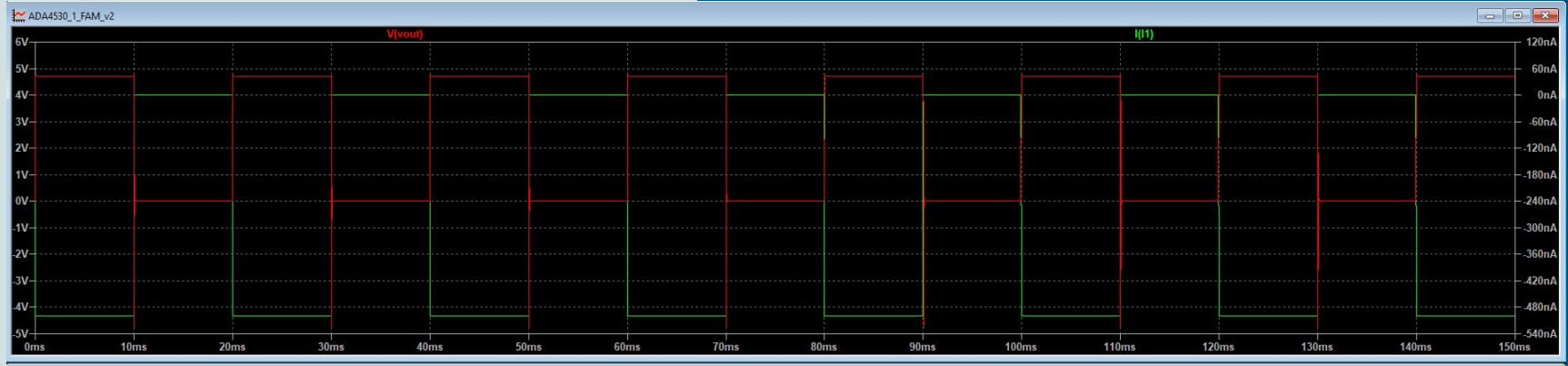
5fA @ 50 & 250 Hz



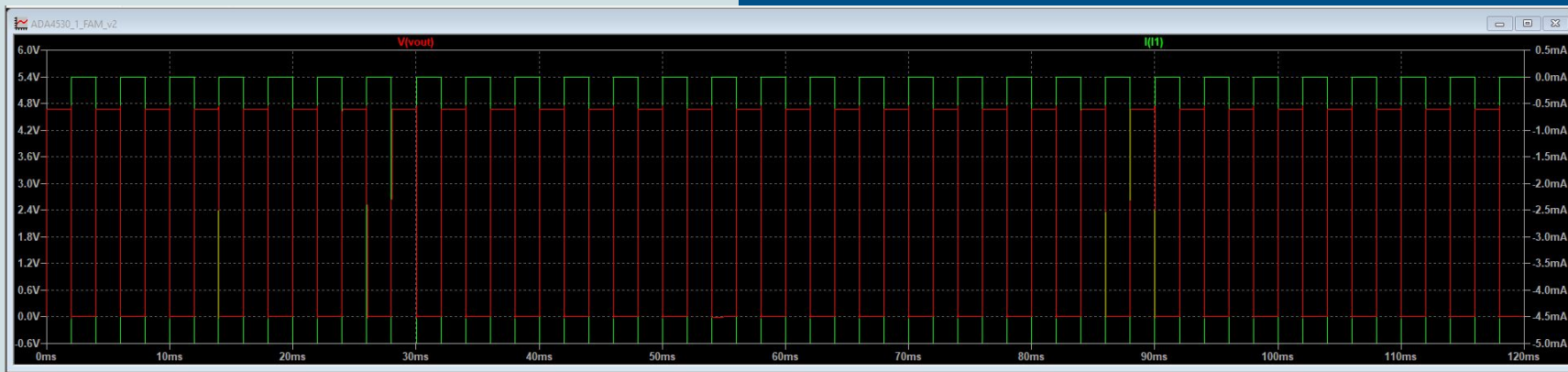
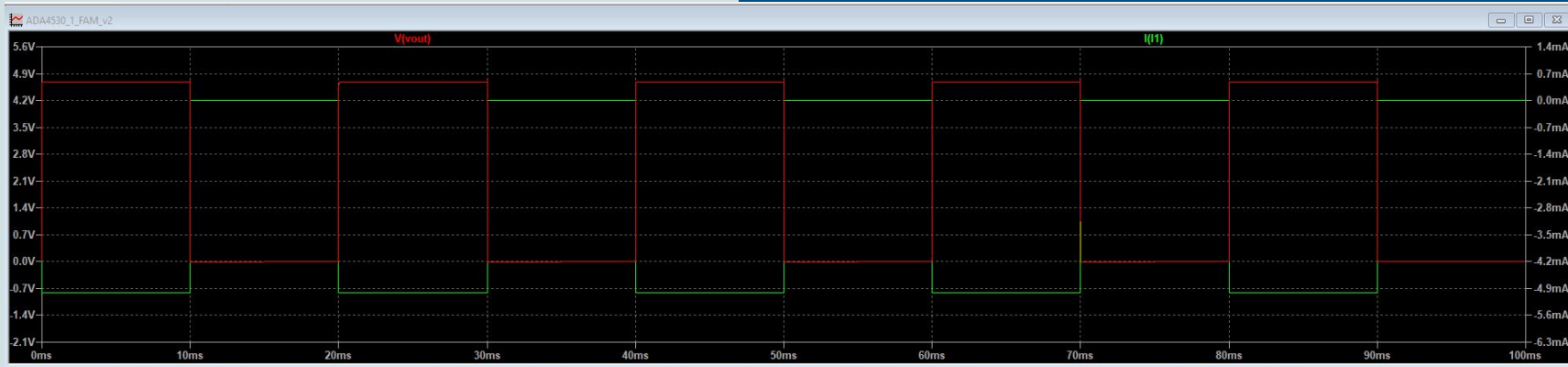
50pA @ 50 & 250 Hz



500nA @ 50 & 250 Hz



5mA @ 50 & 250 Hz



Conclusion & Future Scope

Conclusion & Future Scope

1. Both the problems have been solved as the requirements are met meticulously.
2. POC realization is the next step.
3. Components Selection
4. Schematic Design with other modules like ADC & MCU
5. BOM Generation & Justification of Cost-Effectiveness.
6. Gerber realization & PCB made with focus on rules applicable for ultra-low current measurements and Huge Resistances with ultra-low capacitances.



Source

1. ADA4530-1: <https://www.digikey.in/en/products/detail/analog-devices-inc/ADA4530-1ARZ-R7/5725985>
2. ADA4530-1 EVAL: <https://www.digikey.in/en/products/detail/analog-devices-inc/ADA4530-1R-EBZ-TIA/5726244>