Home Assignment Rollno > 190108022

Do ve feedbook

Voltage sampling > Current mixing

(Shund sampling > shund mixing

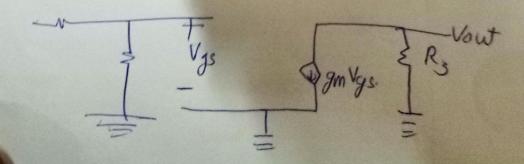
(b) There is a Shout connection at the output so output resistance & by (IFA E)

closed Loop Row = Open doop Row HAB

there is a shund connection at it so if a sesistance is by a factor 4AB

CL to Rin = OL Rin 1 + AB

E At very high fixed small signal equivals As capadition get short cht at High freg



$$V_{gs} = \frac{V_{in} \times R_{a}}{R_{i} + R_{a}} - 0$$

$$Apt Ying KCL$$

$$gmVgs + \frac{(Voud - 0)}{R_{3}} = 0$$

$$gmVgs = -\frac{V_{aut}}{R_{3}}$$

$$we gh gm \left(\frac{V_{in} R_{a}}{R_{i} + R_{i}}\right) = \frac{-V_{0}}{R_{3}}$$

$$CLA = \frac{Valt}{V_{in}} = \frac{-IaOA10^{3}}{A} = -0.06$$

CL gain at High frequency A = -0.06At very small/low freq. small signal equivalent Vin - R + W R 3 Jan Vgs KCL at output node: Vin - Vout = gn Vgs - (1)
RI+R2+R3. As some avoient golgs flows through R. => Vin- Vgs = gmVgs Vgs= Vin (1) Vin = (I+Rgm) Vgs from (14(7) Vout = 10 (R,+R+R3) gm. CL goin at low grap 0.648 Bry 1+gm (R) = 10.36 = 0.698

a) regative feedback voltage sampling > Voltage mixing shund sampling - seriesmixing D) As feedback abt is connected in shund with the Off. The Rout for a closed loop aby by a feed both factor by open loop gain closed leep Rout = open loop Rout. As the feedback is conmoded in series with the input. The 1/8 resistance for closed loop ckt 1 by foctor of (1+AB) closed loop Rin= (open loop Kin) (I+AP) open loop ordand impedence as; to we can neglect rolly There fore open loop Row = Rit Re OL Rad= PKN Therefore Gain dosed look can be given by Jeedbock factor

B = Vf/Vow = R1

Ri+R2 1/2 PRI AS A0 = 0 AV = 1 = 2 there-fore closed lookgain. CL = Av = 2.

loop circuit open look gain = Vout Vow # for first stage gain = Vx = -gm (rds3/lk) where = R=(RillRs)+ &dsi +gmrds(R,1118) = As 1=0 =) rds= rds=0 Therefore ods3/1R=00 for second stage; Gain = Vout = -9m (RHR) ·· Vout = (Vx) (Vout) = oo (-gn (R,+R)) Therefore openloop Grain = P 01 Ao = 00 - 8

closed look output impedence
as discoused in part B.

CL fort = OL Rout

1+ Ao B

as Ao ≈∞ (open look Grain).

Closed look Rout = 2KR

1+∞XI = O

Therefore closed loop Rout = 0

a > Rout = 0