

# 1. Description

## 1.1. Project

Project Name	trial_key
Board Name	custom
Generated with:	STM32CubeMX 6.13.0
Date	04/28/2025

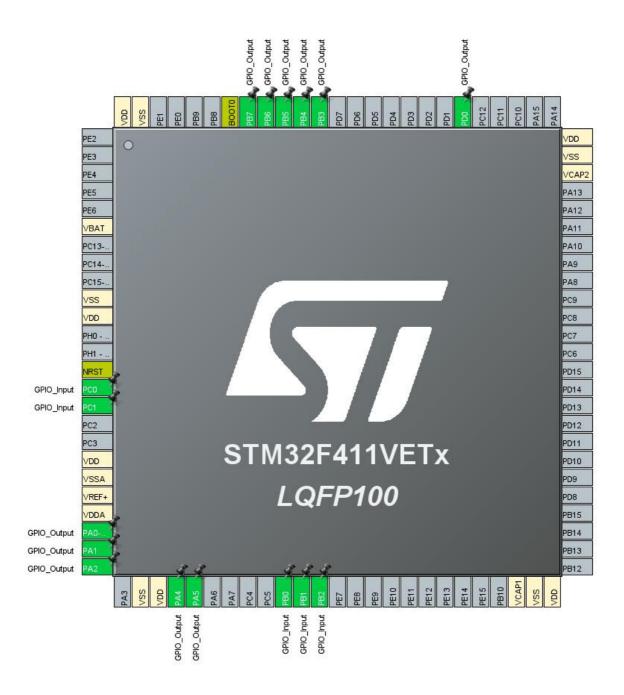
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F411
MCU name	STM32F411VETx
MCU Package	LQFP100
MCU Pin number	100

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

## 2. Pinout Configuration

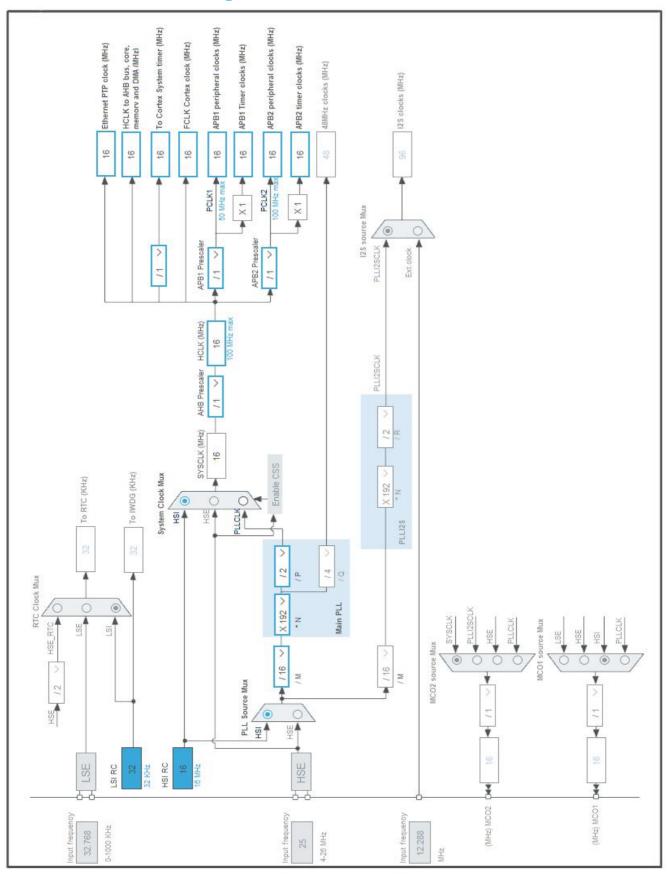


# 3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP100	(function after		Function(s)	
	reset)			
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
14	NRST	Reset		
15	PC0 *	I/O	GPIO_Input	
16	PC1 *	I/O	GPIO_Input	
19	VDD	Power		
20	VSSA	Power		
21	VREF+	Power		
22	VDDA	Power		
23	PA0-WKUP *	I/O	GPIO_Output	
24	PA1 *	I/O	GPIO_Output	
25	PA2 *	I/O	GPIO_Output	
27	VSS	Power		
28	VDD	Power		
29	PA4 *	I/O	GPIO_Output	
30	PA5 *	I/O	GPIO_Output	
35	PB0 *	I/O	GPIO_Input	
36	PB1 *	I/O	GPIO_Input	
37	PB2 *	I/O	GPIO_Input	
48	VCAP1	Power		
49	VSS	Power		
50	VDD	Power		
73	VCAP2	Power		
74	VSS	Power		
75	VDD	Power		
81	PD0 *	I/O	GPIO_Output	
89	PB3 *	I/O	GPIO_Output	
90	PB4 *	I/O	GPIO_Output	
91	PB5 *	I/O	GPIO_Output	
92	PB6 *	I/O	GPIO_Output	
93	PB7 *	I/O	GPIO_Output	
94	воото	Boot		
99	VSS	Power		
100	VDD	Power		

\* The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F411
мси	STM32F411VETx
Datasheet	DS10314_Rev6

### 1.2. Parameter Selection

Temperature	25
Vdd	1.7

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

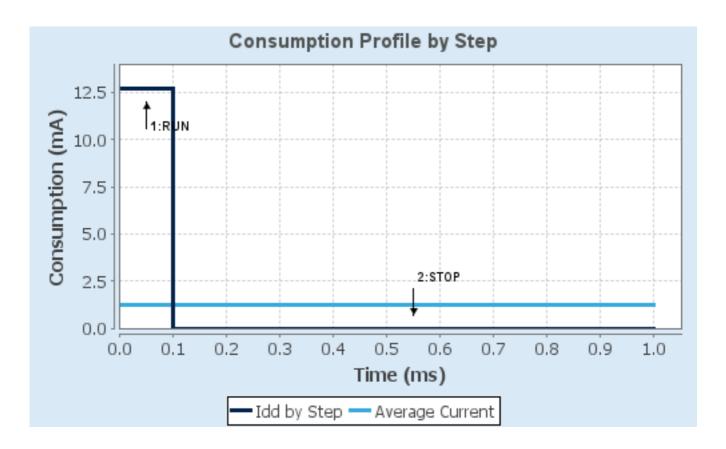
## 1.4. Sequence

	1	
Step	Step1	Step2
Mode	RUN	STOP
Vdd	1.7	1.7
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	SRAM	n/a
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash-
		PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	12.7 mA	9 µA
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	104.07	105
Category	In DS Table	In DS Table

## 1.5. Results

Sequence Time	1 ms	Average Current	1.28 mA
Battery Life	3 months, 19	Average DMIPS	125.0 DMIPS
	days, 6 hours	-	

### 1.6. Chart



# 2. Software Project

## 2.1. Project Settings

Name	Value
Project Name	trial_key
Project Folder	C:\Users\jayas\STM32CubeIDE\workspace_1.17.0\trial_key
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX GPIO Init	GPIO

## 3. Peripherals and Middlewares Configuration

#### 3.1. RCC

#### 3.1.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

TIM Prescaler Selection Disabled

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 3.2. SYS

**Timebase Source: SysTick** 

<sup>\*</sup> User modified value

# 4. System Configuration

## 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
GPIO	PC0	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PC1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PA0-WKUP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB0	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PB1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PB2	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PD0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

## 4.2. DMA configuration

nothing configured in DMA service

## 4.3. NVIC configuration

## 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
FPU global interrupt	unused			

## 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Non maskable interrupt	laise	tide	laise
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

#### \* User modified value

# 5. System Views

- 5.1. Category view
- 5.1.1. Current



### 6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f411\_bsdl.zip https://www.st.com/resource/en/ibis\_model/stm32f411\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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tools\_portfolio.pdf

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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- driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
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- interconnections-on-stm32f401-and-stm32f411-linesstmicroelectronics.pdf
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