

Vishal Yadav

Add: H-5, Village Kamardin Nagar, Nangloi, Delhi-110041

Phone: 91-8750353344 | **Email:**

ydv_vishal@hotmail.com

EDUCATION

Exam/Degree	School/College	Year of passing	Percentage
B.Tech Electronics Engineering	Delhi College of Engineering	2015	77.5%
Standard XII	S.D.R.Public School , Delhi	2011	88%
Standard X	S.D.R.Public School , Delhi	2009	91.2%

TECHNICAL SKILLS

Programming Languages – C,C++ , Core Java , Data Structure

Front End Development: Basic of HTML & JavaScript

DataBase Management: SQL*PLUS(Oracle) ,PL/SQL(Procedural Language)

Software: P-SPICE , Eagle , Eclipse ,MS-Office

Framework: Spring MVC

WORK AND LEADERSHIP EXPERIENCE

Fidelity Investments

Analyst Programmer, Technology

Gurgaon(Haryana),India

1st July,2015 –

- Developed an Online Portal for HR-Ops of Fidelity Investment which automated the process of Tax-Document Submission , New Joinee form filling , Letter Request and Probation Confirmation.
- An auto-email Generation function was made which can populate the Template from data-base and can mail with attachment.

Texas Instrument University Program

Internship

NSIT(Dwarka) , Delhi

9th Dec 2013 - 4th Jan,2014

- Four week winter internship program in ‘Micro-Controller based Embedded System Design’.
- Worked on PCB manufacturing.
- Made a Speedometer which uses MSP430 Microcontroller . It can detect the speed of object and display it in both (km/hr and m/sec)on lcd..

Microtek Private Limited

Delhi, India

Internship

June 2013 & June 2014 – July 2014

- Studied Digital UPS , it's charging and discharging circuit.
- Performed Simulation on various Circuits of Inverter in P-SPICE.
- Performed regression analysis to predict the fault condition and inverter efficiency.
- Performed regression analysis on the number of inverter sold to the amount spent on advertisement.
- Worked on reducing the charging circuit of inverter.

RESEARCH AND DEVELOPMENT PROJECTS

- Performed simulation of “**Operational Transresistance Amplifier**” (OTRA) on P-SPICE software and it's hardware realisation using **AD844** IC
- Made ‘Heart Rate Monitor’ and ‘Electronic voting Machine’ during TROIKA 2014
- Worked on “**Differential Sallen-key Filter realisation using OTRA**” under the guidance of Dr Rajeshwari Pandey (DTU).
- Worked on making “**Voltage Differential Transconductance Amplifier(VDTA)**” block in P-SPICE software and performed it's simulation.
- Made “**Multivibrators circuits (Astable , monostable & bistable) using VDTA**” and verified their result.
- Made “**Universal Multimode Filter using VDTA**” under the guidance of Dr. Rajeshwari Pandey (DTU)

ACTIVITIES AND INTERESTS

Awards and Honor

- Meritorious Awards for academic excellence in classes 5th 7th , 8th , 10th and 11th .
- Second position in “**DESIGN PRO**” a designing event organized by TROIKA IEEE DTU.
- Online Coding Competition at Hacker-Rank:
 - i) Project Euler : 3000 out of 42,000.
 - ii) World-Cup : Reached into finals(by clearing Quarter-final and semi-final)
 - iii) Magic Lines : 361

Extra-Curricular

- Active member of IEEE Society during 2012 and helped in Organizing TROIKA 2012 during Technical Fest
- Attended a workshop on ethical hacking during 2011-12.
- Organized an event Arena 2014

Interests: Cricket, Travelling, Music