

# Jaylen Wang

[Personal Site](#)

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## EDUCATION

### Carnegie Mellon University

*PhD Candidate* in Electrical and Computer Engineering

Advisor: Prof. Akshitha Sriraman

Pittsburgh, PA

*August 2022 ~ May 2027*

### Harvard University

*Bachelor of Science* in Electrical Engineering, *Minor* in Computer Science

GPA: 4.0 | See last page for Selected Courses

Cambridge, MA

*May 2022*

## RESEARCH INTERESTS

My research interest broadly covers hardware-software co-design to perform novel design space explorations and illuminate **design tradeoffs in of performance, resilience, and sustainability**. This has manifested itself in my current work to find co-design strategies to increase fault tolerance of machine learning accelerators as well as previous work to improve carbon-efficiency of cryptocurrency mining hardware.

Currently I am exploring how to co-design solutions for **more energy and carbon efficient datacenter/cloud systems, hardware, and applications**. I am also newly interested in how to build equitable systems that ensure fair use to the diverse set of users that systems today must serve.

## WORKSHOPS/ POSTERS

**Jaylen Wang**, Abdulrahman Mahmoud, Gu-Yeon Wei, David Brooks, *A Dataflow-Aware Fault Resilience Analysis Framework for Deep Neural Network Accelerators*, Young Architect Workshop (YArch) co-located with ASPLOS, 2021.

Sahana Rangarajan, Xuesi Chen, **Jaylen Wang**, Pratyush Patel, Akshitha Sriraman, *Designing Equitable Data Center Scheduling Systems*, Career Workshop for Inclusion and Diversity in Computer Architecture (CWIDCA) co-located with MICRO, 2022.

## RESEARCH EXPERIENCE

### Systems and Architecture Research Lab

*Graduate Researcher*

Pittsburgh, PA

*Fall 2022 – Present*

- Developing hardware/software solutions for carbon-efficient data center hardware.
- Profiling data center applications to run on heterogeneous generations of hardware to promote hardware reuse.

### Harvard Architecture, Circuits, and Compilers Group

*Undergraduate Researcher*

Cambridge, MA

*Summer 2021 – Present*

- Leading research to develop framework for analyzing hardware-aware resilience of deep neural network systems to soft errors (*working towards publication*).
- Providing insights into how an accelerator's dataflow affects error propagation in DNN systems.
- Led research project into sustainability of cryptocurrency, considering mining server operational and manufacturing carbon costs of cryptocurrency hardware.

### Harvard Edge Computing Lab

*Undergraduate Researcher*

Cambridge, MA

*Summer 2020*

- Wrote C++ code to add a SLAM ROS node within an existing micro aerial vehicle (MAV) simulation framework called "MAVBench".

- Analyzed and studied how using SLAM for localization affects efficiency and power usage within MAV applications depending on the environment and hardware.

WORK EXPERIENCE	<b>Hoffman Lab</b> <i>Undergraduate Researcher</i>	Cambridge, MA <i>Summer 2019</i>
	<ul style="list-style-type: none"> <li>• Developed a tensioning system for an XY-walker system in order to extend the range of a scanning tunneling microscope used to research the proximity effect of superconductivity.</li> <li>• Gained experience with LabVIEW, SolidWorks, and working with graduate students and professors to solve engineering problems.</li> </ul>	
	<b>MathWorks</b> <i>Intern in Deep Learning HDL Toolbox Team</i>	Natick, MA <i>Summer 2021</i>
	<ul style="list-style-type: none"> <li>• Enabled mapping of non-square convolution kernels onto square PE array allowing users to deploy models using non-square kernels onto FPGAs.</li> <li>• Improved performance of mapping algorithm using more efficient data structures.</li> <li>• Led work to support imported flatten layers from Keras and ONNX.</li> </ul>	
AWARDS	<b>Carnegie Institute of Technology Dean's Fellow</b>	<i>August 2022</i>
	Full tuition and support for first year.	
	<b>Harvard SEAS Dean's Engineering Design Award</b>	<i>May 2022</i>
	\$500 awarded to <u>top 7 (out of 43) best Senior engineering design projects.</u>	
	<b>Sophia Freund Prize</b>	<i>May 2022</i>
	\$1000 awarded to <u>highest ranking undergraduate</u> in major's department (EE).	
	<b>Phi Beta Kappa Member</b>	<i>Class of 2022</i>
	Admitted into Harvard's chapter, one of 146 out of 1962 (7.4%) students.	
	<b>John Harvard Scholar</b>	<i>May 2020</i>
TEACHING EXPERIENCE	Award given to <u>top 5% (4.0 GPA)</u> of students in respective class.	
	<b>Derek Bok Center Distinction in Teaching</b>	<i>Fall 2020-Fall 2021</i>
	Awarded to <u>highly rated (by students) TAs</u> ; received distinction in three semesters.	
	<b>Harvard College Research Program Funding Recipient</b>	<i>May 2019</i>
	Awarded \$3,500 to perform independent research during the summer of 2019.	
	<b>Detur Prize Winner</b>	<i>September 2019</i>
LEADERSHIP & ACTIVITIES	Recognizes students with top academic standing in their first year at Harvard.	
	<b>Harvard University, Undergraduate Teaching Assistant</b>	Cambridge, MA
	- Systems Programming and Machine Organization (CS 61) Prof. Eddie Kohler	<i>Fall 2020, 2021</i>
	- Circuits, Devices, and Transduction (ES 152) Prof. Gage Hills & Woodward Yang	<i>Fall 2021</i>
	- Systems and Control (ES 155) Prof. Li Na & Yue Lu	<i>Fall 2021</i>
	- Introduction to Electrical Engineering (ES 50) Prof. Chris Lombardo & Marko Loncar	<i>Spring 2021</i>
	- Integration, Series and Differential Equations (Math 1B) Dr. Hakim Walker	<i>Fall 2019</i>
LEADERSHIP & ACTIVITIES	<b>CIT K-12 Outreach</b>	Pittsburgh, PA
	<i>STEM Volunteer</i>	<i>Fall 2022 – Present</i>
	<ul style="list-style-type: none"> <li>• Crafting curricula in computer engineering for bi-weekly lessons in underserved and marginalized public schools in the greater Pittsburgh area.</li> <li>• Presenting research to students to promote future students to pursue STEM.</li> </ul>	

**Harvard Club Tennis***Captain/President*

Cambridge, MA

*May 2019 – Spring 2022*

- Elected by club tennis members to organize practices and handle the organization's trips to national tournaments and finances.
- Presided over meetings members of the Harvard Tennis community.

**Harvard College Engineering Society***Co-President*

Cambridge, MA

*Fall 2020 – Spring 2021*

- Elected to lead organization and manage ten committees, delegating work and keeping track of various engineering community of events.
- Secured sponsorships and contacts with companies.
- Managed contact with administration and faculty to discuss students' needs.

**Harvard Engineering Peer Concentration Advisors***Co-President*

Cambridge, MA

*May 2020 – August 2021*

- Elected to lead organization and manage ten committees, delegating work and keeping track of various engineering community of events.
- Secures sponsorships and contacts with companies.
- Manages contact with administration, meeting with faculty to discuss students' needs.

**SELECTED  
COURSES****Carnegie Mellon University**

Modern Computer Architecture and Design (18.740)

Modern Computer Systems (18.847B)

**Harvard University**

Advanced Computer Architecture (CS 146)

Advanced Design of VLSI Circuits and Systems (CS 248)

Close Readings in Distributed Systems (CS 246)

Computing Hardware (CS 141)

Circuits, Devices and Transduction (ES 152)

Operating Systems (CS 161)

Systems Programming and Machine Organization (CS 61)

Data Structures and Algorithms (CS 124)

Computer Vision (ES 143)

Signals and Communications (ES 156)

**MIT (Cross-Registered)**

Hardware Architecture for Deep Learning (6.812)

Intro to Machine Learning (6.036)

**COURSE  
PROJECTS****Hardware Architecture for Deep Learning (6.812)***Final Project*

Cambridge, MA

*Spring 2021*

- Explored trade-offs between accuracy, area, and energy of different quantization methods in DNN models on accelerator frameworks.
- Used Timeloop/Accelergy, a DNN accelerator energy and area modeling framework, to perform analysis.

**Advanced Computer Architecture (CS 146)***Final Project*

Cambridge, MA

*Spring 2019*

- Researched for and wrote final research paper on how an understanding of fundamental computing hardware leads to speedups in machine learning matrix multiplication computations.

- Implemented and optimized such techniques as loop unrolling and tiling in C to allow for more effective computations and utilization of the CPU.

## TECHNICAL SKILLS

<b>Programming Languages</b>	C++, C, Python, Verilog, SystemVerilog, Assembly, Catapult HLS, MATLAB
<b>System Skills</b>	Systems programming, Docker, performance characterization
<b>Simulators/Tools</b>	Gem5, Pin, Linux perf, PyTorch, Git