

PDP-11/05 SCL Connector

(as viewed from the back of system)

UU 39	SS 37	PP 35	MM 33	KK 31	HH 29	EE 27	CC 25	AA 23	Y 21	W 19	U 17	S 15	P 13	M 11	K 09	H 07	E 05	C 03	A 01
VV 40	TT 38	RR 36	NN 34	LL 32	JJ 30	FF 28	DD 26	BB 24	Z 22	X 20	V 18	T 16	R 14	N 12	L 10	J 08	F 06	D 04	B 02

SCL Connector Signals

Signal	SCL Connector Pin
GND	A/01, B/02, UU/39, VV/40
-15V	BB/24
SERIAL OUT + (20mA)	V/18
CLK IN (TTL)	T/16
SERIAL IN - (20mA)	DD/26
READER RUN - (20mA)	R/14
CLK DISAB (TTL)	N/12
SERIAL OUT - (20mA)	L/10
+5V	C/03
SERIAL OUT (TTL)	D/04
READER RUN + (20mA)	F/06
SERIAL IN (TTL) *	RR/36
20ma INTERLOCK **	NN/34
SERIAL IN + (20ma)	LL/32

Notes

* **SERIAL IN (TTL)** [RR/36] expects the input signal to be inverted as compared to a traditional TTL serial line--i.e. 0V represents a serial MARK and +5V represents a SPACE.

** To enable the 20ma interface, **20ma INTERLOCK** [NN/34] must be connected to **SERIAL IN (TTL)** [RR/36].