31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Cond				F		I	Opcode				S	Rn				Rd				Operand2												Instruction
1110				00		1	Opcode				S	Rn				Rd				RotSpec				Imm8								DP imm
						0														ShiftAmount: Imm5				shift type		0	Rm					DP reg sh_I
																				Rs				0		1						DP reg sh_R

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Cond				F			P	U		W	L	Rn				Rd				OffsetSpec, SH												Instruction
1110				00		0	P	U	0	W	0	Rn				Rd				Imm7_4				1	SH		1	Imm3_0				STR SH imm
											1																						LDR SH imm
									1		0									0000								Rm				STR SH reg
											1																					LDR SH reg

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Cond				F		I	P	U	B	W	L	Rn				Rd				OffsetSpec												Instruction
1110				01		0	P	U	B	W	0	Rn				Rd				Imm12												STR imm
											1																						LDR imm
						1					0									ShiftAmount: Imm5				shift type		0	Rm					STR reg
											1																						LDR reg

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
Cond				F		Opc		OffsetSpec																								Instruction
1110				10		10		Imm24																								B
0000																																BEQ
0001																																BNE

Note 1: For DP instructions, S must be '1' if Opcode = "10xx"

Note 2: For DT instructions, W must be '0' if P = '0'

Note 3: For DT SH instructions, SH must be "01" if L = '0'

shift type = "00" (LSL), "01" (LSR), "10" (ASR), "11" (ROR)

SH = "01" (unsigned half word), "10" (signed byte), "11" (signed half word)