

## Branch Trace Record Description

1. Address of branch instruction<sup>1</sup>: Hexadecimal
2. Taken (T) or not taken (N)
3. Address of branch instruction target<sup>2</sup>: Hexadecimal (only needed when studying branch target buffers).
4. Instruction Name: String, not used in analysis at the moment.

Example:

48c30c T 48c1de JMP\_IMM

48c1ec T 48d24d JMP\_REG

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<sup>1</sup> Branch addresses are not necessarily divisible by 4: the instructions are extracted from an X86 trace, which - unlike RISC V- had variable length instructions.

<sup>2</sup> When the branch instruction is not taken, the target is the next instruction whose address is not necessarily PC+4.