

# dual\_lm13700\_vca

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Eeschema 5.1.5+dfsg1-2build2

**Component Count:33**

Ref	Value	Description
C1, C2, C3, C4	0.1u	Polarized capacitor, small symbol
J1	CV1	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
J2	IN1	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
J3	Conn_02x08_O	Generic connector, double row, 02x08, odd/even pin numbering scheme (row 1 odd numbers, row 2 even numbers)
J4	OUT1	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
J5	CV2	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
J6	IN2	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
J7	OUT2	Audio Jack, 2 Poles (Mono / TS), Switched T Pole (Normalling)
Q1, Q2	BC557	0.1A Ic, 45V Vce, PNP Small Signal Transistor, TO-92
R1, R2, R4, R8, R9, R11	100k	Resistor symbol for simulation only
R3, R5, R10, R12	220R	Resistor symbol for simulation only
R6, R13	13k	Resistor symbol for simulation only
R7, R14	330k	Resistor symbol for simulation only
R15, R16	22k	Resistor symbol for simulation only
RV1, RV2	100k	Potentiometer
U1	TL074	Quad Low-Noise JFET-Input Operational Amplifiers, DIP-14/SOIC-14
U2	LM13700	Dual Operational Transconductance Amplifiers with Linearizing Diodes and Buffers, DIP-16/SOIC-16