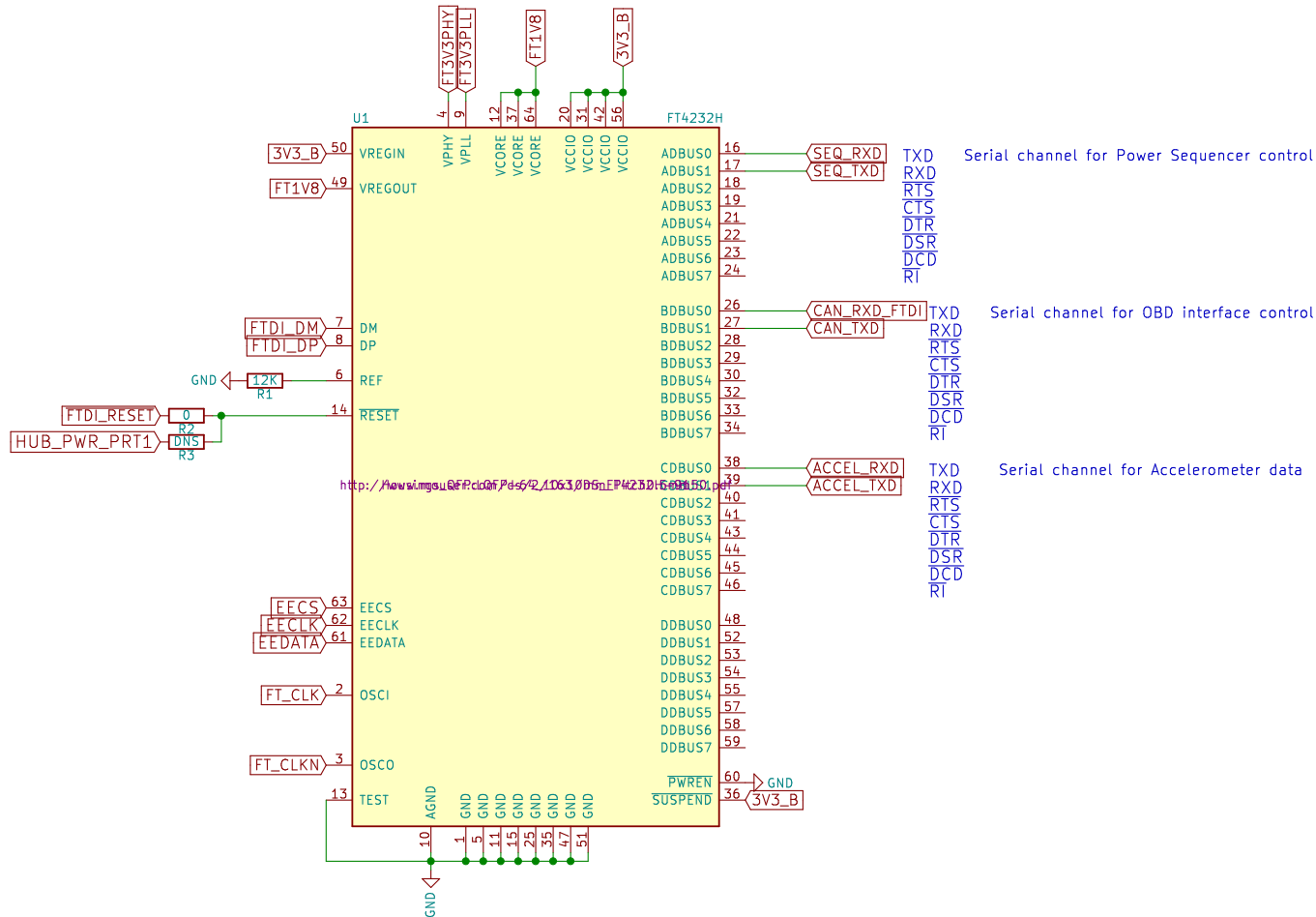
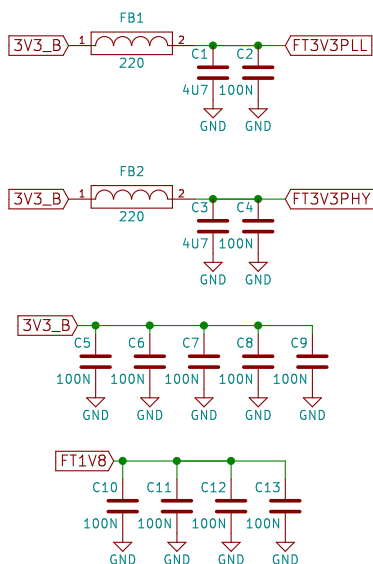
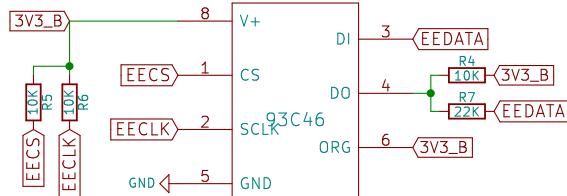


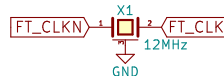
## Power Filtering



## FTDI Device Configuration EEPROM



## 12MHz FTDI USB Clock



Manager: Magnus Feuer  
Advisor: Rudolf Steif  
Designer: Jesse Banks  
**Jaguar Land Rover OSTC**

Sheet: /V2X FTDI Sub/  
File: V2X\_FTDI.sch

**Title: V2X – Open source RVI over SMS daughter card**

Size: A4 Date: 2015-07-17

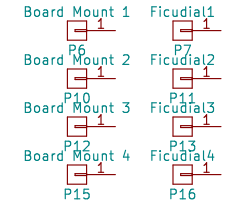
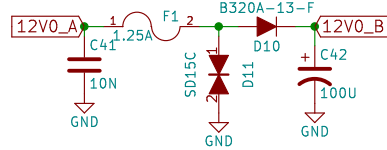
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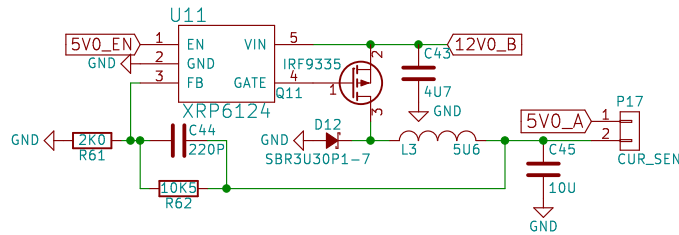
Id: 2/8



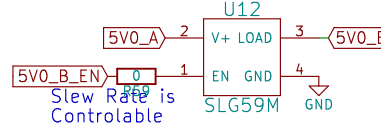
# Vehicle Power Input Filtering and ESD Spike Suppression



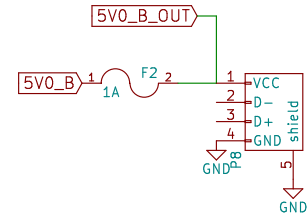
## 5V @ 1.25A DC-DC Power Supply



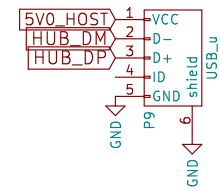
## HOST 5V0 rail



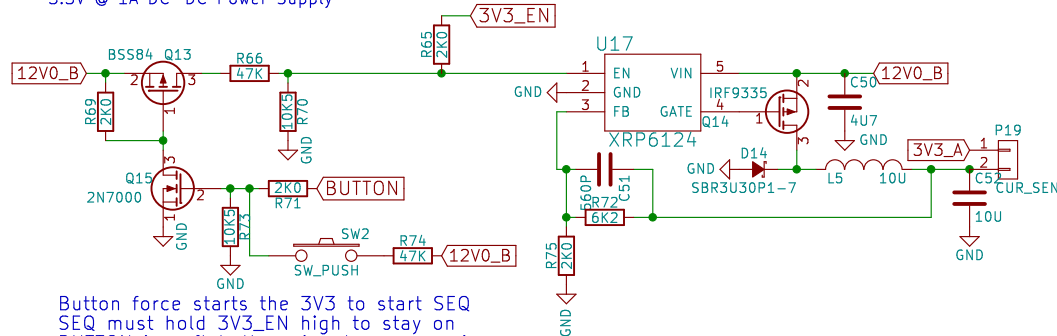
## HOST Power Connection



## HUB Data, HOST Power

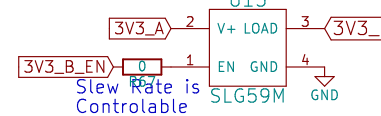


## 3.3V @ 1A DC-DC Power Supply

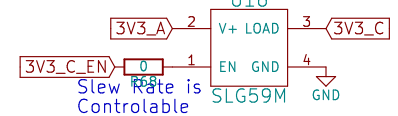


Button force starts the 3V3 to start SEQ  
SEQ must hold 3V3\_EN high to stay on  
BUTTON is soft button signal once running  
3V3\_EN pulled low kills supply regardless if button pressed

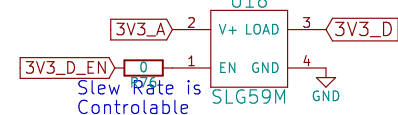
## HUB & FTDI 3V3 rail



## CAN 3V3 rail

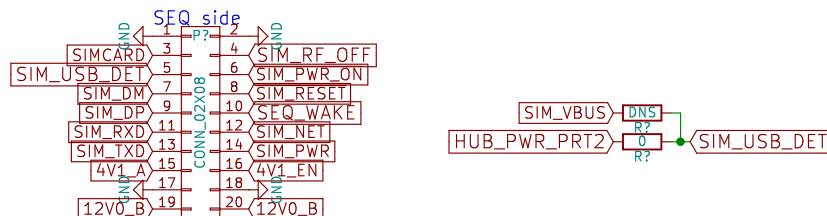


## Accelerometer 3V3 rail



SEQ will disable the 3V3 Supply and siphon power from 4V1 while in SMS standby receive power state.

## GSM+GPS board header



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Sheet: /V2X Power Sub/  
File: V2X\_PWR.sch

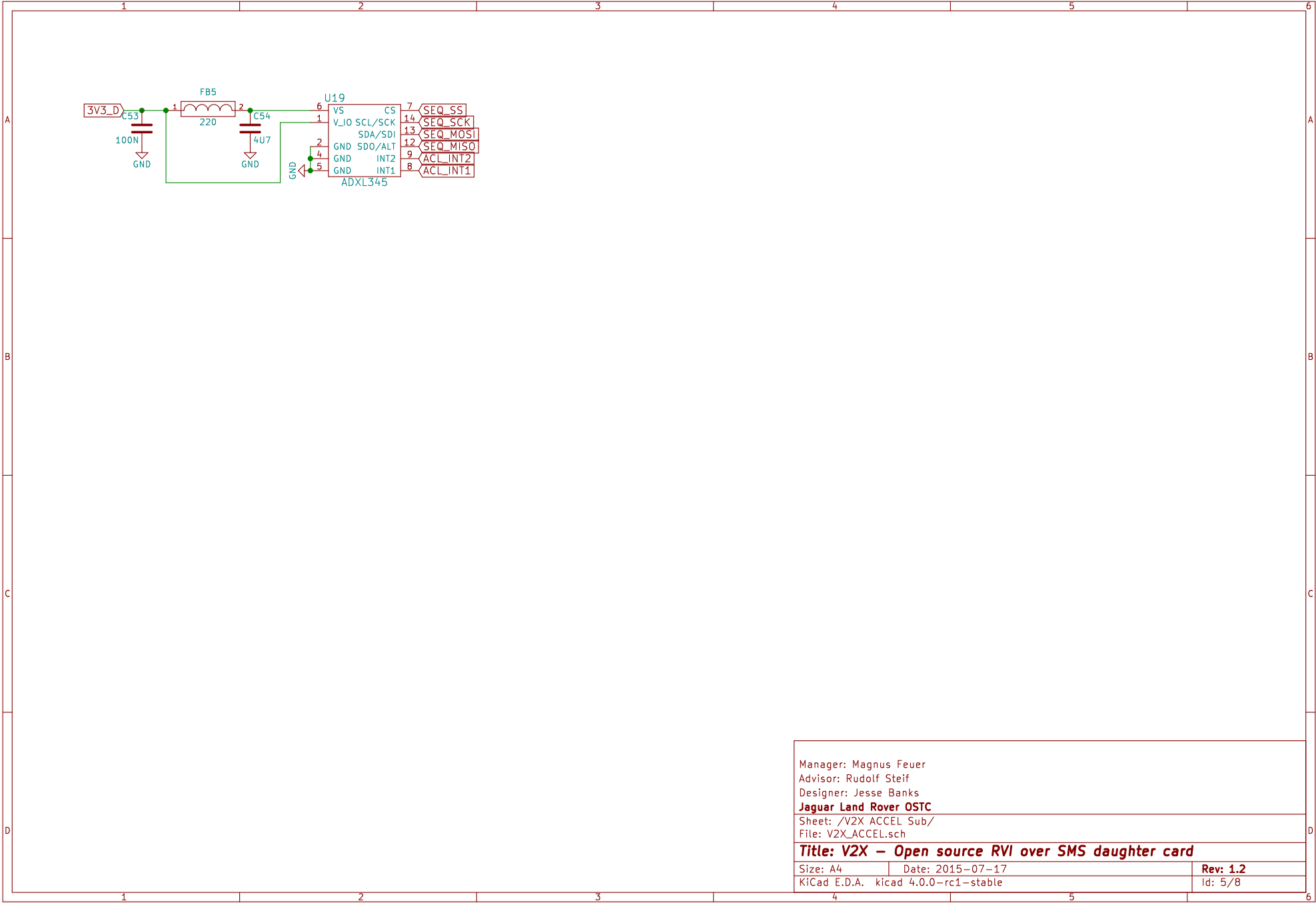
**Title: V2X – Open source RVI over SMS daughter card**

Size: USLetter Date: 2015-07-17

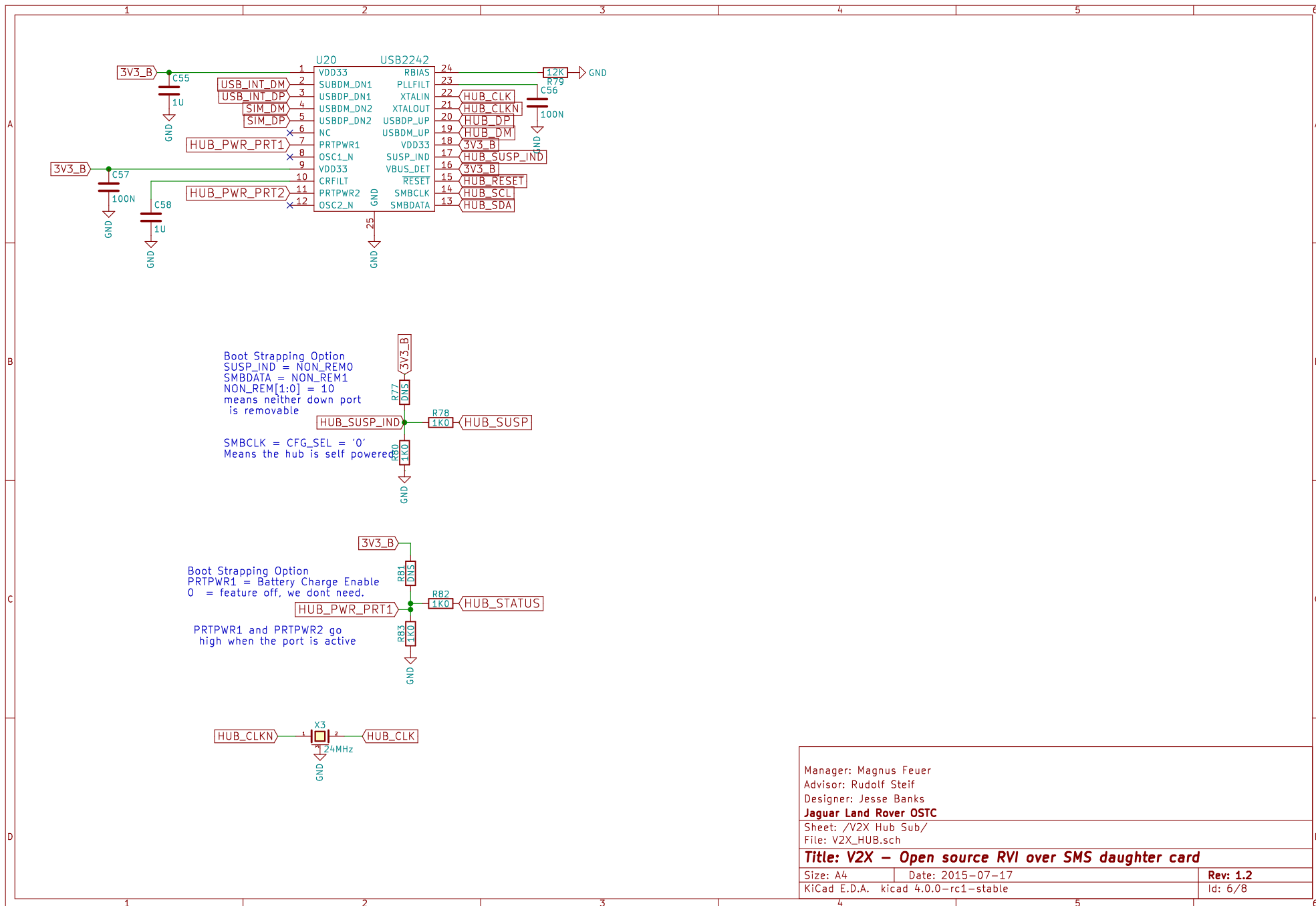
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Advisor: Rudolf Steif		
Designer: Jesse Banks		
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Sheet: /V2X ACCEL Sub/		
File: V2X_ACCEL.sch		
Title: V2X – Open source RVI over SMS daughter card		
Size: A4	Date: 2015-07-17	Rev: 1.2
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Sheet: /V2X Hub Sub/  
 File: V2X\_HUB.sch

**Title: V2X – Open source RVI over SMS daughter card**

Size: A4 Date: 2015-07-17

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