WiLamp-WiCL
Customer: WI4B

Rev: V1.0
10/08/2020

Revision History
a3_revision_history.SchDoc

TopDsnBlk
a4_top_dsn.SchDoc

RELEASED 28-OCTOBER-2020

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Date: 10/28/2020	Engineer: JBayalas	Wi4B S.r.l

DOC: REVISION HISTORY

Revision	Changes	Engineer
1.0	First Release.	AAsodisen

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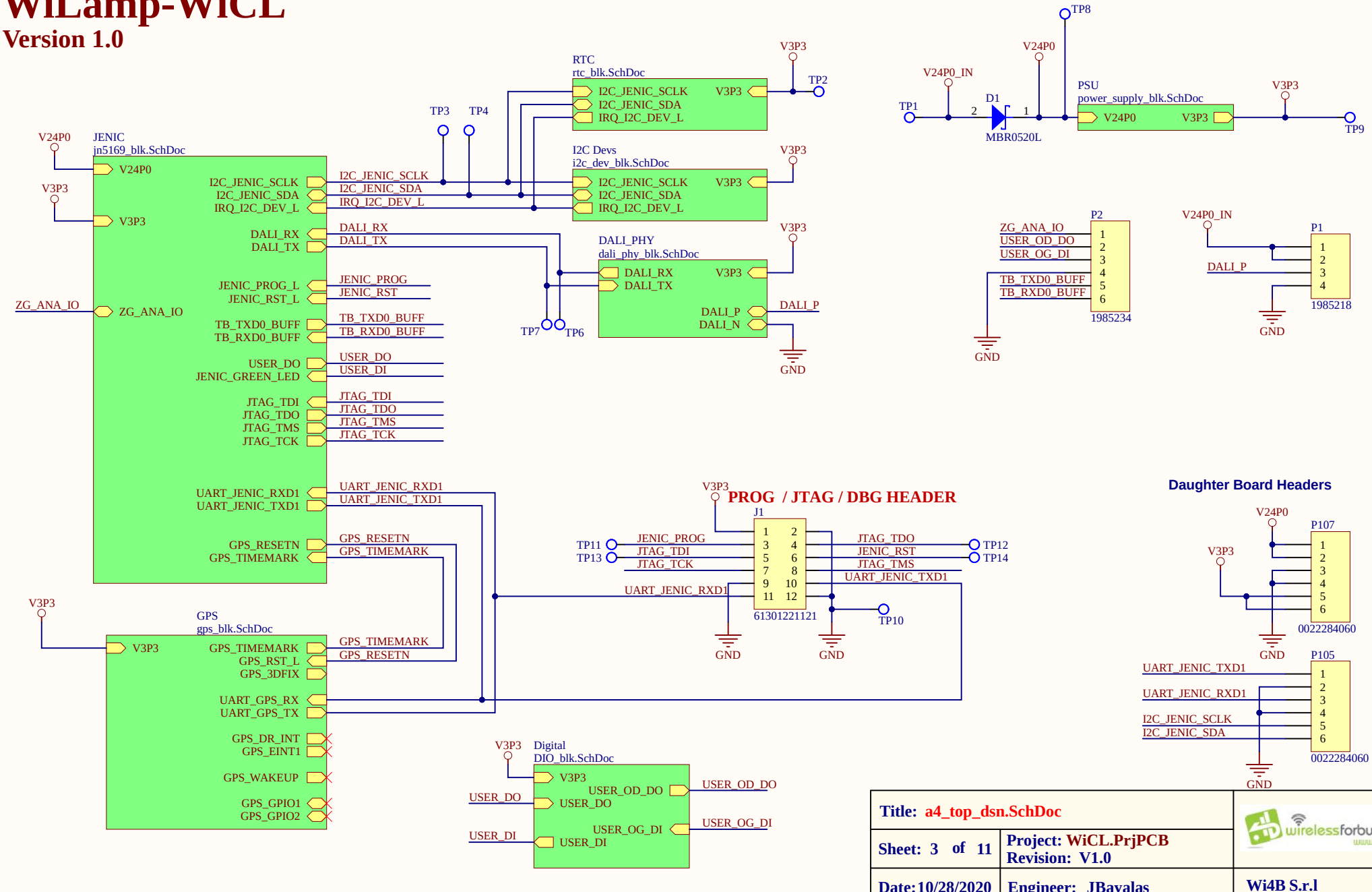
WiLamp-WiCL

Version 1.0



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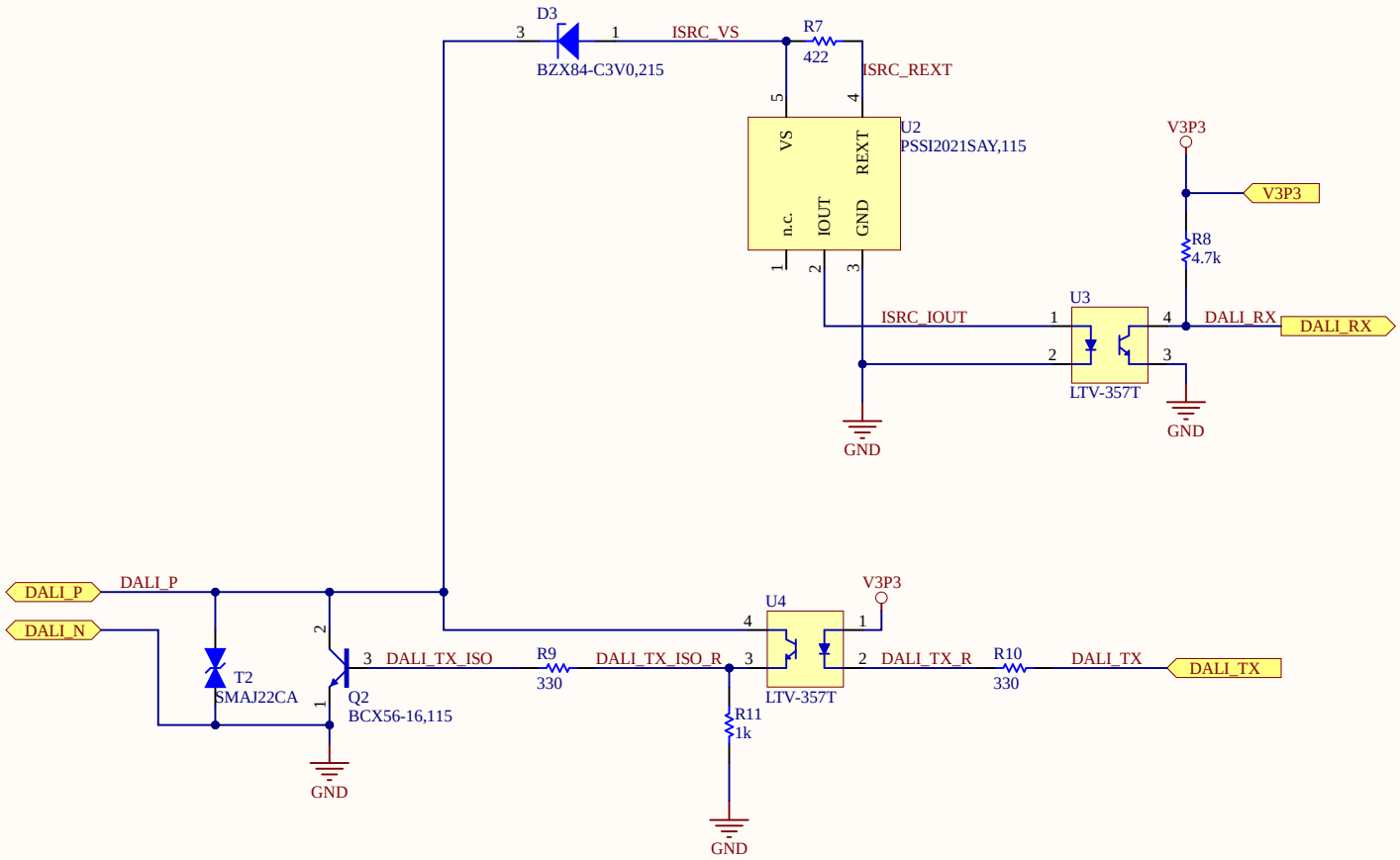
DALI PHY Block



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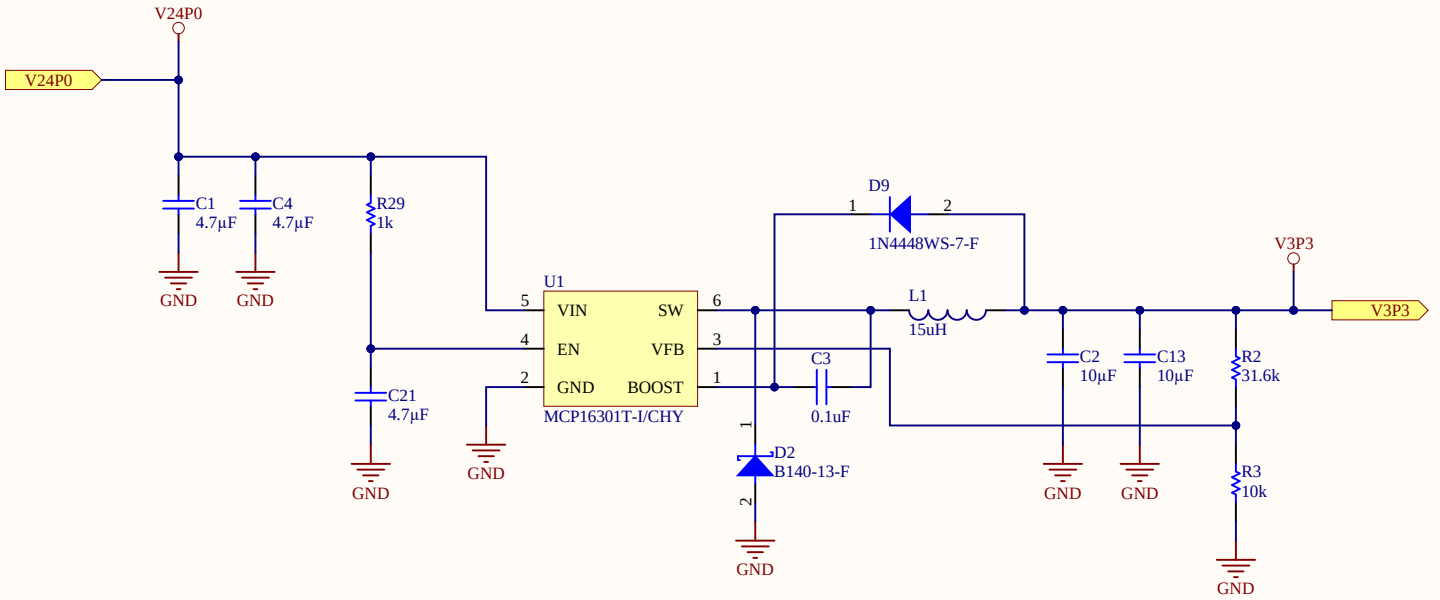

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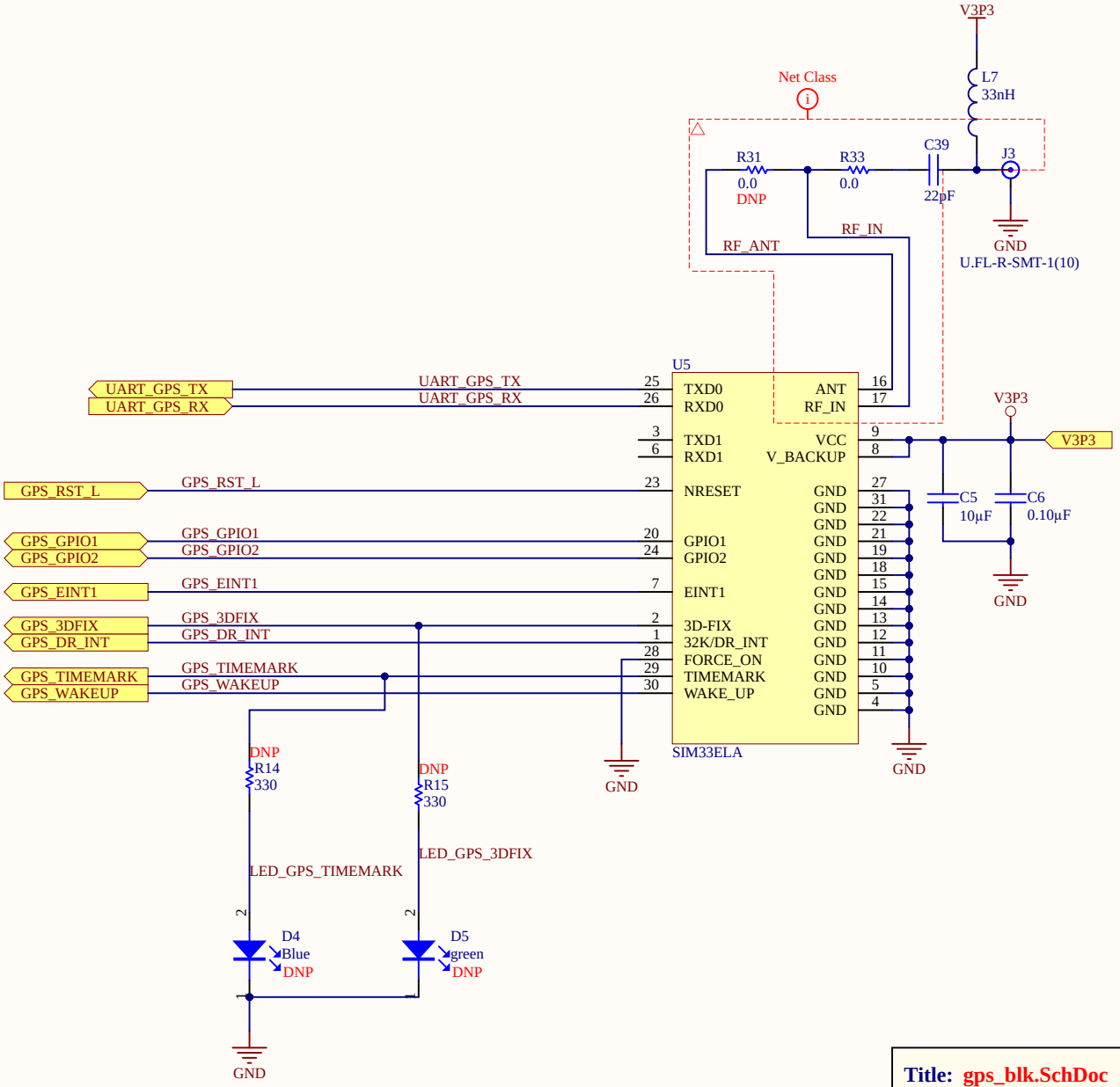
GPS



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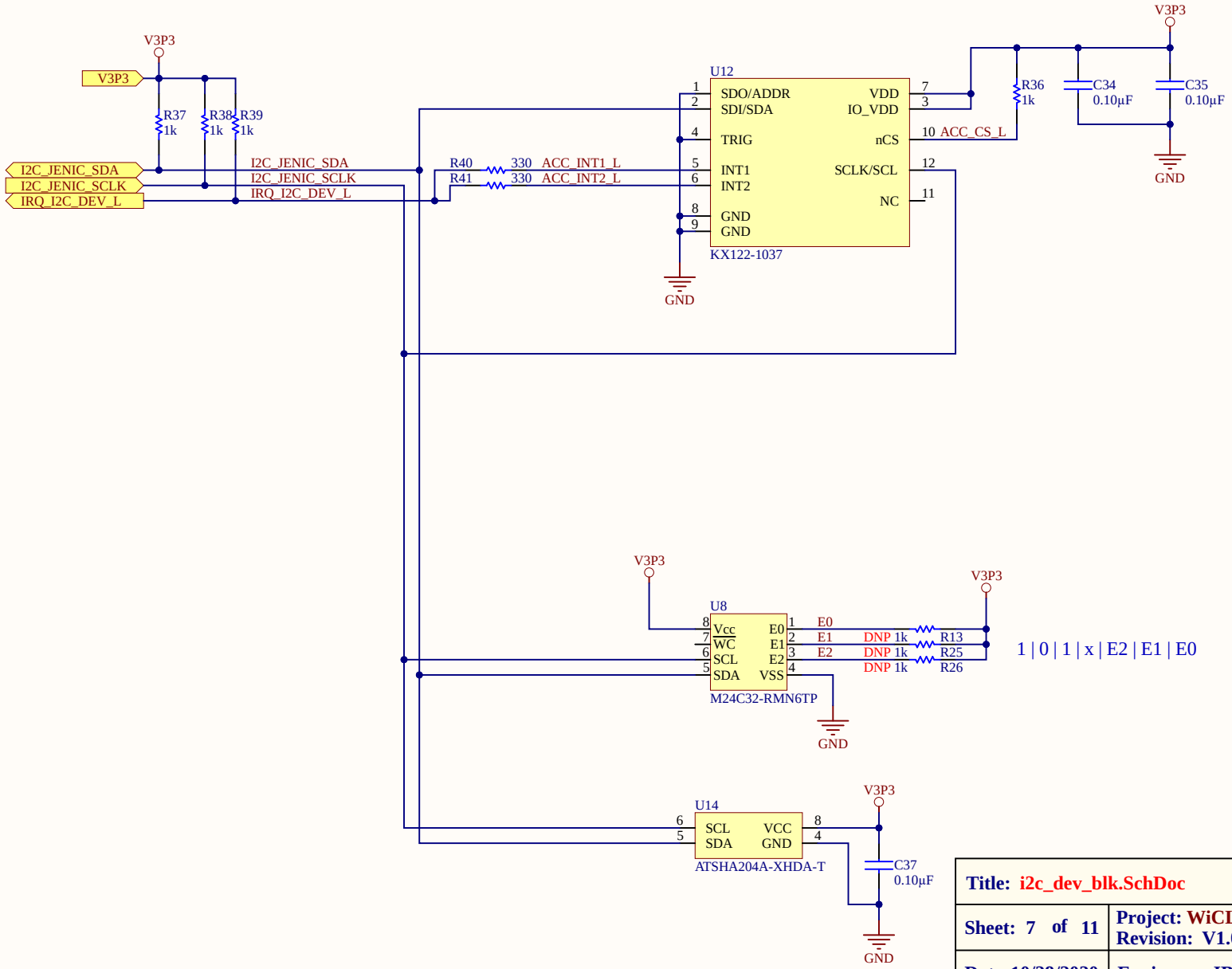
I2C Devices



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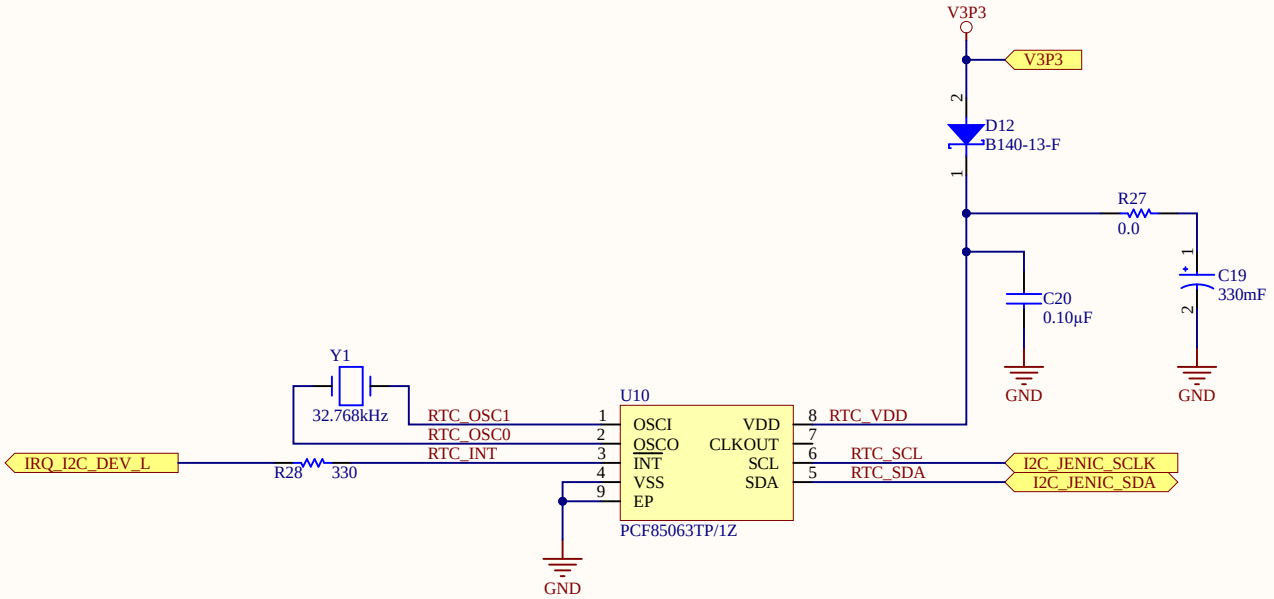
RTC



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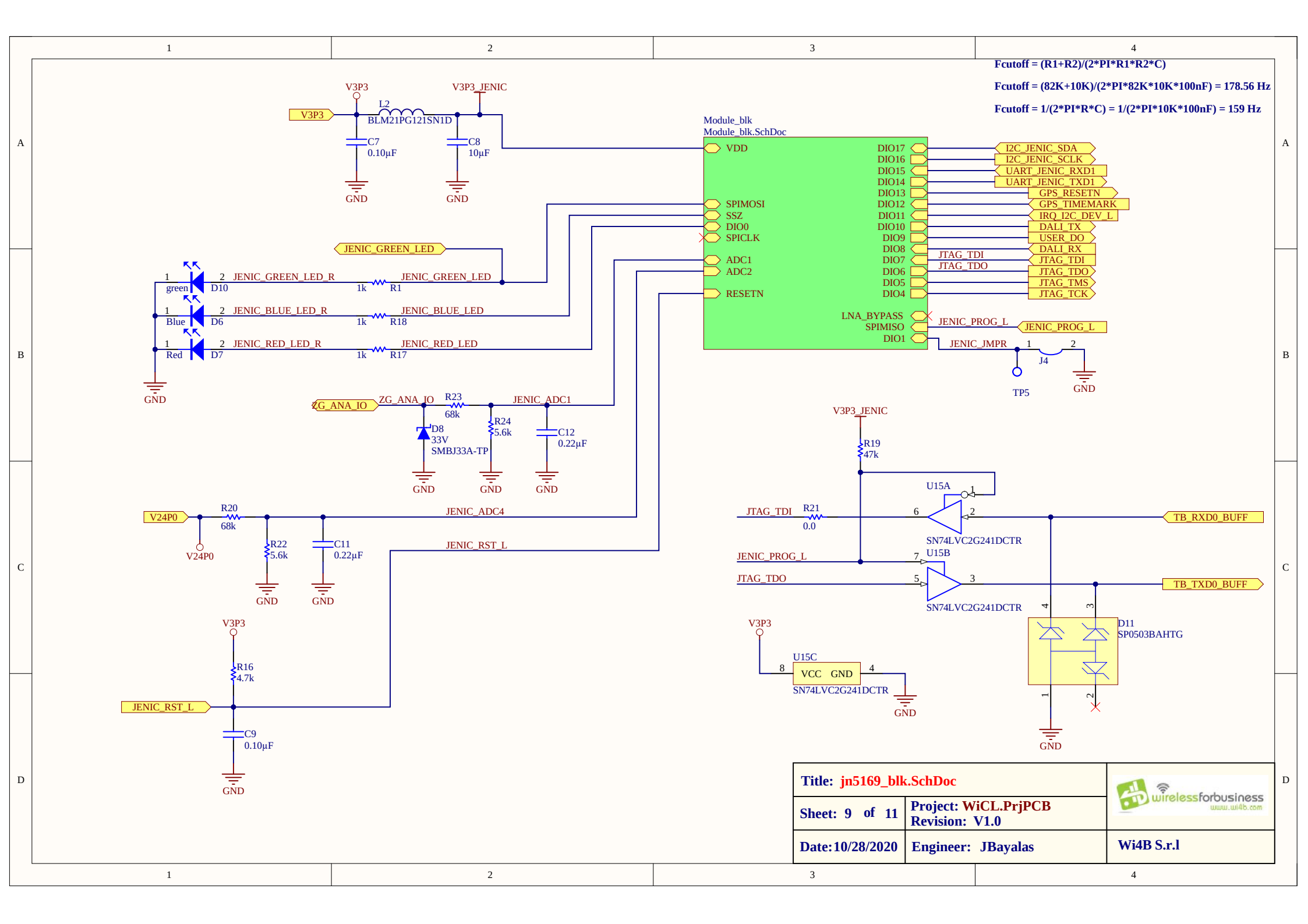
Fcutoff = (R1+R2)/(2*PI*R1*R2*C)
Fcutoff = (82K+10K)/(2*PI*82K*10K*100nF) = 178.56 Hz
Fcutoff = 1/(2*PI*R*C) = 1/(2*PI*10K*100nF) = 159 Hz
V3P3
V3P3_JENIC
L2
BLM21PG121SN1D
C7
0.10µF
C8
10µF
GND
Module_blk
Module_blk.SchDoc
VDD
SPIMOSI
SSZ
DIO0
SPICLK
ADC1
ADC2
RESETN
DIO17
DIO16
DIO15
DIO14
DIO13
DIO12
DIO11
DIO10
DIO9
DIO8
DIO7
DIO6
DIO5
DIO4
LNA_BYPASS
SPIMISO
DIO1
I2C_JENIC_SDA
I2C_JENIC_SCLK
UART_JENIC_RXD1
UART_JENIC_TXD1
GPS_RESETN
GPS_TIMEMARK
IRQ_I2C_DEV_L
DALI_TX
USER_DO
DALI_RX
JTAG_TDI
JTAG_TDO
JTAG_TMS
JTAG_TCK
JENIC_PROG_L
JENIC_JMPR
J4
TP5
JENIC_GREEN_LED
JENIC_GREEN_LED_R
JENIC_BLUE_LED_R
JENIC_RED_LED_R
green
Blue
Red
D10
D6
D7
1k
R1
R18
R17
ZG_ANA_IO
R23
68k
JENIC_ADC1
D8
33V
SMBJ33A-TP
R24
5.6k
C12
0.22µF
V24P0
R20
68k
R22
5.6k
C11
0.22µF
JENIC_ADC4
JENIC_RST_L
R16
4.7k
C9
0.10µF
R19
47k
U15A
U15B
U15C
JTAG_TDI
JENIC_PROG_L
JTAG_TDO
R21
0.0
SN74LVC2G241DCTR
VCC
GND
TB_RXD0_BUFF
TB_TXD0_BUFF
D11
SP0503BAHTG
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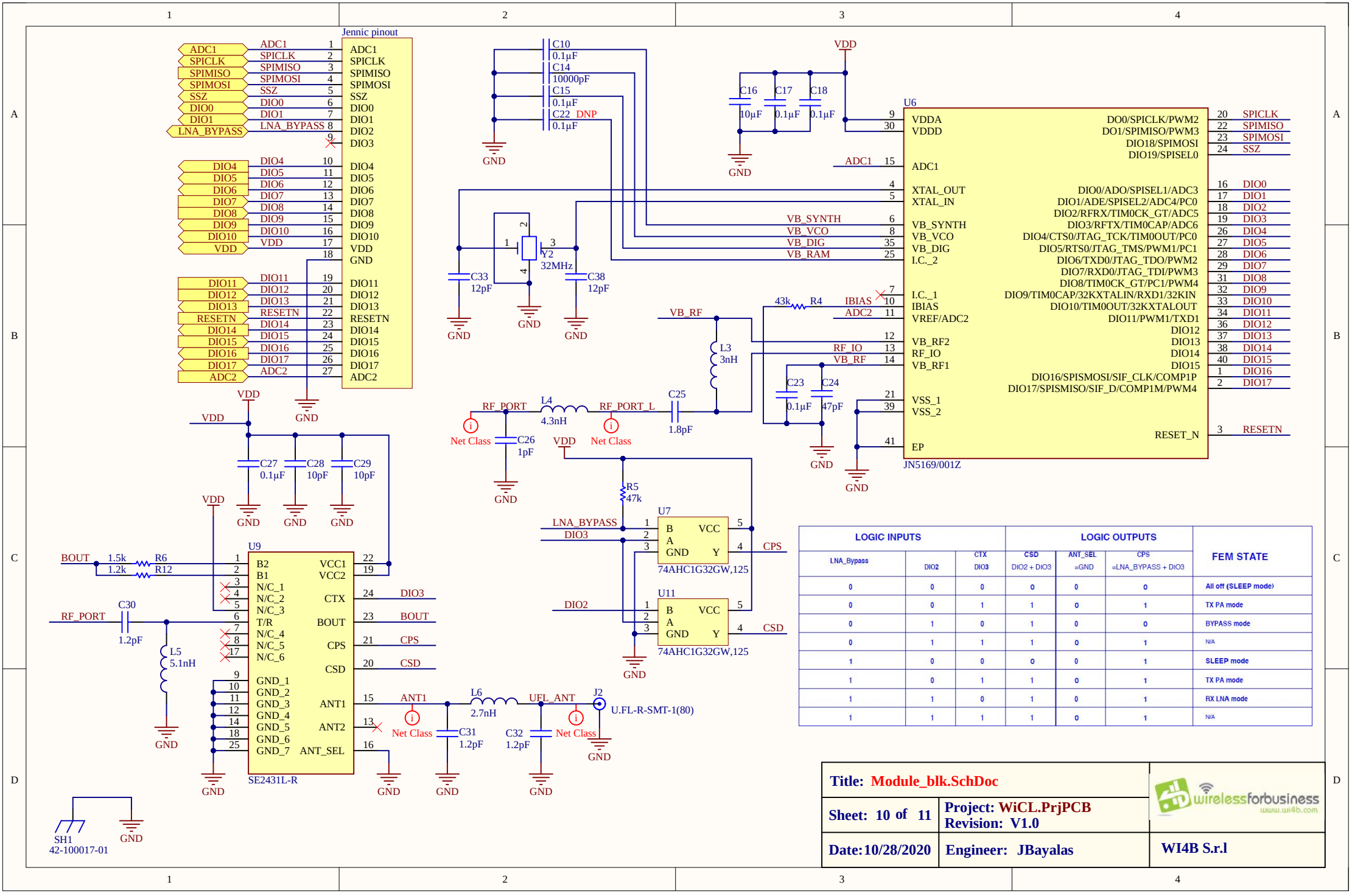

LOGIC INPUTS			LOGIC OUTPUTS			FEM STATE
LNA_Bypass	DIO2	CTX DIO3	CSD DIO2 + DIO3	ANT_SEL =GND	CPS =LNA_BYPASS + DIO3	
0	0	0	0	0	0	All off (SLEEP mode)
0	0	1	1	0	1	TX PA mode
0	1	0	1	0	0	BYPASS mode
0	1	1	1	0	1	N/A
1	0	0	0	0	1	SLEEP mode
1	0	1	1	0	1	TX PA mode
1	1	0	1	0	1	RX LNA mode
1	1	1	1	0	1	N/A

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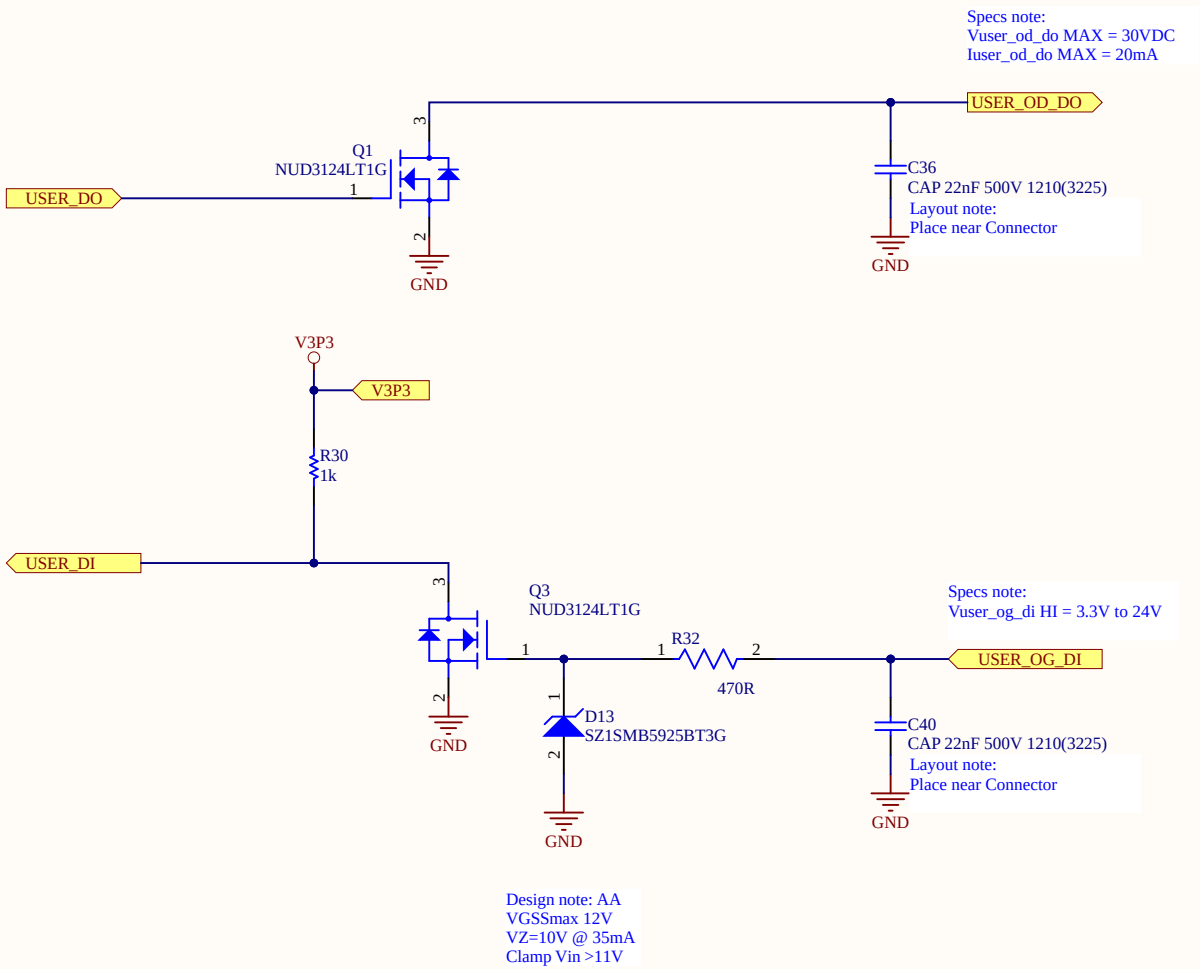
Digital Input Output



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