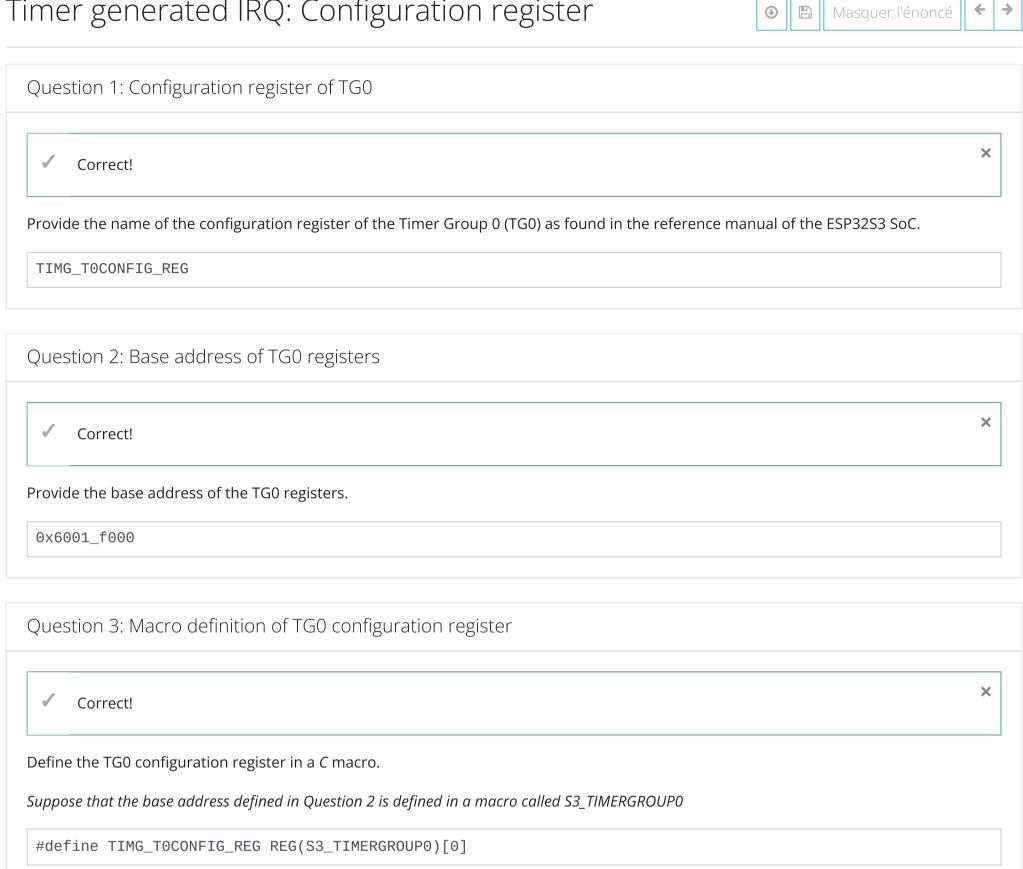




Timer generated IRQ: Configuration register



Question 4: Timer configuration X Correct! Which bit do you have to set in order to use XTAL_CLK and increase the timer counter? Enter the bit positions as a list of numbers separated by a single space. Order them in increasing order (i.e. from LSB to MSB) 9 30

Soumettre