



Timer generated IRQ: Alarm and Interrupt



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Question 1: Timer alarm register offset (higher bits)

✓ Correct!



Provide the offset of the timer value (higher bits) reload register.

Hint: the answer is expected in decimal. This should be the value you would use in a macro definition.

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Question 2: Timer alarm register definition (lower bits)

✓ Correct!



Define the TG0 configuration register in a C macro.

```
#define TIMG_T0ALARMLO_REG REG(S3_TIMERGROUP0)[4]
```

Question 3: Enabling the TG0 alarm

✓ Correct!



Write C code enabling the TG0 alarm according to the specification of the ESP32s3 reference manual.

```
TIMG_T0CONFIG_REG |= BIT(10);
```

Question 4: Interrupt configuration register of Timer0 (from TG0)

✓ Correct!



Define in a C macro the interrupt configuration register of Timer0 of TG0.

```
#define TIMG_INT_ENA_TIMERS_REG REG(S3_TIMERGROUP0)[28]
```

Question 5: TG0 interrupt activation

✓ Correct!



Write C code enabling the TG0 interrupt according to the specification of the ESP32s3 reference manual.

```
TIMG_INT_ENA_TIMERS_REG |= BIT(0);
```

Soumettre