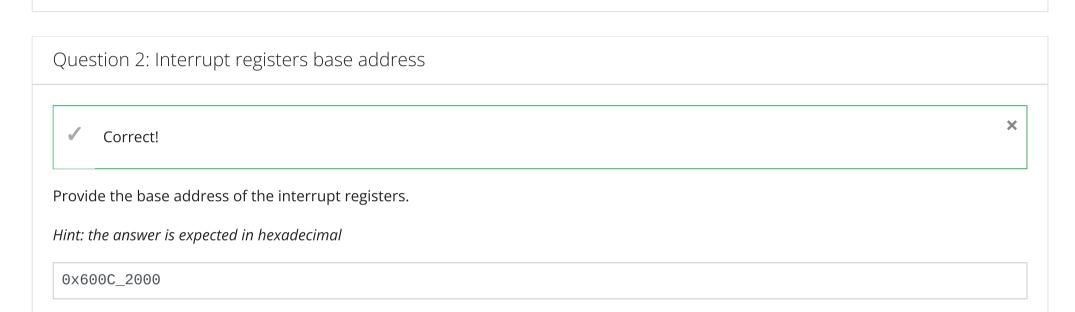


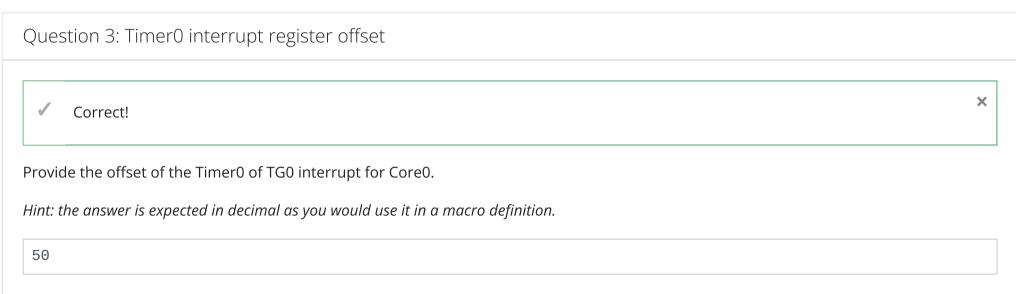
Correct!

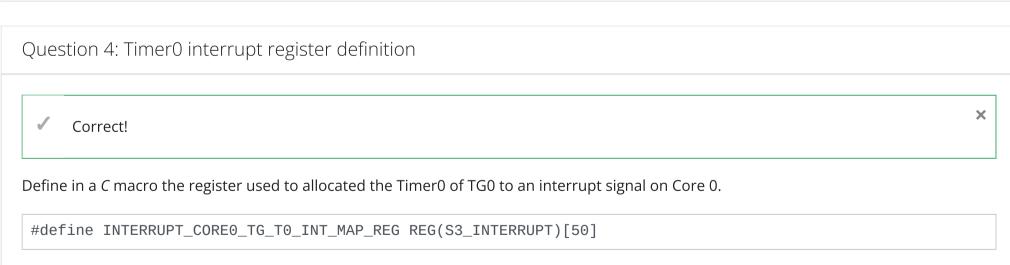


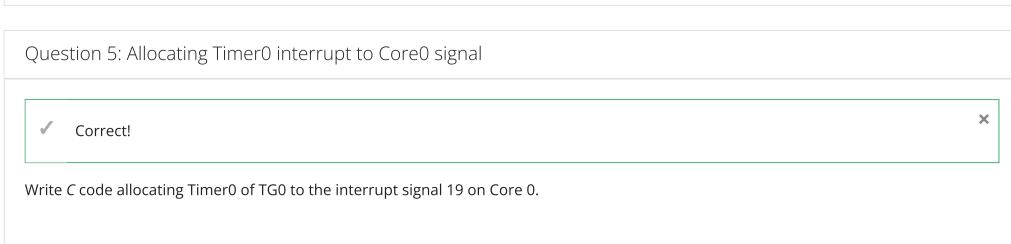
Provide the name of the register used to allocated the Timer0 of TG0 to an interrupt signal on Core 0.

INTERRUPT_CORE0_TG_T0_INT_MAP_REG









INTERRUPT_CORE0_TG_T0_INT_MAP_REG = 19;

Soumettre

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