

```
1  /*
2   * interrupt_echo.c
3   *
4   * Created: 3/18/2021 7:00:09 PM
5   * Author : Judah Ben-Eliezer
6   */
7
8  #define BAUD_RATE 4800UL // ↗
9      baud rate.
10
11 #define F_CPU 4000000UL // ↗
12     clock at 4 MHz.
13
14
15 #include <avr/io.h>
16 #include <util/delay.h>
17 #include <avr/interrupt.h>
18
19
20 uint8_t USART_sw_read(); // ↗
21     read function declaration.
22
23 void USART_sw_write(char); // ↗
24     write function declaration.
25
26
27 char c;
28
29 int main(void)
30 {
31     PORTB.DIRCLR = PIN1_bm; // ↗
32     set PB1 as input.
33
34     PORTB.PIN1CTRL |= PORT_ISC_FALLING_gc; // ↗
35     enable interrupt on falling edge of PB1.
36
37     sei(); // ↗
38     enable global interrupts.
39
40     while (1)
41     {
42         asm volatile ("nop"); // ↗
43         nop to avoid optimization deletion of while loop.
44     }
45 }
46
47 ISR (PORTB_PORT_vect) {
48     c = USART_sw_read(); // ↗
49     call USART_sw_read.
50
51     USART_sw_write(c - 0x20); // ↗
52     write uppercase c.
53
54     PORTB.INTFLAGS |= PIN1_bm; // ↗
55     clear interrupt.
56 }
57
58 uint8_t USART_sw_read() {
```

```

39     uint8_t d; // 7
40     bit time.
41     if (BAUD_RATE == 4800UL) {
42         d = 48;
43     } else if (BAUD_RATE == 9600UL) {
44         d = 99;
45     } else if (BAUD_RATE == 19200UL) {
46         d = 201;
47     } else return 0x00;
48
49     uint8_t data = 0;
50
51     _delay_us(d/2);
52     if ((PORTB_IN & PIN1_bm) != 0) return 0x00; // 7
53     check for false start.
54     _delay_us(d); // 7
55     delay for bit time.
56
57     uint8_t i;
58     for (i = 0; i < 8; ++i) {
59         data >>= data | ((PORTB_IN | PIN1_bm) << 6); // 7
60         read little endian input into data.
61         _delay_us(d); // 7
62         delay for bit time.
63     }
64
65     return data;
66 }
67
68 void USART_sw_write(char c) {
69     PORTB_DIRSET = PIN0_bm; // 7
70     set PB0 as output.
71     uint8_t d; // 7
72     bit time.
73     if (BAUD_RATE == 4800L) {
74         d = 48;
75     } else if (BAUD_RATE == 9600L) {
76         d = 99;
77     } else if (BAUD_RATE == 19200L) {
78         d = 201;
79     } else return;
80
81     uint8_t data = (uint8_t) c;
82
83     PORTB_OUT = 0x00 | PIN0_bm; // 7
84     send start bit.
85     _delay_us(d); // 7
86     delay for bit time.

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79  
80     uint8_t i;  
81     for (i = 0; i < 8; ++i) {  
82         PORTB_OUT = data | PIN0_bm; // ↗  
            send lsb of data.  
83         data >>= data; // ↗  
            shift data right.  
84         _delay_us(d); // ↗  
            delay for bit time.  
85     }  
86  
87     PORTB_OUT = PIN0_bm; // ↗  
        send end bit.  
88     _delay_us(d); // ↗  
        delay for bit time.  
89 }  
90  
91
```