

RAMP

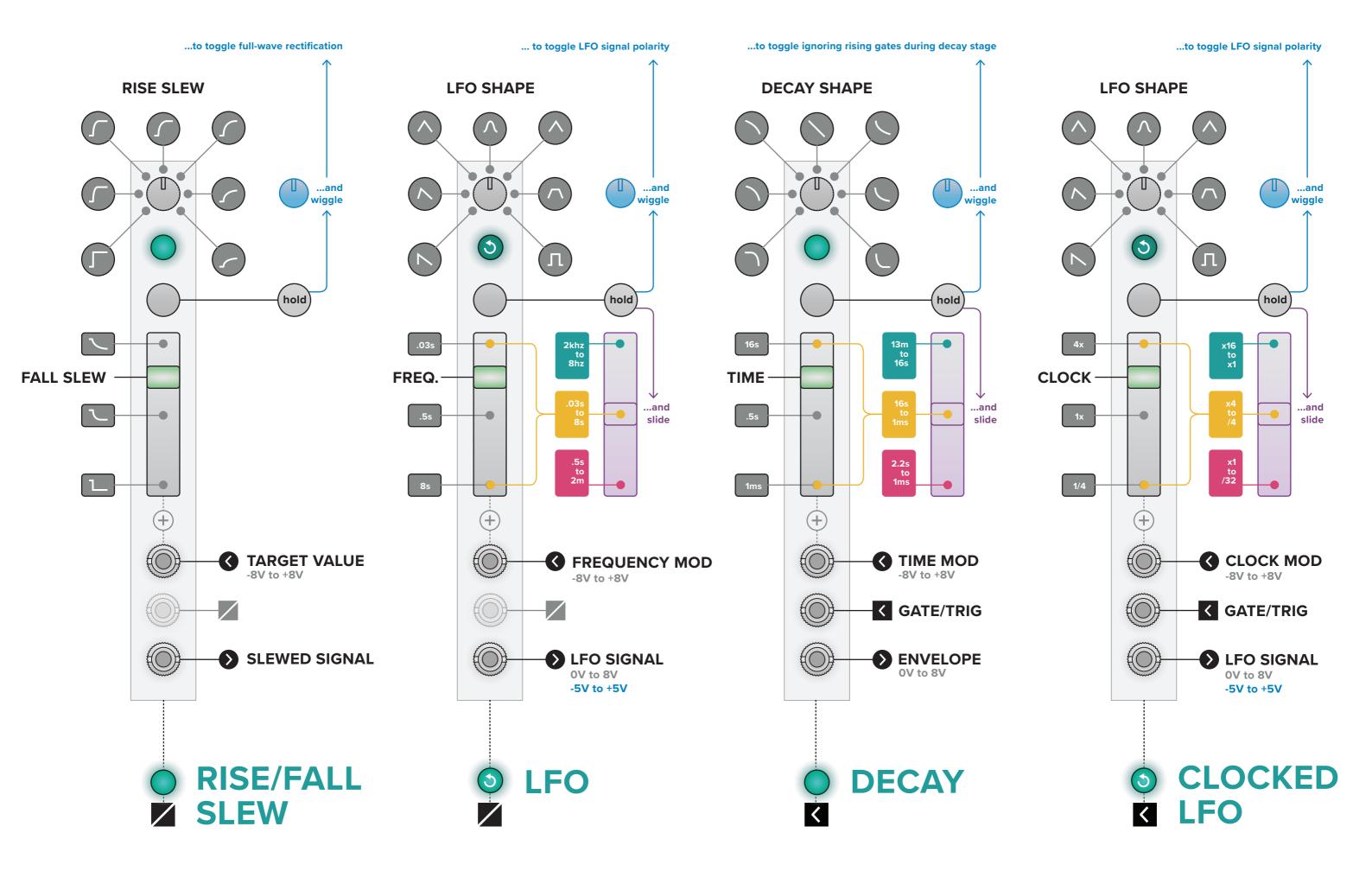
STEP

HOLD

SHIFT REG.

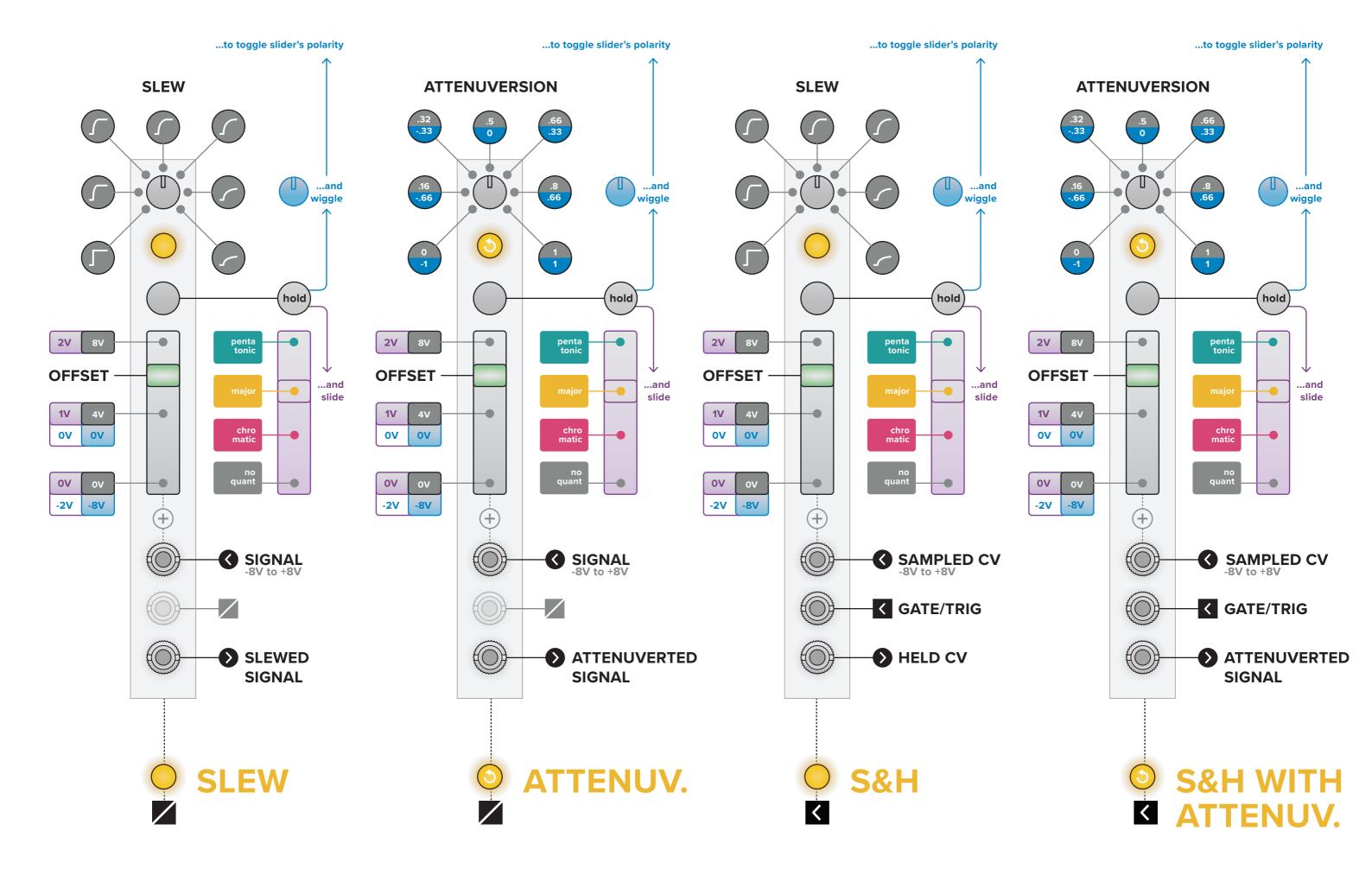






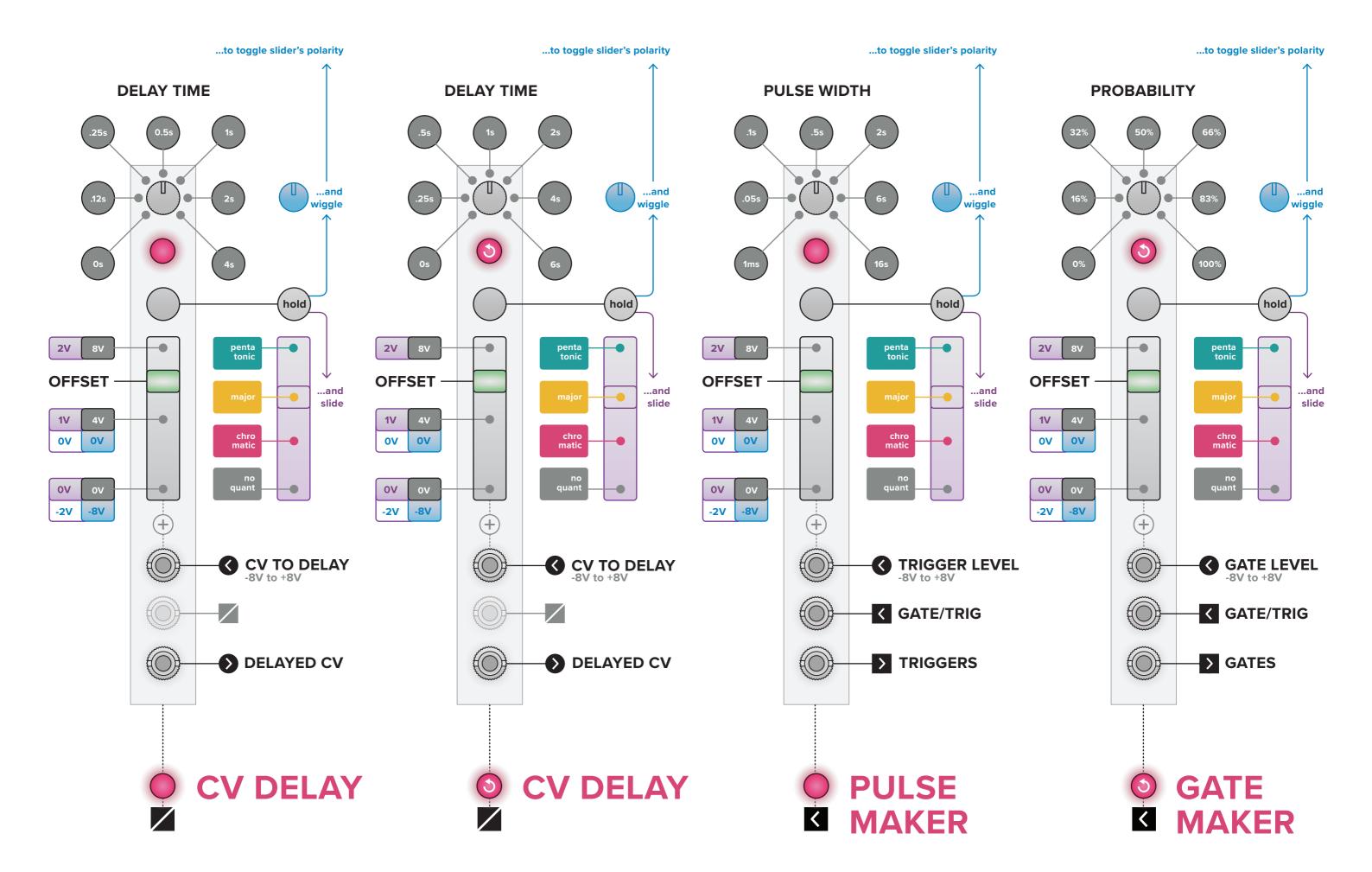






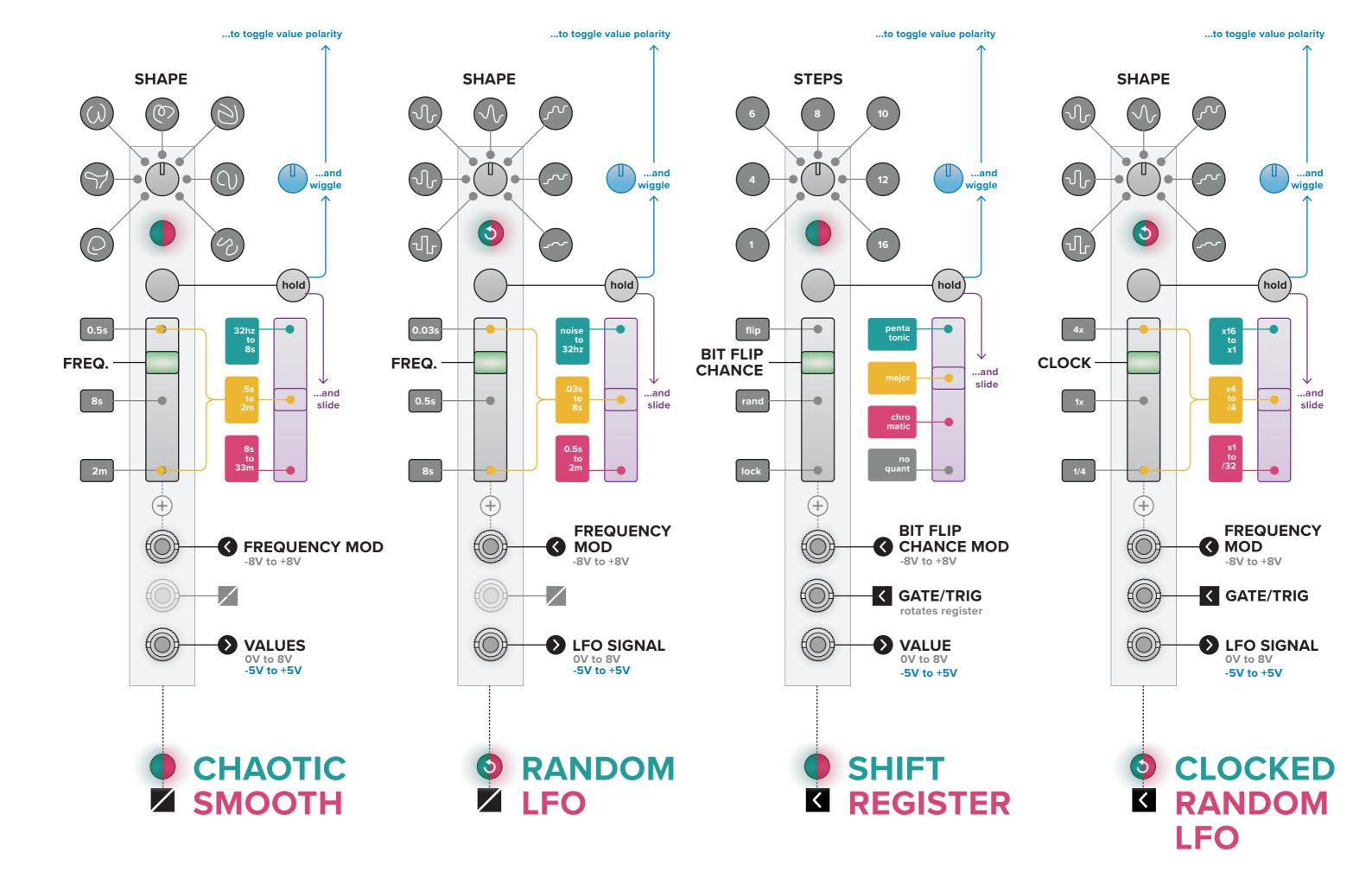


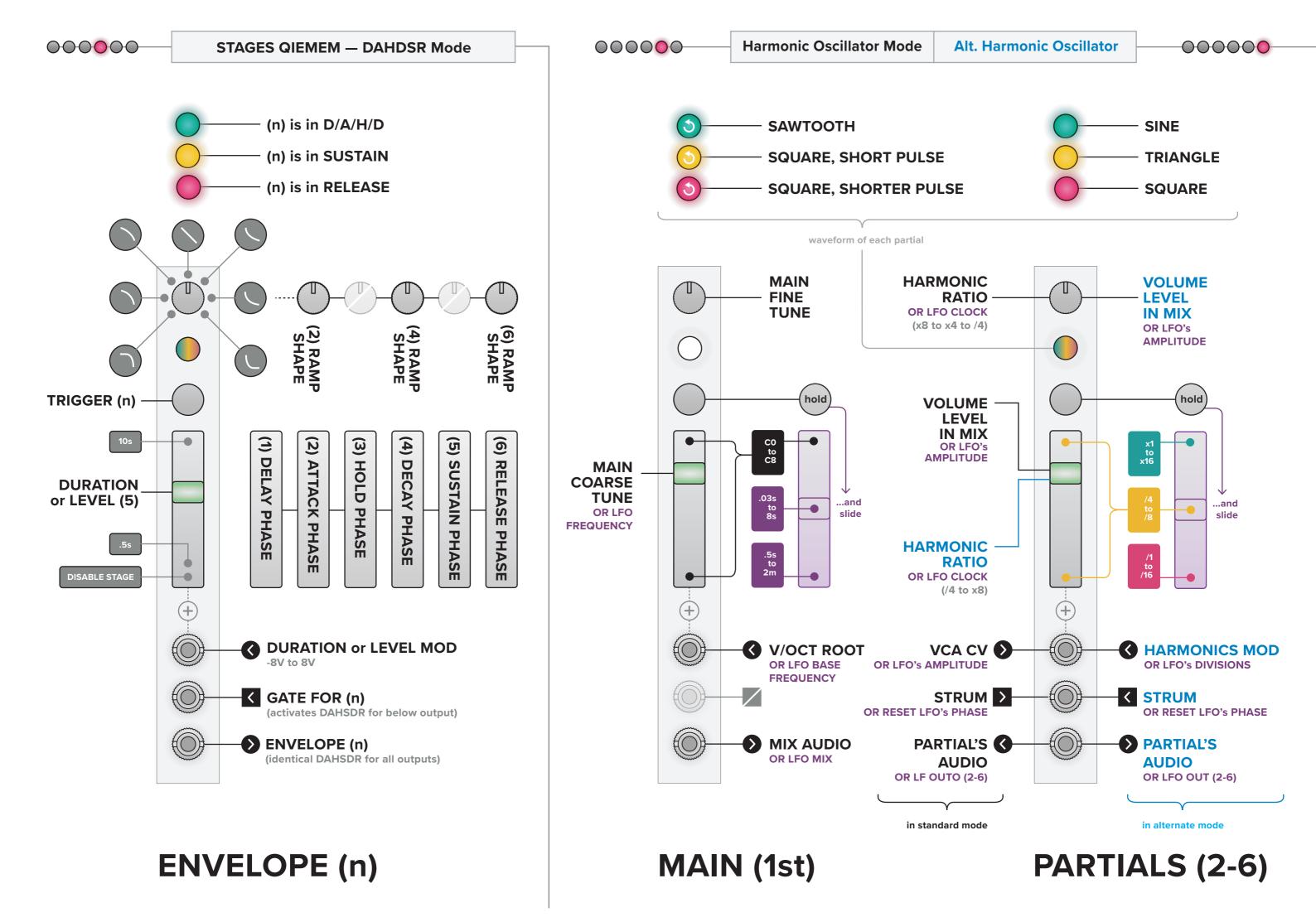












AD envelope

AR envelope

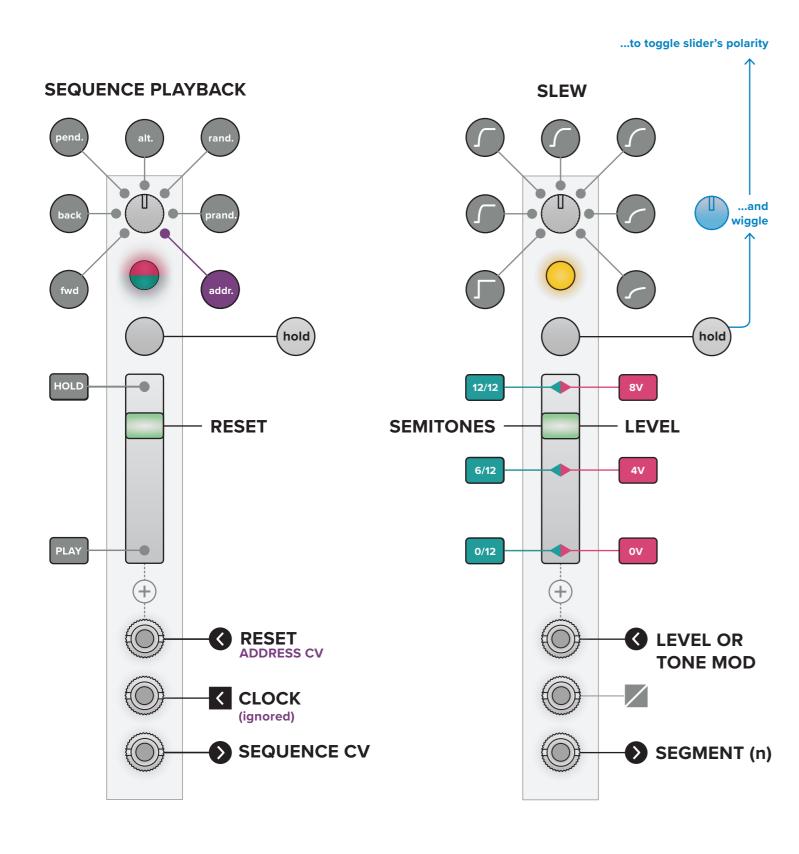
ADR envelope

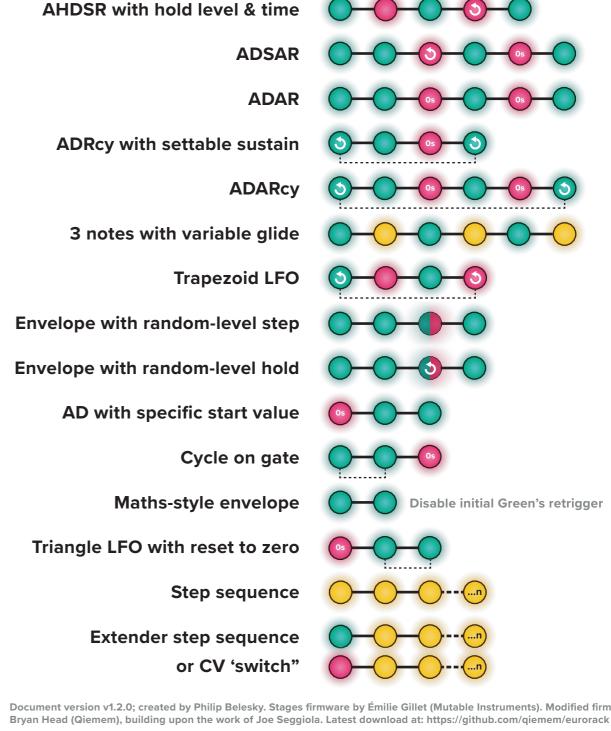
ADSR envelope

ADSR with pre-delay & start level

ADSR with non-zero rest level







CONTROL (1st) STEP (n)