

Address

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Mail

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outlook.com
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Project Profiles

github.com/
jbharter
git.uarkeleg.com/
jbharter

Programming

Software Languages

Java 8, Scala, ES6,
C++

Distributed

Hadoop, Spark

Internet Languages

HTML, CSS,
JavaScript, PHP,
MySQL

Cloud

Google Cloud
Platform, Microsoft
Azure

Hardware Languages

Assembly, VHDL

Electrical Analysis

Cadence EDA,
Powerworld

Systems

Administration

Microsoft AD/DS,
Open LDAP, Samba

OS Proficiency

OSX
Linux
Windows

Jacob Harter

Software Engineer & Google Cloud Architect

Experience

- 01/17 - **Software Engineer, Analyst** [Acxiom Corp. \(LiveRamp, Oct 18\), Conway, Arkansas](#)
Data Services API *A low latency entity resolution platform* Worked with a geographically diverse group of engineers to develop and grow the Data Service API. Sub-projects encompassed a diverse range of technologies and responsibilities, from developing connectors to deploy legacy code in modern distributed computed frameworks, such as Apache Hadoop, and Apache Spark, to expanding the capabilities of the API using the Spring framework.
- 02/16 - 12/16 **Senior Design Project Member** [Pinnacle Foods Group, Fayetteville, Arkansas](#)
Exception Detection and Handling-Process Automation *A project to design and develop a system to detect production issues, and reject them before they cause damage.*

Certifications

- 07/27/2018 **Google Cloud Certified Professional Cloud Architect** [Google Cloud](#)
Through an understanding of cloud architecture and Google technology, this individual can design, develop, and manage robust, secure, scalable, highly available, and dynamic solutions to drive business objectives.

Projects

- **ML Classification Engine** From gathering the data via myriad sources, to defining an ingest process, and creating a classifier trained to classify names based on intrinsic patterns of characters using the ML library of Apache Spark.
- **Distributed SPICE** Defining a build process to gather and compile the requirements and sources of Sandia Labs, Xyce software. Long term goal is to build a more portable parallelization mechanism to speed circuit analysis.

Education

- 2016 **B.S. Electrical Engineering** [University of Arkansas, Fayetteville, Arkansas](#)
Electrical Engineering
Notable Classes: Digital Signal Processing System Design, Digital Design, Senior Design, Energy Systems, Control Systems
- 2008 - 2012 **High School** [Little Rock, Arkansas](#)
Catholic High School

Awards

- 05/2012 **Eagle Scout** [Boy Scouts of America](#)
Troop 007, St. Marks Church, Little Rock AR
- 05/2012 **Rector's Award** [Catholic High School](#)
"For exceptional devotion to school, church, and community"