

Samsung Class 100 eMCP Solution

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Nov, 2012

Memory Sales & Marketing Samsung Memory

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Class100 eMCP Introduction



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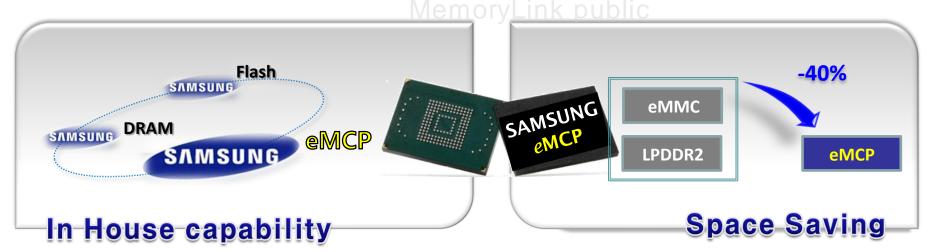
Intro



What is eMCP?



- **■eMCP**: Multi-Chip Package with eMMC
- **■Why Samsung eMCP?**
 - Combine ROM and RAM: Single supplier (Samsung Flash, Samsung DRAM)
 - •Small Space needed: Save up to 40% of space on memory b'd
- **■Which eMCP adoptable for Set Makers**
 - •ME : eMMC + LPDDR2 DRAM
 - •MD : eMMC + LPDDR1 DRAM
 - Both chipsets that can support LPDDR1 and LPDDR2 can use Samsung eMCP by Full Line-up support





Samsung eMMC Class Line-up

What is Class?

- Samsung eMMC Line-up naming based on 'Random write IOPS'
- Random IOPS is KEY factor for system memory operation
- Samsung Test Condition

Pre-condition	Test Environment & methodology			
Dirty Condition	1) Platform : eMMC device level, x8 bus width, 50MHz DDR 2) Random read/write Chunk size : 4KB*, Total I/O (1GB, 1GB range)			

^{* 4}KB data chunk size is file system meta size and main chunk size for operating system

Suitable solution for each Segmentation (12'Q4~13'1H Model)

Item	Class100		Class400		Class1500	Class2000
Smart Phone	ULC Low-end		nd	Mid-ra	nge High-ei	nd (Flagship)
Segment	Singl	e Core	Dual	Core	Quad Core	
Major Functions (Example)	Basic C	peration	Gaming, Multi-tasking, etc		Burst shooting with high quality pixel, 3D Multimedia playing, etc.	
OS Requirement	Android	*, Window			ne Android*, Windows Phone, Window8	

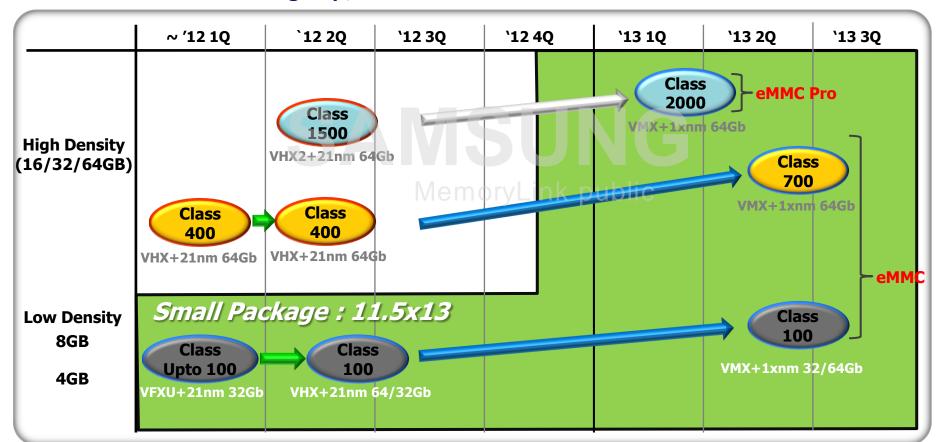
^{*} Android doesn't have special specification for Memory



Samsung eMCP Class Roadmap



- Samsung has wide range of eMMC solution based on performance requirements
 - eMMC for Low-end to High-end with Class 100 to Class 700
 - eMMC Pro for Flagship/Win8 PJT with Class 1500 to Class 2000





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Line-up



Samsung Class100 eMCP Line-up



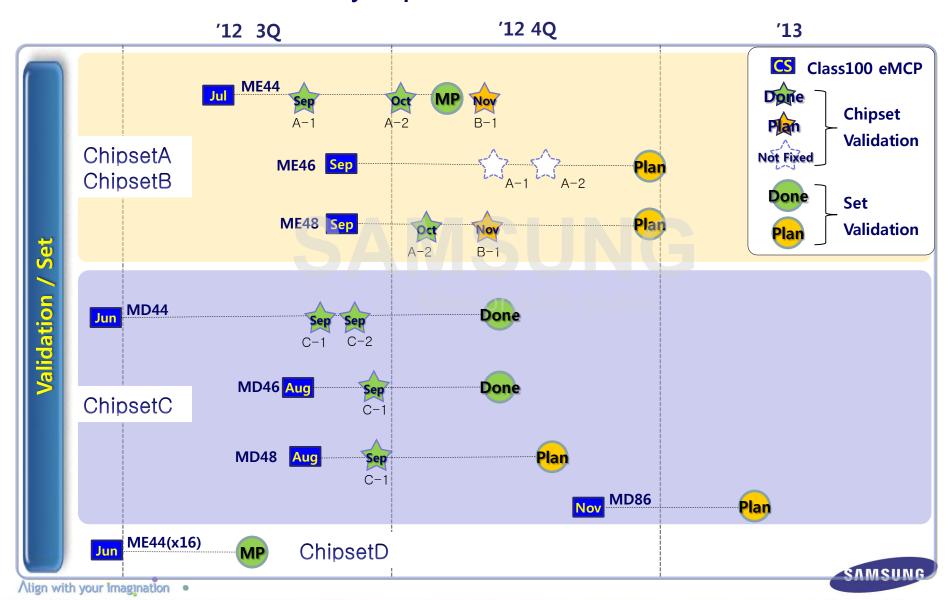
IF	еМСР	Package	Part#	Patch	Status	Validation on Chipset
	MD4GB_4G	153B, 11.5x13x1.0mmt	KMS5U000KM-B308	02	CS	Done
MDDR	MD4GB_6G	153B, 11.5x13x1.0mmt	KMJ5U000WA-B409	02	CS	Done
based	MD4GB_8G	153B, 11.5x13x1.2mmt	KML5U000HM-B505	02	CS	Done
	MD8GB_6G	153B, 11.5x13x1.2mmt	MSUN	02	CS : Nov. '12	(Not Planned Yet)
	ME4GB_4G (x16)	162B, 11.5x13x1.0mmt	KMN5U000FM-B203		MP	Done
	ME4GB_4G (x32)	162B, 11.5x13x1.0mmt	KMN5U000ZM-B203	02	MP	Done
LPDDR2 based	ME4GB_6G (x32)	162B, 11.5x13x1.0mmt	KMK5U000YM-B309	02	CS	(Not Planned Yet)
	ME4GB_8G (x32)	162B, 11.5x13x1.0mmt	KMK5U000VM-B309	02	CS	Done
A Kanan a JAL	ME8GB_8G (x32)	162B, 11.5x13x1.0mmt	-	-	CS : Feb. '12	(Not Planned Yet)

Align with your imagination •

Samsung Class 100 Validation Status



Class100 eMCP is validated by Chipset and Set Makers



Smart Phone with eMMC Class100



■ ICS Smart Phone MP with eMCP ME4GB4G(eMMC Class100)

	Galaxy Chat Specifications				
Maker	Samsung Mobile				
Chipset	BCM21654				
os	Android 4.0 ICS				
DRAM	Samsung eMCP ME4G4G				
Flash	(Samsung eMMC Class100 4GB)				
LCD Size	3.0 Inch				
Resolution	QVGA(240X320), 2MPixel				
Launching	'12 July				



Class100 eMCP Technical Review



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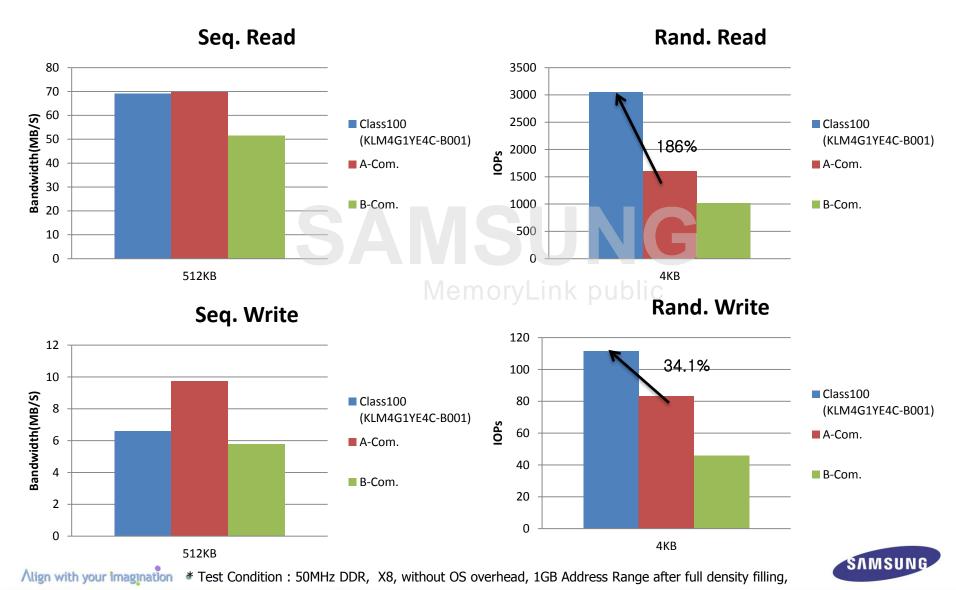
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Performance



eMCP Level Performance of eMMC Class 100 4GB CONFIDENTIAL

Class100 has competitive edge comparing to others



Set Level Performance Comparison Result



Test Condition

	AP	Memory	os
A Smart Phone	A-CPU	MD4GB4Gb	GB

Test Result

	Test Item	Description	`11Year [sec]	`12Year [sec]	Performance Gap	Test Environment
	File copy Write	Small file copy(Sec) Write (PC \rightarrow eMMC)	47.2	29.6	37.3%	225MB
Performance	File copy Write	Big file copy(Sec) Write (PC \rightarrow eMMC)	49.6	33.0	33.5%	316MB
Clean	File conv Dood	Small file copy(Sec) Read (eMMC \rightarrow PC)	e ^{21.2} /	19.4	Jb 8.5%	225MB
Precondition	File copy Read	Big file copy(Sec) Read (eMMC \rightarrow PC)	22.7	23.4	-3.1%	316MB
	Арр	Application install(Sec)	22.9	15.8	31.0%	101MB
	=:1:	Small file copy(Sec) Write (PC → eMMC)	55.1	56.3	-2.2%	225MB
Performance Dirty	File copy Write	Big file copy(Sec) Write (PC \rightarrow eMMC)	63.9	57.0	10.8%	316MB
Precondition (62 % Filled)	File servi Dead	Small file copy(Sec) Read (eMMC → PC)	16.4	14.8	9.8%	225MB
600MB remained	File copy Read	Big file copy(Sec) Read (eMMC → PC)	16.4	18.3	-11.6%	316MB
	Арр	Application install(Sec)	52.8	49.9	5.5%	101MB



Set Level Performance Comparison Result



Test Condition

	AP	Memory	OS
A Smart Phone	B-CPU	ME4GB4Gb	ICS

Test Result

	Test Item	Description	`11Year [sec]	`12Year [sec]	Performance Gap	Test Environment
	Eilo cony Writo	Small file copy(Sec) Write (PC \rightarrow eMMC)	43.7	36.0	17.6%	225MB
Performance	File copy Write	Big file copy(Sec) Write (PC \rightarrow eMMC)	41.8	37.3	10.8%	316MB
Clean	File conv Dood	Small file copy(Sec) Read (eMMC \rightarrow PC)	33.4	29.9	10.3%	225MB
Precondition	File copy Read	Big file copy(Sec) Read (eMMC → PC)	30.2	27.2	9.8%	316MB
	Арр	Application install(Sec)	25.2	14.0	44.5%	101MB

	File com Muite	Small file copy(Sec) Write (PC → eMMC)	43.9	39.4	10.3%	225MB
Performance Dirty	File copy Write	Big file copy(Sec) Write (PC → eMMC)	44.1	44.4	- 0.8%	316MB
Precondition (62 % Filled)	File conv Dood	Small file copy(Sec) Read (eMMC → PC)	33.2	29.9	9.9%	225MB
600MB remained	File copy Read	Big file copy(Sec) Read (eMMC → PC)	29.6	27.9	5.7%	316MB
	Арр	Application install(Sec)	22.5	15.5	31.1%	Racing Game (101MB)

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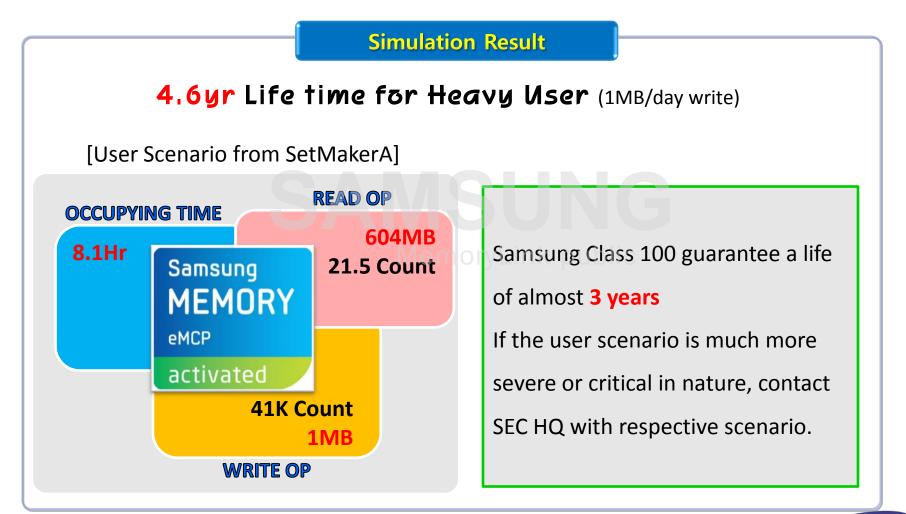
Endurance



User Scenario base Life Time of 4GB Class100



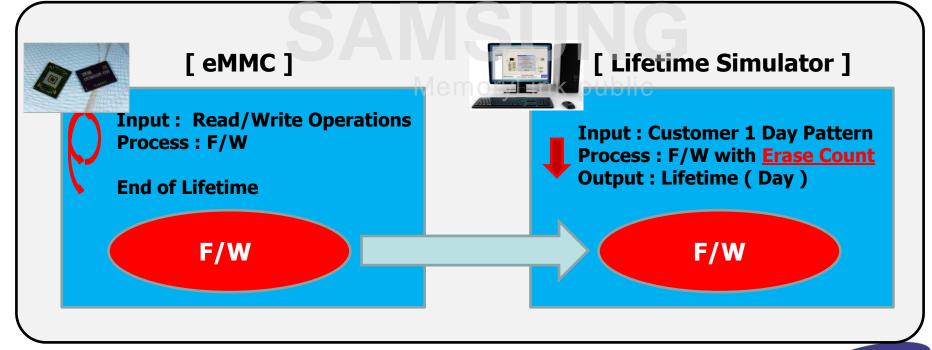
- Class100 eMCP can Endure for 3years following Simulation
 - User Scenario



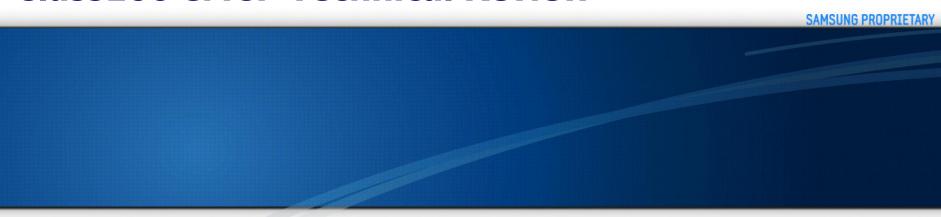
How to Simulate Lifetime



- Life time estimation of eMMC could be using Lifetime Simulator
- Simulator expects a day's work load pattern.
 - Lifetime of a third party vendor device cannot be simulated.
 - No Simulator for Other Vendor's or No Way to Know Erase Count



Class 100 eMCP Technical Review



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Solution for Each Display



Memory Recommendation: LPDDR1



- Line-up for various smart phone specification
 - Without DRAM POP, eMCP will support various DRAM Density
 - POP Stack Process is not needed



Memory Recommendation: LPDDR2



- Line-up for various smart phone specification
 - Without DRAM POP, eMCP will support various DRAM Density
 - POP Stack Process is not needed



Class100 eMCP Technical Review



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Validation Result



Chipset Joint Validation Result (Class 100 + Chipset

- eMCP Class100 4GB Validated on Popular platform (Chipset A)
 - Platform Info. : Chipset A + Android 4.0(ICS)
- By validating eMMC Class100 4GB on Chipset A, we've optimized performance related factors

#	Validation Item	Reference	Analysis Results	Comments
		AC/DC parameter	No issue	-
		Timing Parameter	No issue	
1	Pagis Analysis	Power up Cycling	No issue	5
1	Basic Analysis	Booting/initialize sequence	lem Nojssiek pu	Unnecessary CMD optimization
		Transfer Mode check (R/W function)	No issue	Address misalign, chunk size optimization done
	Performance	B/M Tool Test	No issue	Address discontinuity, chunk size
2	Analysis	User Scenario performance test	No issue	optimization done
2	SI/PI	SI Check	No issue	-
3	Management	PI Check	No issue	1



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Contact for More about Class100



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Thank you

