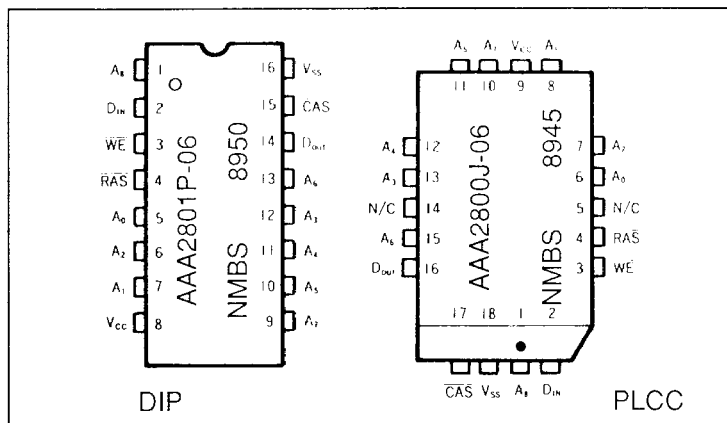


256K x 1 DRAM

FEATURES

- Industry Standard Pinouts
- 60, 70 or 80ns RAS Access Time (t_{RAC})
- Static Column or Enhanced Page Mode
- RAS-Only Refresh, Hidden Refresh and CAS-before-RAS Refresh
- Commercial Voltage/Temperature Range
 - 4.5 - 5.5 Volts V_{CC}
 - 0 - 70°C Operating Temperature
- Packages - 16 Pin 300 mil PDIP
- 18 Pin PLCC



AAA2800 SERIES

Part Number	t_{RAC} 1	t_{CAC} 2	t_{AA} 3 (tCAA)	t_{RC} 4	t_{PC} 5	I_{CC} 6 (Standby)	I_{CC} 7 (Active)	Refresh 8 (tREF)
256K x 1 - STATIC COLUMN MODE								
AAA2800X-06	60ns	11ns	32ns	121ns	35ns	2.5mA	75mA	4.4ms
AAA2800X-07	70ns	12ns	35ns	136ns	40ns	2.5mA	70mA	4.4ms
AAA2800X-08	80ns	13ns	40ns	151ns	45ns	2.5mA	65mA	4.4ms
256K x 1 - ENHANCED PAGE MODE								
AAA2801X-06	60ns	11ns	32ns	121ns	37ns	2.5mA	75mA	4.4ms
AAA2801X-07	70ns	12ns	35ns	136ns	41ns	2.5mA	70mA	4.4ms
AAA2801X-08	80ns	13ns	40ns	151ns	46ns	2.5mA	65mA	4.4ms

NOTES:

1. t_{RAC} defines maximum RAS Access Time.
2. t_{CAC} defines maximum CAS Access Time.
3. t_{AA} (alternatively tCAA) defines maximum Column Address Access Time.
4. t_{RC} defines minimum Read or Write Cycle Time.
5. t_{PC} defines minimum Enhanced Page Mode or Static Column Mode Cycle Time.
6. I_{CC} (Standby) defines maximum V_{CC} supply current with $RAS \geq V_{CC} - 0.4V$, other inputs at CMOS levels, $4.5 \leq V_{CC} \leq 5.5V$ and $0^\circ C \leq T_a \leq 70^\circ C$.
7. I_{CC} (Active) defines maximum V_{CC} supply current under normal operation with $t_{RC} = t_{RC \text{ min.}}$, $4.5 \leq V_{CC} \leq 5.5$ volts and $0^\circ C \leq T_a \leq 70^\circ C$.
8. t_{REF} defines the Refresh Interval during which all internal rows (256) must be refreshed.

ORDERING INFORMATION:

AAA280XX - XX

