



NHD-7.0-800480EF-ATXL#-CTP

TFT (Thin-Film-Transistor) Color Liquid Crystal Display Module

NHD- Newhaven Display 7.0- 7.0" Diagonal

800480- 800xRGBx480 pixels

EF- Model

A- Built-in driver / No Controller

T- White LED backlight

X- TFT

L- 12:00 Optimal View, Wide Temp

#- RoHS Compliant

CTP- Capacitive Touch Panel with built-in controller

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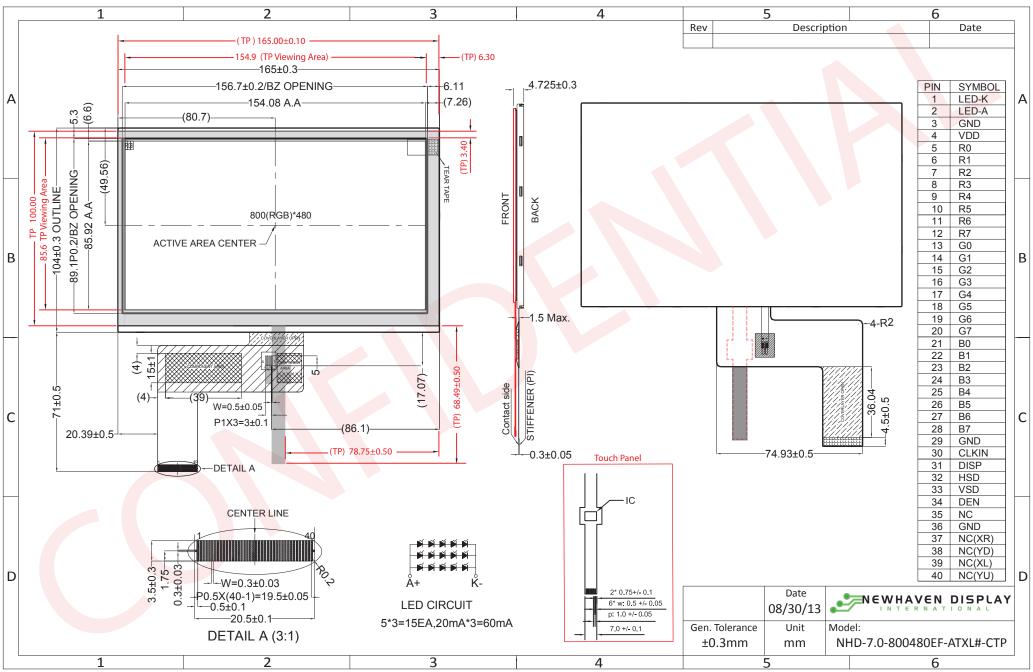
Document Revision History

Revision	Date	Description	Changed by
0	8/30/2013	Initial Release	ML

Functions and Features

- 800xRGBx480 resolution
- LED backlight
- 24-bit digital RGB interface
- 16.7M colors
- Capacitive Touch Panel with built-in controller

Mechanical Drawing



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Pin Description

TFT:

Pin No.	Symbol	Connection	Function Description
1	LED-K	Power Supply	Ground for Backlight
2	LED-A	Power Supply	Backlight Power Supply (60mA @ 16V)
3	GND	Power Supply	Ground
4	VDD	Power Supply	Power Supply (+3.3V)
5-12	[R0-R7]	MPU	Red Data Signals
13-20	[G0-G7]	MPU	Green Data Signals
21-28	[B0-B7]	MPU	Blue Data Signals
29	GND	Power Supply	Ground
30	CLKIN	MPU	Clock for input data
31	DISP	MPU	Display on/off DISP=1:Display on
32	HSD	MPU	Line synchronization signal
33	VSD	MPU	Frame synchronization signal
34	DEN	MPU	Data Enable signal
35	NC	-	No Connect
36	GND	Power Supply	Ground
37	NC(XR)	-	No Connect
38	NC(YD)	-	No Connect
39	NC(XL)	-	No Connect
40	NC(YU)	-	No Connect

LCD connector: 0.5mm pitch 40-Conductor FFC. Molex p/n: 54104-4031 (top contact)

Backlight connector: on LCD connector Mates with: ---

Capacitive Touch Panel:

Pin No.	Symbol	External	Function Description
		Connection	
1	VDD	Power Supply	Power Supply (3.0V)
2	GND	Power Supply	Ground
3	SCL	MPU	Serial I2C Clock (Requires pull-up resistor)
4	SDA	MPU	Serial I2C Data (Requires pull-up resistor)
5	/INT	MPU	Interrupt signal from touch panel module to host
6	/WAKE	MPU	External interrupt signal from host (0: Disable /INT 1: Enable /INT)

Recommended connector: 1.0mm pitch 6-Conductor FFC. Molex p/n: 52271-0679

Driver/Controller Information

TFT:

Built-in HX8264-D02 Source Driver: http://www.newhavendisplay.com/app notes/HX8264-D02.pdf
Built-in HX8664-B Gate Driver: http://www.newhavendisplay.com/app notes/HX8664-B.pdf

Capacitive Touch Panel:

Built-in FocalTech FT5x06 controller.

Please download specification at http://www.newhavendisplay.com/app notes/FT5x06.pdf

Electrical Characteristics

TFT:

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	Тор	Absolute Max	-20	-	+70	٥C
Storage Temperature Range	Tst	Absolute Max	-30	-	+80	٥C
Supply Voltage	VDD		3.0	3.3	3.6	V
Supply Current	IDD	VDD=3.3V 25°C	60	85	120	mA
"H" Level Input	VIH		0.7*VDD	-	VDD	V
"L" Level Input	VIL		GND	•	0.3*VDD	V
"H" Level Output	VOH		VDD-0.4	•	-	V
"L" Level Output	VOL		1	1	GND+0.4	٧
Backlight Supply Voltage	VLED		15	16	17	V
Backlight Supply Current	ILED	VLED=16V	45	60	75	mA

Capacitive Touch Panel:

Item Symb		Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	Тор	Absolute Max	-20	-	+70	°C
Storage Temperature Range	Tst	Absolute Max	-30	-	+80	°C
Supply Voltage	VDD		2.8	-	3.3	V
Supply Current – Operating	IDD	Ta=25°C, VDD=2.8V	-	6.0	-	mA
Supply Current – Hibernate	IDD	Ta=25°C, VDD=2.8V	-	0.03	-	mA
"H" Level Input	Vih		0.7*VDD	-	VDD	V
"L" Level Input	Vil		VSS	-	0.3*VDD	V
"H" Level Output	Voh		0.7*VDD	-	VDD	V
"L" Level Output	Vol		VSS	-	0.3*VDD	V

Optical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing Angle – Top			-	55	-	0
Viewing Angle – Bottom		C= >10	-	65	-	0
Viewing Angle – Left		Cr ≥10	-	70	-	0
Viewing Angle – Right			-	70	-	0
Contrast Ratio	Cr	-	-	400	-	
Luminance	L	-	220	280	-	cd/m ²
Response Time	Tr+Tf	-	-	25	35	ms

Viewing angles based on 6:00 gray scale inversion

Capacitive Touch Panel Material Characteristics:

Property	Requirement	Unit
IC	FT5406D09	
Glass thickness	0.7	mm
Top film thickness	0.125	mm
Surface Hardness	6(750)	H(g)
Light transmission	82%	1
Operating Humidity	45~85	RH
Storage Humidity	5~95	RH

Timing Characteristics

Parameter	Symbol		Spec.				
Parameter	Symbol	Min.	Тур.	Max.	Unit		
HS setup time	T _{hst}	8	-	-	ns		
HS hold time	T _{hhd}	8	-	-	ns		
VS setup time	T _{vst}	8	-	-	ns		
VS hold time	T_{vhd}	8	-	- <	ns		
Data setup time	T _{dsu}	8	-	-	ns		
Data hold time	T_{dhd}	8	-	(0)	ns		
DE setup time	T _{esu}	8	-	Q_V/() ns		
DE hold time	T _{ehd}	8	-	WILL	ns		
VDD Power On Slew rate	T _{POR}	-	-	20	ms		
RSTB pulse width	T _{Rst}	10	((us		
CLKIN cycle time	T _{cph}	20	- (V-	ns		
CLKIN pulse duty	T _{cwh}	40	50	◇ 60	%		
Output stable time	T _{sst}	-	(((-(0))	6 (us		

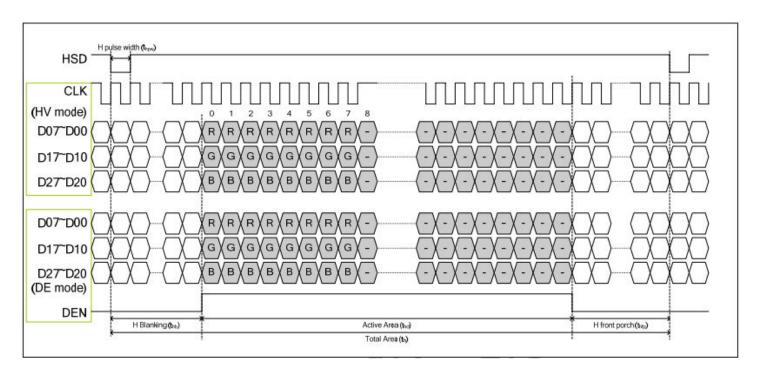
Horizontal Timing

Parameter	Symbol		Spec.	Unit	
Farameter	Syllibol	Min.	Тур.	Max.	Ollit
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255 🗸	DCLK
HS Back Porch (Blanking)	thb		88		DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	512	DCLK

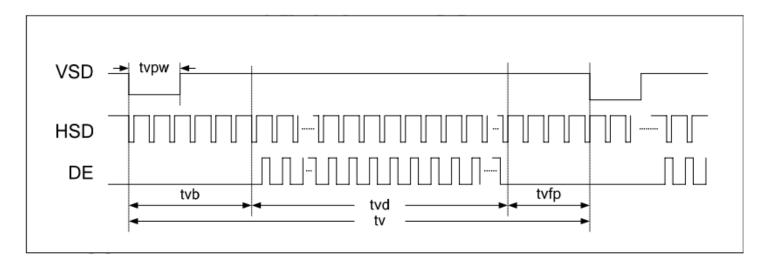
Vertical Timing

Parameter	Symbol		Unit			
Farameter	Symbol	Min.	Тур.	Max.	Oilit	
Vertical Display Area	tvd		480	~//	Тн	
VS period time	tv	513	525	767	T _H	
VS pulse width	tvpw	3	3	255	T _H	
VS Back Porch (Blanking)	tvb	5//	32		Тн	
VS Front Porch	tvfp		13	255	T _H	
DE mode Blanking	tv-tvd	(4)	45	255	T _H	

Horizontal Timing



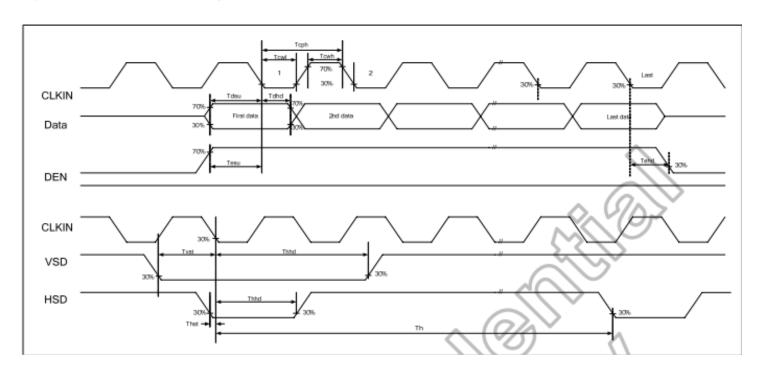
Vertical Timing



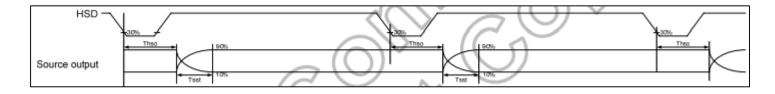
Parallel 24-bit RGB mode

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	- ///
Time from HSD to LD	Thld		64		CLKIN	2.\\\ -
Time from HSD to STV	Thstv		2		CLKIN	(O ₁ / -
Time from HSD to CKV	Thckv		20		CLKIN	
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10	1	CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74	(02)	CLKIN	-

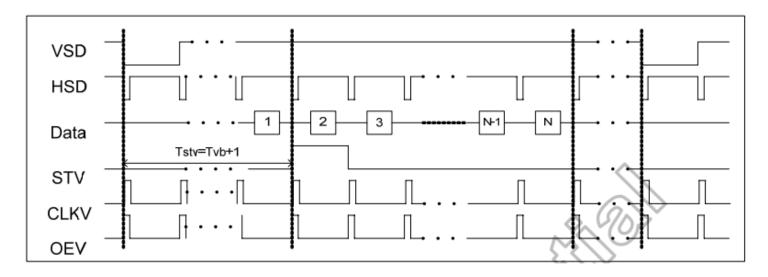
Input Clock and Data Timing



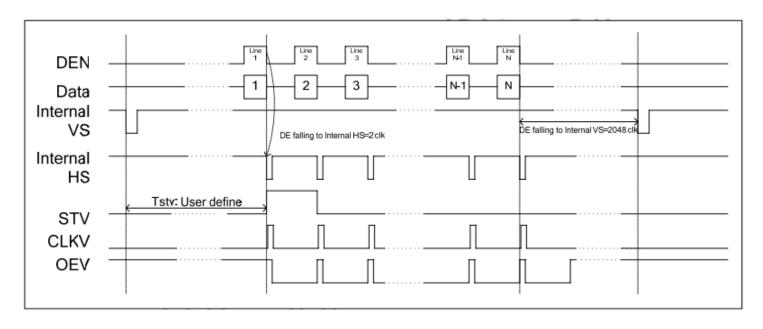
Source Output Timing



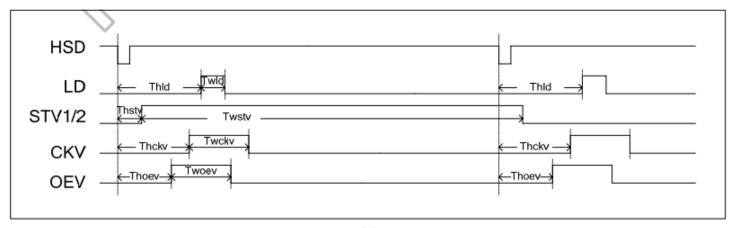
Vertical Timing HV (Cascade)



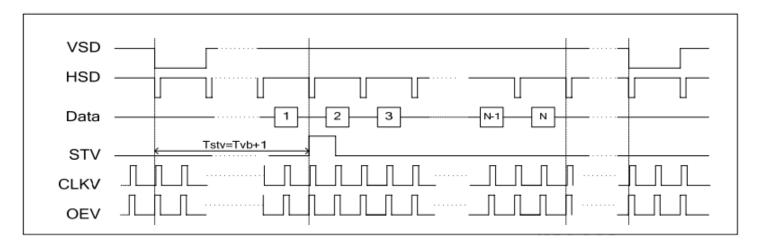
Vertical Timing DE (Cascade)



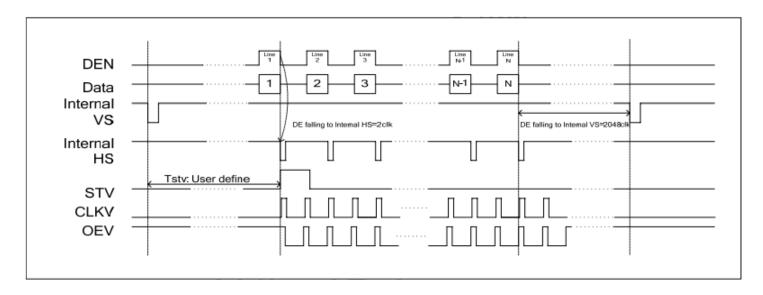
Gate Output Timing (Cascade)



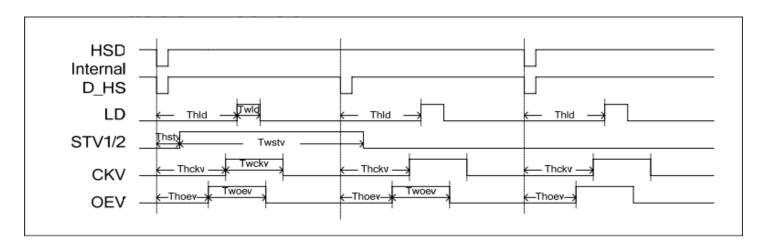
Vertical Timing HV (Dual Gate)



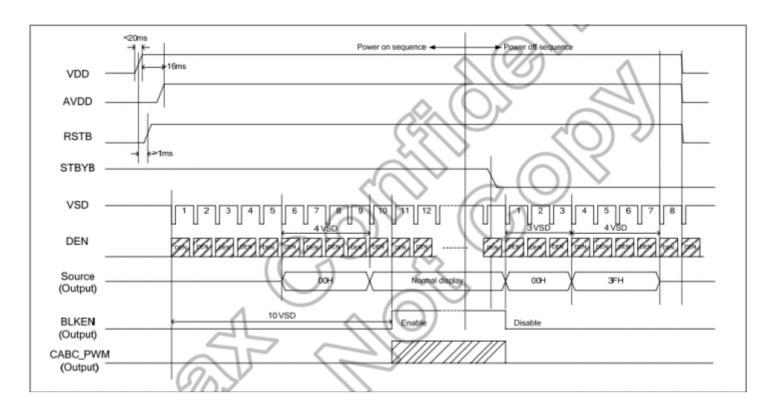
Vertical Timing DE (Dual Gate)



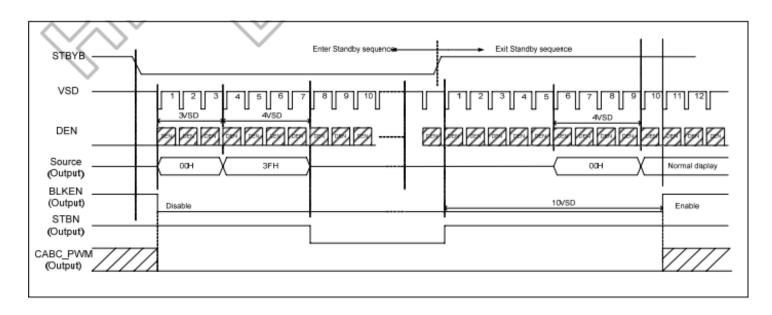
Gate Output Timing (Dual Gate)



Power ON/OFF Sequence



Enter/Exit Standby Mode Sequence



Capacitive Touch Panel Registers

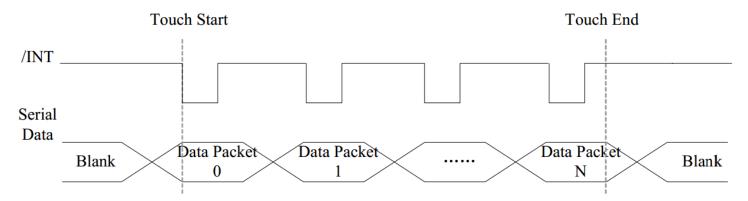
Address	Name	B7	B6	B5	B4	В3	B2	B1	В0	Access
		•			•					
00h	DEVICE_MODE		Device	Mode [2.	.0]					R/W
01h	GEST_ID	Gesture ID [70]						R		
02h	TD_STATUS	Touch Points [30]						R		
03h	TOUCH1_XH	Event F	lag			1st Tou	ıch X Pos	ition MSE	3 [118]	R
04h	TOUCH1_XL	1st Tou	ıch X Posi	ition LSB	[70]					R
05h	TOUCH1_YH	Touch I	D [30]			1st Tou	ıch Y Pos	ition MSE	3 [118]	R
06h	TOUCH1_YL	1st Tou	ıch Y Posi	tion LSB	[70]					R
07h										R
08h				-						R
09h	TOUCH2_XH	Event F	lag			2nd To	uch X Po	sition MS	B [118]	R
0Ah	TOUCH2_XL	2nd To	uch X Pos	sition LSB	[70]	_				R
0Bh	TOUCH2_YH	Touch I	D [30]			2nd To	uch Y Po	sition MS	B [118]	R
0Ch	TOUCH2_YL	2nd To	uch Y Pos	sition LSB	[70]					R
0Dh										R
0Eh				7						R
0Fh	TOUCH3_XH	Event F	lag			3rd To	uch X Pos	ition MSI	B [118]	R
10h	TOUCH3_XL	3rd Tou	uch X Pos	ition LSB	[70]	1				R
11h	TOUCH3_YH	Touch I	D [30]			3rd To	uch Y Pos	ition MSI	B [118]	R
12h	TOUCH3_YL	3rd Tou	uch Y Pos	ition LSB	[70]					R
13h										R
14h				7						R
15h	TOUCH4_XH	Event F	lag			4th Tou	uch X Pos	ition MSI	B [118]	R
16h	TOUCH4_XL	4th Tou	ıch X Pos	ition LSB	[70]	1				R
17h	TOUCH4_YH	Touch I	D [30]			4th Tou	uch Y Pos	ition MSI	B [118]	R
18h	TOUCH4_YL	4th Tou	ıch Y Pos	ition LSB	[70]					R
19h										R
1Ah				7						R
1Bh	TOUCH5_XH	Event F				5th Tou	uch X Pos	ition MSI	B [118]	R
1Ch	TOUCH5_XL			ition LSB	[70]	1				R
1Dh	TOUCH5_YH	Touch I	D [30]			5th Tou	uch Y Pos	ition MSI	B [118]	R
1Eh	TOUCH5_YL	5th Tou	ıch Y Pos	ition LSB	[70]					R
1Fh										R

Address	Name	B7	B6	B5	B4	В3	B2	B1	В0	Access
80h	ID_G_THGROUP	valid to	valid touching detect threshold					R/W		
81h	ID_G_THPEAK	valid to	valid touching peak detect threshold					R/W		
82h	ID_G_THCAL	the thre	eshold w	hen calcu	lating th	e focus o	f touchin	g		R/W
83h	ID_G_THWATER	the thre	eshold w	hen there	e is surfac	ce water				R/W
84h	ID_G_TEMP	the thre	the threshold of temperature compensation					R/W		
85h	ID_G_THDIFF	the thre	the threshold whether the coordinate is different from original					R/W		
86h	ID_G_CTRL					Power	Control N	/lode [1.	.0]	R/W
87h	ID_G_TIME_ENTER_MONITOR	the timer for entering monitor status					R/W			
88h	ID_G_PERIODACTIVE					Period	Active [3	0]		R/W
89h	ID_G_PERIODMONITOR	the timer of entering idle when in monitor status					R/W			
A0h	ID_G_AUTO_CLB_MODE	auto calibration mode					R/W			
A1h	ID_G_LIB_VERSION_H	Firmwa	re Librar	y Version	H byte					R
A2h	ID_G_LIB_VERSION_L	Firmwa	re Librar	y Version	L byte					R
A3h	ID_G_CIPHER	Chip ve	ndor ID							R
A4h	ID_G_MODE	the inte	errupt sta	itus to ho	st					R
A5h	ID_G_PMODE	Power	Consume	Mode						
A6h	ID_G_FIRMID	Firmware ID					R			
A7h	ID_G_STATE	Runnin	g State							
A8h	ID_G_FT5201ID	CTPM V	/endor ID)						R
A9h	ID_G_ERR	Error Co	ode							R
AAh	ID_G_CLB	Configure TP module during calibration in Test Mode					R/W			
FEh	LOG_MSG_CNT	The log	MSG cou	unt						R
FFh	LOG_CUR_CHA	Current	characte	er of log ı	message					R

NOTE: Registers 80h – AFh have been configured for optimum settings and do not need to be modified.

Register No	Register Name	Bits	Value	Description
00h	Device Mode	[2:0]	000b	Normal Operating Mode
			100b	Test Mode - read raw data (reserved)
			001b	System Information Mode (reserved)
01h	Gesture ID	[7:0]	48h	Zoom In
			49h	Zoom Out
			00h	No Gesture
02h	Touch Points	[3:0]	000b	0 touch points detected
			001b	1 touch point detected
			010b	2 touch points detected
			011b	3 touch points detected
			100b	4 touch points detected
			101b	5 touch points detected
03h	Touch 1 Event Flag	[7:6]	00b	Put Down
			01b	Put Up
			10b	Contact
			11b	Reserved
03h	TOUCH1_XH	[3:0]	0h - 1h	Upper 4 bits of X touch coordinate
04h	TOUCH1_XL	[7:0]	00h - FFh	Lower 8 bits of X touch coordinate
05h	TOUCH1_YH	[3:0]	0h - 1h	Upper 4 bits of Y touch coordinate
06h	TOUCH1_YL	[7:0]	00h - FFh	Lower 8 bits of Y touch coordinate
09h	Touch 2 Event Flag	[7:6]	00b	Put Down
			01b	Put Up
			10b	Contact
			11b	Reserved
09h	TOUCH2_XH	[3:0]	0h - 1h	Upper 4 bits of X touch coordinate
0Ah	TOUCH2_XL	[7:0]	00h - FFh	Lower 8 bits of X touch coordinate
0Bh	TOUCH2_YH	[3:0]	0h - 1h	Upper 4 bits of Y touch coordinate
0Ch	TOUCH2_YL	[7:0]	00h - FFh	Lower 8 bits of Y touch coordinate
0Fh	Touch 3 Event Flag	[7:6]	00b	Put Down
	-		01b	Put Up
			10b	Contact
			11b	Reserved
0Fh	TOUCH3_XH	[3:0]	0h - 1h	Upper 4 bits of X touch coordinate
10h	TOUCH3_XL	[7:0]	00h - FFh	Lower 8 bits of X touch coordinate
11h	TOUCH3_YH	[3:0]	0h - 1h	Upper 4 bits of Y touch coordinate
12h	TOUCH3_YL	[7:0]	00h - FFh	Lower 8 bits of Y touch coordinate
15h	Touch 4 Event Flag	[7:6]	00b	Put Down
			01b	Put Up
			10b	Contact
			11b	Reserved
15h	TOUCH4_XH	[3:0]	0h - 1h	Upper 4 bits of X touch coordinate
16h	TOUCH4_XL	[7:0]	00h - FFh	Lower 8 bits of X touch coordinate
17h	TOUCH4_YH	[3:0]	0h - 1h	Upper 4 bits of Y touch coordinate
18h	TOUCH4_YL	[7:0]	00h - FFh	Lower 8 bits of Y touch coordinate

Register No	Register Name	Bits	Value	Description	
1Bh	Touch 5 Event Flag	[7:6]	00b	Put Down	
			01b	Put Up	
			10b	Contact	
			11b	Reserved	
1Bh	TOUCH5_XH	[3:0]	0h - 1h	Upper 4 bits of X touch coordinate	
1Ch	TOUCH5_XL	[7:0]	00h - FFh	Lower 8 bits of X touch coordinate	
1Dh	TOUCH5_YH	[3:0]	0h - 1h	Upper 4 bits of Y touch coordinate	
1Eh	TOUCH5_YL	[7:0]	00h - FFh	Lower 8 bits of Y touch coordinate	
80h	ID_G_THGROUP	[7:0]	00h - FFh	Valid touching detect threshold	Recommended: 46h
				Actual value will be 4 times register's value	
81h	ID_G_THPEAK	[7:0]	00h - FFh	valid touching peak detect threshold	Recommended: 3Ch
82h	ID_G_THCAL	[7:0]	00h - FFh	Touch focus threshold	Recommended: 1Dh
83h	ID_G_THWATER	[7:0]	00h - FFh	threshold when there is surface water	Recommended: D3h
84h	ID_G_THTEMP	[7:0]	00h- FFh	threshold of temperature compensation	Recommended: EBh
85h	ID_G_THDIFF	[7:0]	00h- FFh	Touch difference threshold	Recommended: A0h
				Actual value is 32 times the register's value	
86h	ID_G_CTRL	[1:0]	00h	Power Control Mode: Not Auto Jump	
			01h	Power Control Mode: Auto Jump	
87h	ID_G_TIME_ENTER_MONITOR	[7:0]	00h-FFh	Delay to enter 'Monitor' status (s)	Recommended: C8h
88h	ID_G_PERIODACTIVE	[3:0]	3h-Eh	Period of 'Active' status (ms)	Recommended: 6h
89h	ID_G_PERIODMONITOR	[7:0]	1Eh-FFh	Timer to enter 'idle' when in 'Monitor' (ms)	Recommended: 28h
A0h	ID_G_AUTO_CLB_MODE	[7:0]	00h	Auto calibration mode: Enable auto calibration	
			FFh	Auto calibration mode: Disable auto calibration	
A1h	ID_G_LIB_VERSION_H	[7:0]	30h	Firmware Library Version H byte	
A2h	ID_G_LIB_VERSION_L	[7:0]	01h	Firmware Library Version L byte	
A3h	ID_G_CIPHER	[7:0]	55h	Chip vendor ID	
A4h	ID_G_MODE	[0:0]	00h	Interrupt status: Enable interrupt to host	
			01h	Interrupt status: Disable interrupt to host	
A5h	ID_G_PMODE	[1:0]	00h	'Active' Mode	
			01h	'Monitor' Mode	
			03h	'Hibernate' Mode	
A6h	ID_G_FIRMID	[7:0]	05h	Firmware ID	
A7h	ID_G_STATE	[7:0]	00h	Running State: Configure	
			01h	Running State: Work	
			02h	Running State: Calibration	
			03h	Running State: Factory	
			04h	Running State: Auto-calibration	
A8h	ID_G_FT5201ID	[7:0]	79h	CTPM Vendor's Chip ID	
A9h	ID_G_ERR	[7:0]	00h	Error Code: OK	
			03h	Error Code: Chip register writing inconsistent w	th reading
			05h	Error Code: Chip start fail	
			1Ah	Error Code: Calibration match fail	



Interrupt trigger mode

Sample code to read touch data:

Sample code to overwrite default register values:

Quality Information

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage	+80°C, 96hrs	2
	temperature for a long time.		
Low Temperature storage	Endurance test applying the low storage	-30°C , 96hrs	1,2
	temperature for a long time.		
High Temperature	Endurance test applying the electric stress	+70°C, 96hrs	2
Operation	(voltage & current) and the high thermal		
	stress for a long time.		
Low Temperature	Endurance test applying the electric stress	-20°C , 96hrs	1,2
Operation	(voltage & current) and the low thermal		
	stress for a long time.		
High Temperature /	Endurance test applying the electric stress	+50°C, 90% RH, 96hrs	1,2
Humidity Operation	(voltage & current) and the high thermal		
	with high humidity stress for a long time.		
Thermal Shock resistance	Endurance test applying the electric stress	-30°C, 30min -> 80°C, 30min,	
	(voltage & current) during a cycle of low	Change time: 5min,	
	and high thermal stress.	10 cycles	
Vibration test	Endurance test applying vibration to	10-55Hz , 1.5mm amplitude.	3
	simulate transportation and use.	60 sec in each of 3 directions	
		X,Y,Z	
		For 15 minutes	
Static electricity test	Endurance test applying electric static	VS=800V, RS=1.5kΩ, CS=100pF	
	discharge.	One time	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.

Precautions for using LCDs/LCMs

See Precautions at www.newhavendisplay.com/specs/precautions.pdf

Warranty Information and Terms & Conditions

http://www.newhavendisplay.com/index.php?main_page=terms

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Newhaven Display:

NHD-7.0-800480EF-ATXL#-CTP NHD-7.0-800480EF-ATXV#-CTP