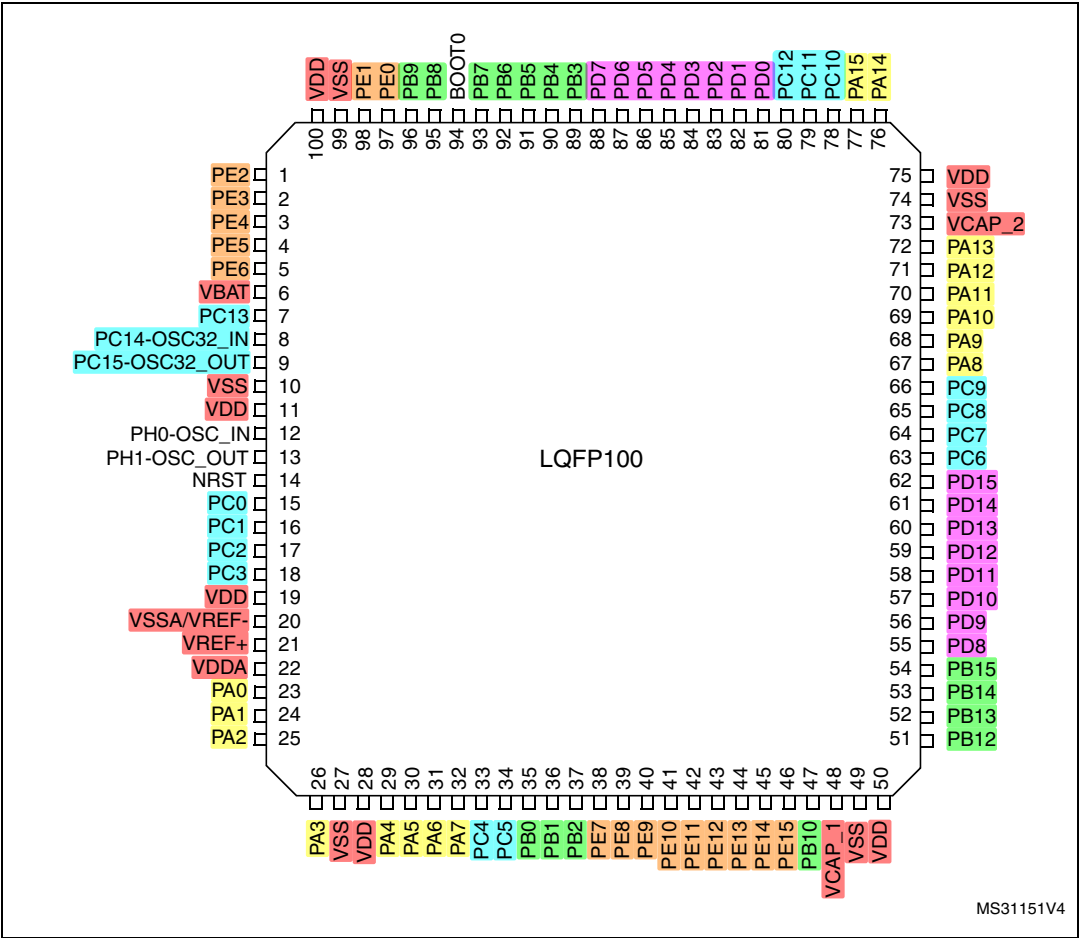
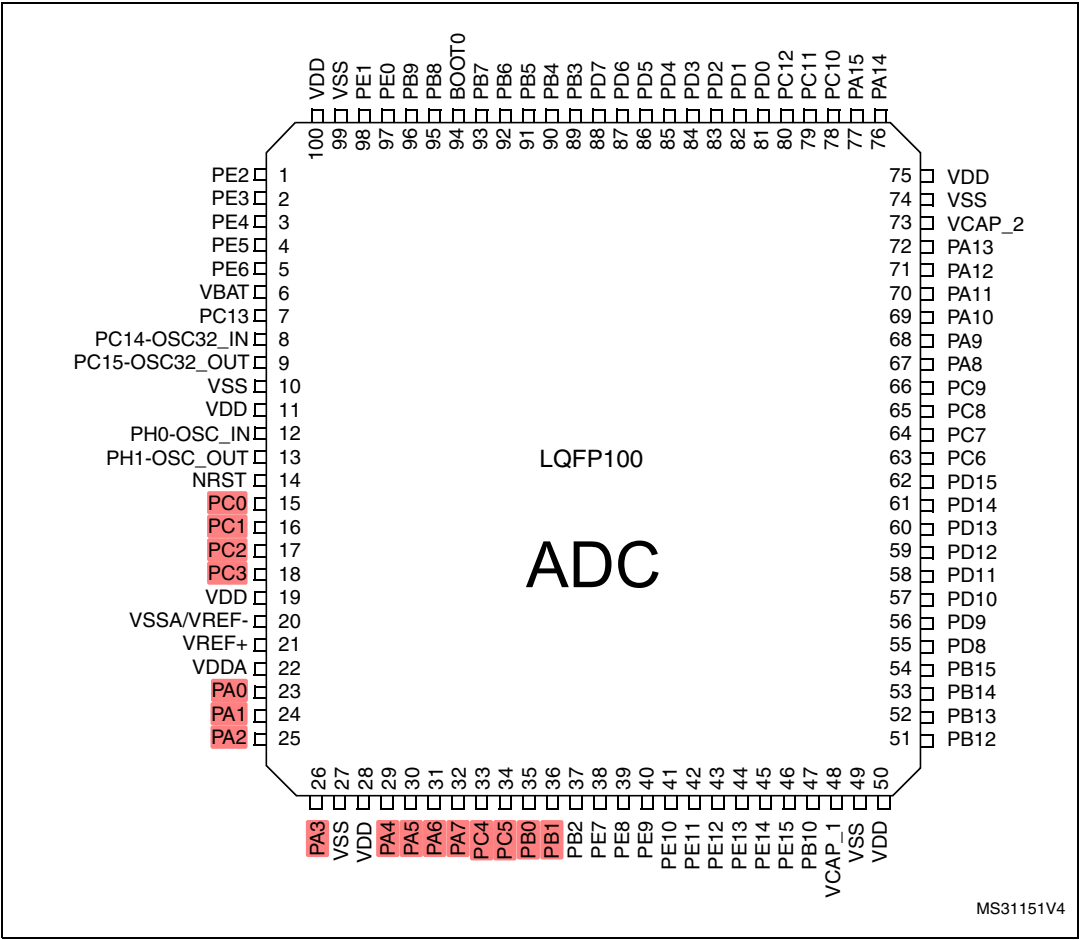


Figure 12. STM32F411xC/xE LQFP100 pinout



1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout

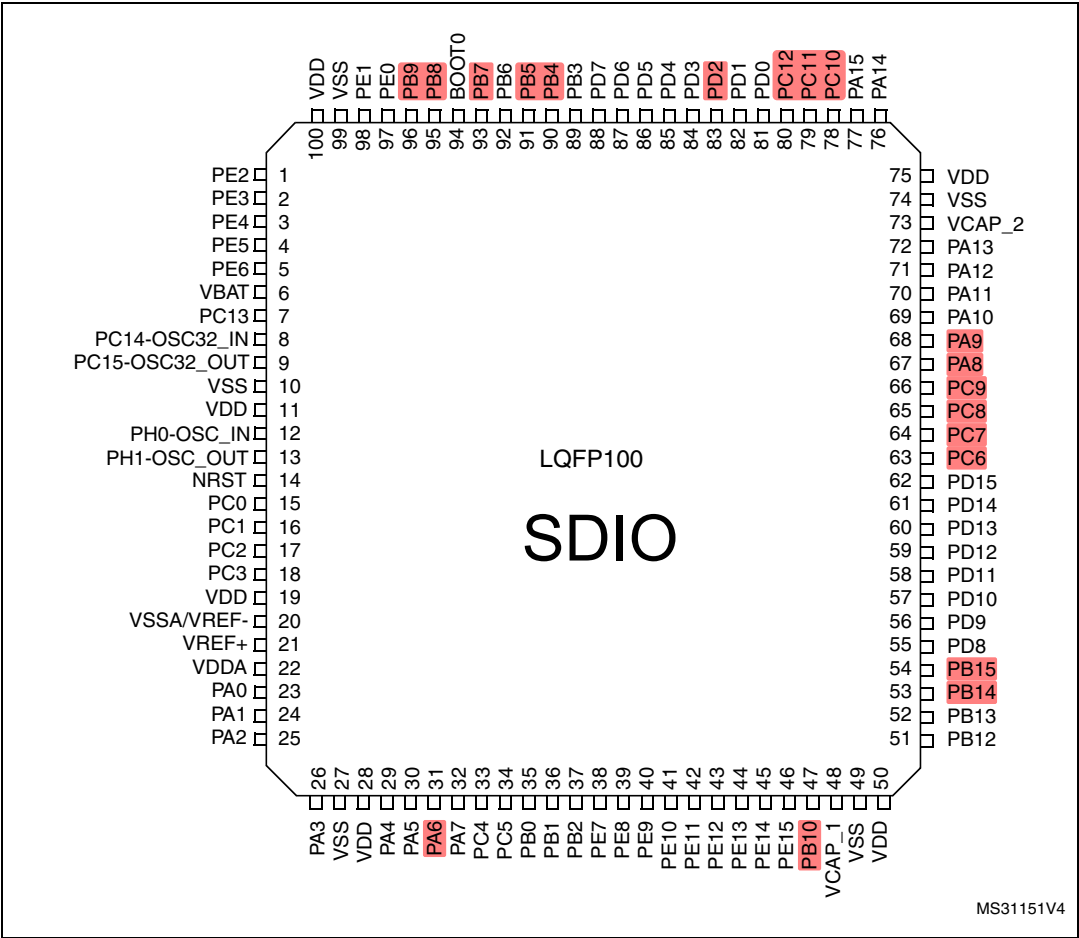


1. The above figure shows the package top view.

Pin diagram of the TIM1 LQFP100 package. The package is labeled LQFP100 and TIM1. The pins are numbered 1 to 100. The top half of the package (pins 1-50) is labeled with various functions: PE2, PE3, PE4, PE5, PE6, VBAT, PC13, PC14-OSC32_IN, PC15-OSC32_OUT, VSS, VDD, PH0-OSC_IN, PH1-OSC_OUT, NRST, PC0, PC1, PC2, PC3, VDD, VSSA/VREF-, VREF+, VDDA, PA0, PA1, PA2, PA3, VSS, VDD, PA4, PA5, PA6, PA7, PC4, PC5, PC6, PB0, PB1, PB2, PE7, PE8, PE9, PE10, PE11, PE12, PE13, PE14, PE15, PB10, VCAP_1, VSS, and VDD. The bottom half of the package (pins 51-100) is labeled with: VDD, VSS, VCAP_2, PA13, PA12, PA11, PA10, PA9, PA8, PC9, PC8, PC7, PC6, PD15, PD14, PD13, PD12, PD11, PD10, PD9, PD8, PB15, PB14, PB13, PB12, VDD, VSS, VCAP_1, VSS, and VDD.

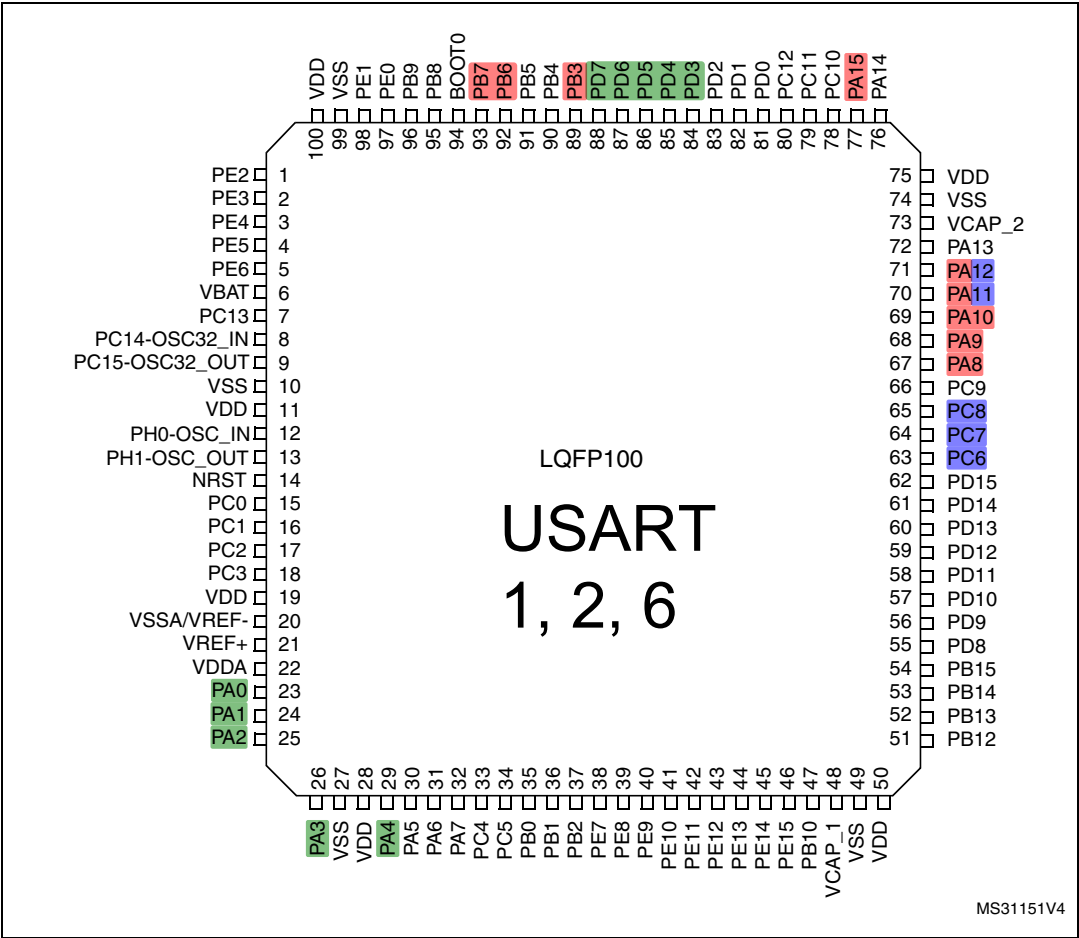
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Figure 12. STM32F411xC/xE LQFP100 pinout



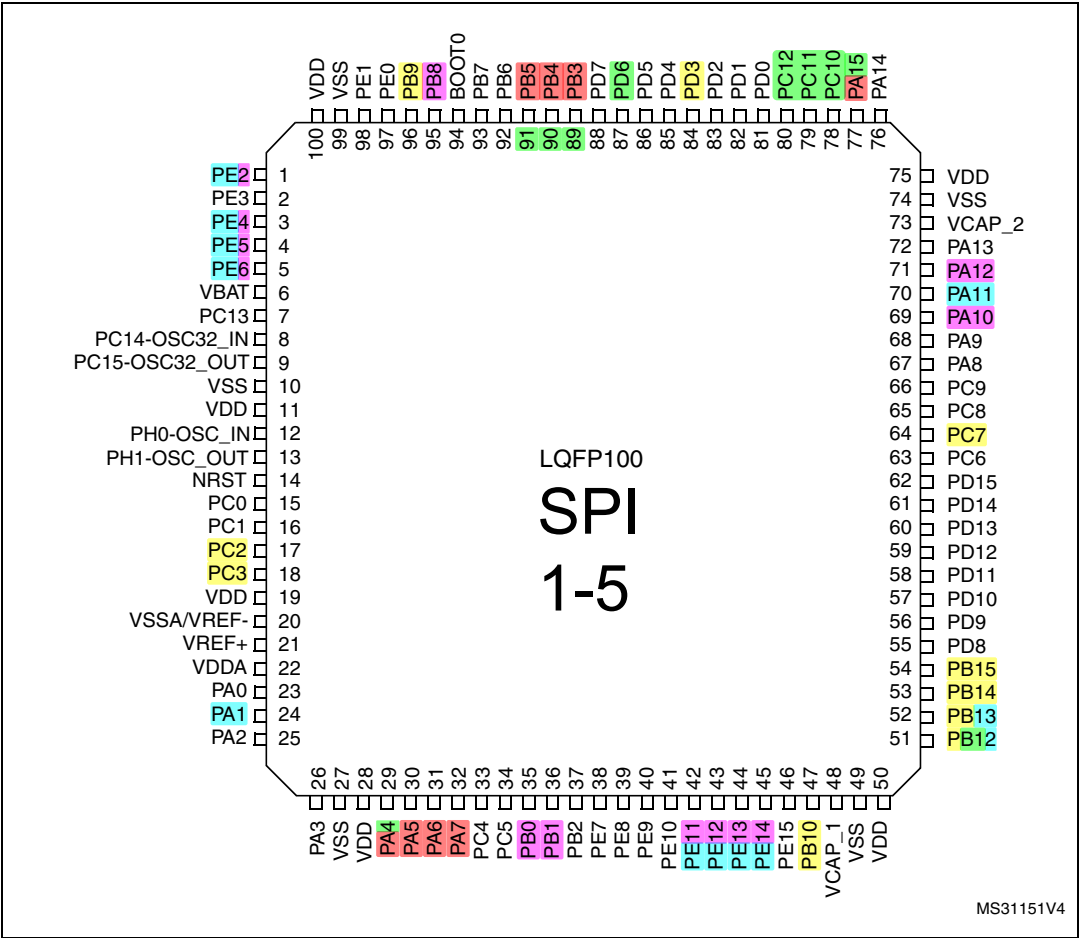
1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout



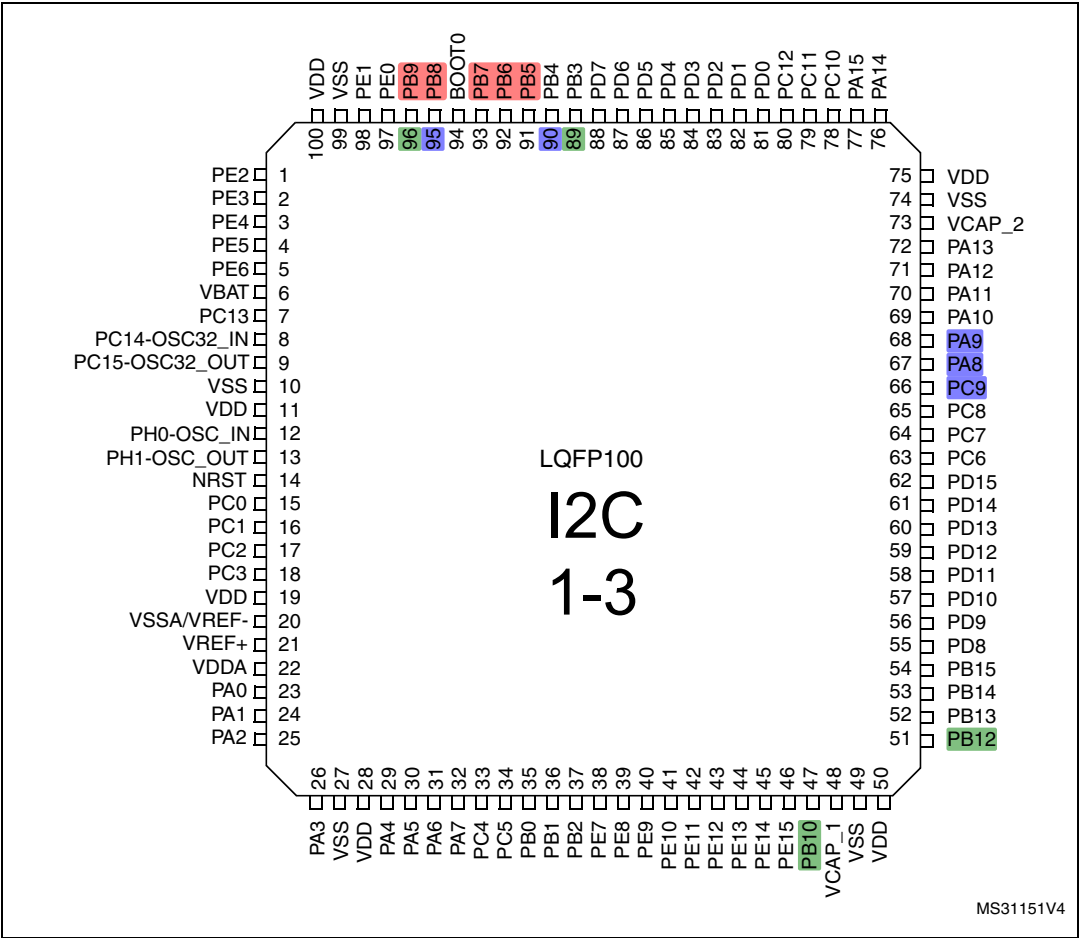
1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout



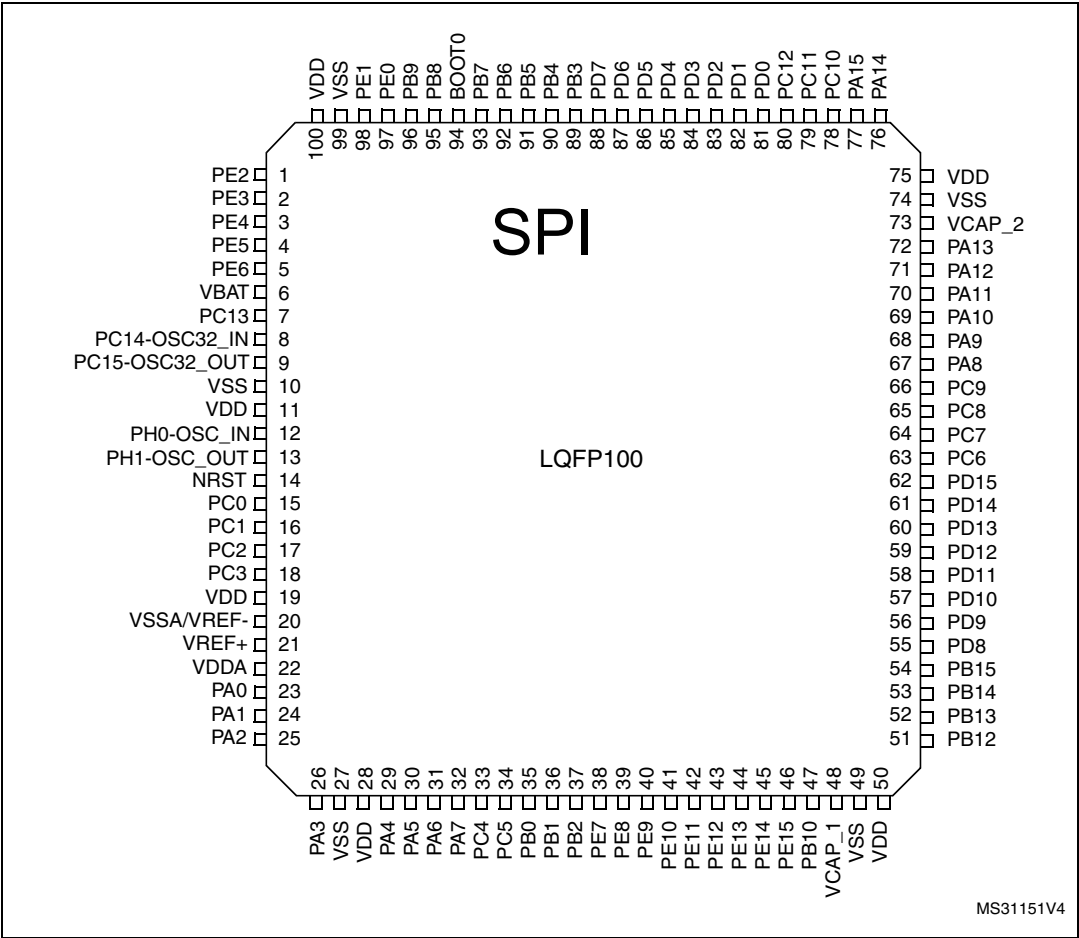
1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout



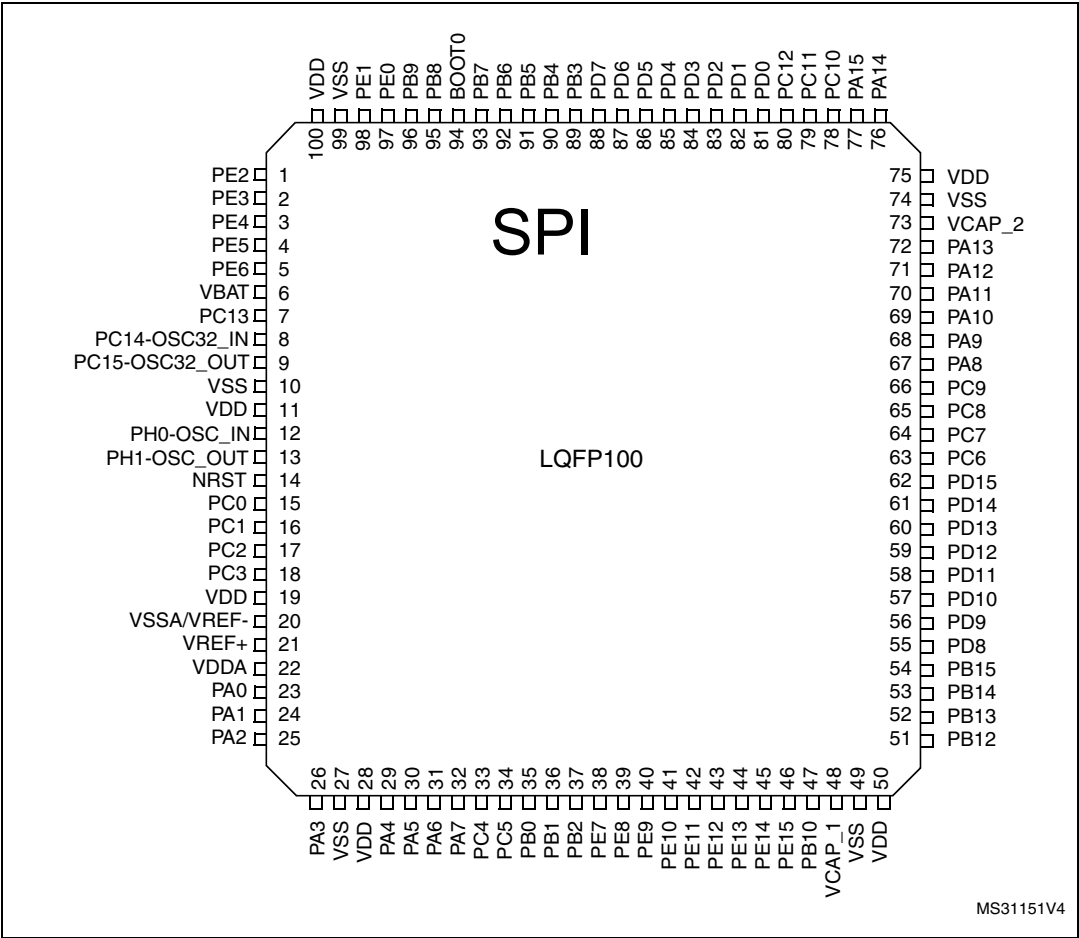
1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout



1. The above figure shows the package top view.

Figure 12. STM32F411xC/xE LQFP100 pinout



1. The above figure shows the package top view.