

Decoupling caps Place close to VDD/VDDA

C11 _

4u7

+373

VDD

C12 C13

+3V3

VDDA

100n

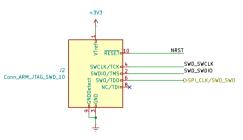


I2S Fsamp calculation: via STM32G431x reference manual

Fsamp = Fi2sclk/[(64)(2(I2SDIV + ODD))]

Currently Fi2sclk is set to the HCLK * APB prescaler (HSI clock, 16 MHz)

(64 as DATALEN != 0b00, instead SD out is 24-bits therefore CHLEN = 1, otherwise replace 64 w/ 32)



On Nucleo32-STM32G431KB devices PFO and PF1 are disconnected. SB11 and SB8 must be connected for this schematic to work.

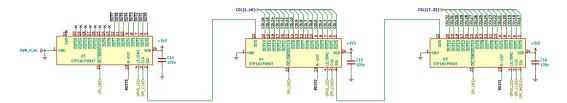
Decoupling capacitor info can be found on datasheet and application note AN5093 No ADC therefore tying VDDA to VDD and GNDA to GND

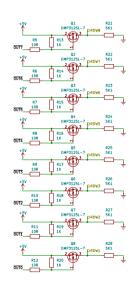
Sheet: /STM32 MCU/ File: STM32 MCU.kicad sch

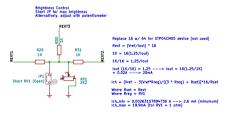
Title: STM32G431Kx Schematic

Date: 2024-12-03 Size: A4 Rev: v04 KiCad E.D.A. 9.0.1 Id: 4/7

Cascade Direction Last (in chain) <--- First (in chain)









Sheet: /LED MATRIX ARRAY #1/	
File: LED_MATRIX_ARRAY_1.kicad_sch	
Title: LED Matrix #1	
Size: A4 Date: 2024-11-23	Rev: v01
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8x16 matrix	
Sheet: /LED MATRIX ARRAY #2/	
File: LED_MATRIX_ARRAY_2.kicad_sch	
Title: LED Matrix #2	
Size: A4 Date: 2024-11-23	Rev: v01
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