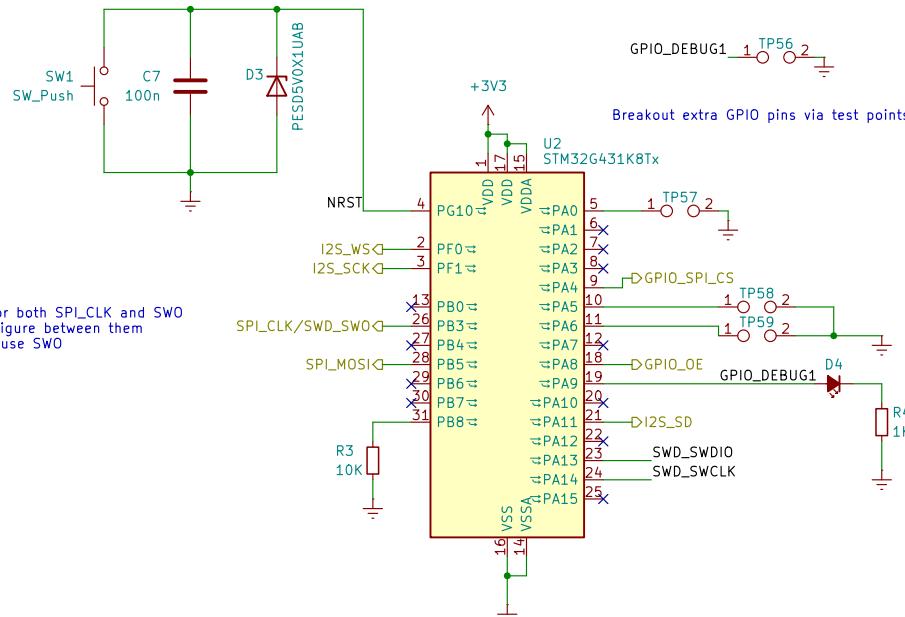


Reset circuit

Reset circuit



PB3 cannot be used for both SPI_CLK and SWO
Programmer must configure between them
Spekky matrix doesn't use SWO

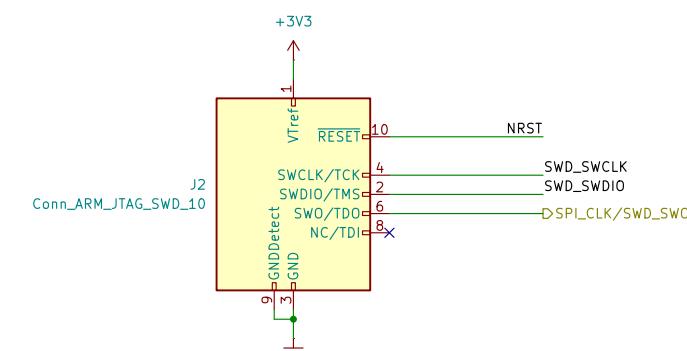
OR NOR DOOR

I2S Fsamp calculation: via STM32G431x reference manual

$$F_{\text{samp}} = F_{\text{I2Sclk}} / [(64)(2(I2SDIV + ODD))]$$

Currently Fi2sclk is set to the HCLK * APB prescaler (HSI clock, 16 MHz)

(64 as DATALEN != 0b00, instead SD out is 24-bits therefore CHLEN = 1, otherwise replace 64 w/ 32)



The diagram illustrates two sets of decoupling capacitor connections. On the left, a green line labeled **VDDA** connects to a node above two parallel red capacitors, labeled **C8 100n** and **C9 1u**. A red arrow points upwards from this node. On the right, a green line labeled **VDD** connects to a node above three parallel red capacitors, labeled **C10 4u7**, **C11 100n**, and **C12 100n**. A red arrow points upwards from this node. Both sets of capacitors are connected to a common ground rail at the bottom.

Decoupling caps

Place close to

C9 C1

— 1u 4u

On Nucleo32-STM32G431KB devices PFO and PF1 are disconnected. SB11 and SB8 must be connected for this schematic to work.

SB11 and SBO must be connected for this scheme to work.
Decoupling capacitor info can be found on datasheet and application note AN5093
No ADC therefore tying VDDA to VDD and GND to GND

Sheet: /STM32 MCU/
File: STM32 MCU.kicad_sch

Title: STM32G431Kx Schematic

Size: A4 Date: 2025-11-01

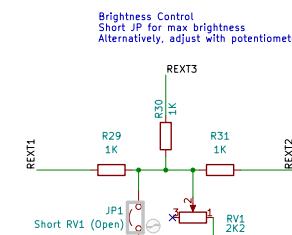
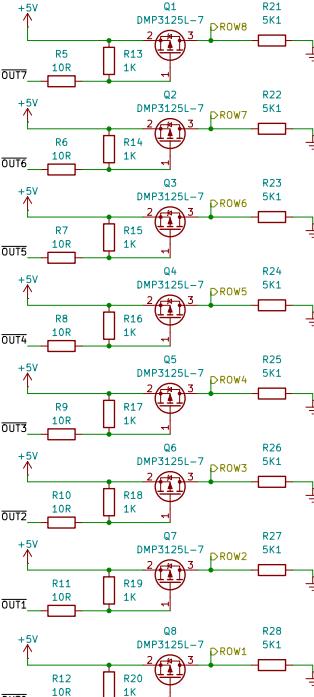
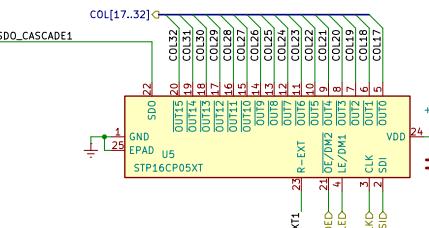
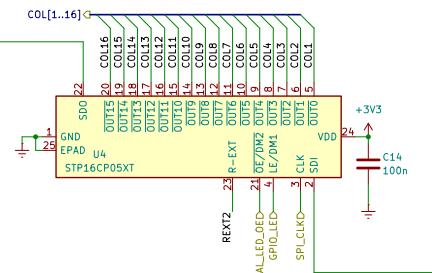
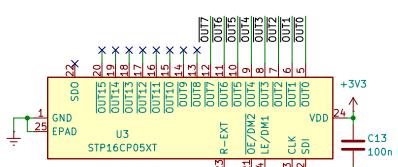
KiCad EDA 9.0.5

Rev. v10

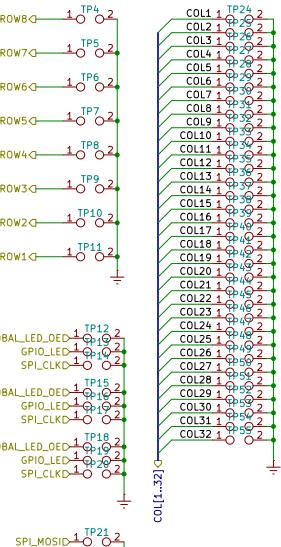
Rev. VI
Id: 4/7

Cascade Direction

Last (in chain) <--- First (in chain)



Replace 16 w/ 64 for STP04CM05 device (not used)
 $R_{ext} = (V_{ref}/I_{out}) * 16$
 $I_{out} = 16(1.25/I_{out})$
 $I_{out}/16 = 1.25 \rightarrow I_{out} = 16(1.25/1K)$
 $I_{out} = 0.02A \rightarrow 20mA$
 $I_{ch} = [V_{ref} - 3(V_{ref}*R_{reg}) / ((3 * R_{reg}) + R_{set})] * 16/R_{set}$
Where $R_{set} = R_{ext}$
Where $R_{reg} = RV1$
 $I_{ch_min} = 0.00263157894736 A \rightarrow 2.6 mA$ (minimum)
 $I_{ch_max} = 19.9mA$ (for $RV1 = 1\ \Omega$)



STP16CP05 based cascaded LED matrices

Sheet: /LED_DRIVING/
File: LED_DRIVING.kicad_sch
Title: 8x32 LED Matrix
Size: A3 Date: 2025-11-01
KiCad E.D.A. 9.0.5 Rev: v08
Id: 5/7

