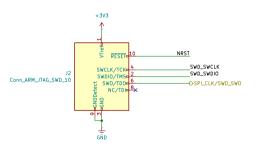


I2S Fsamp calculation: via STM32G431x reference manual

Fsamp = Fi2sclk/[(64)(2(I2SDIV + ODD))]

Currently Fi2sclk is set to the HCLK \* APB prescaler (HSI clock, 16 MHz)

(64 as DATALEN != 0b00, instead SD out is 24-bits therefore CHLEN = 1, otherwise replace 64 w/ 32)



On Nucleo32-STM32G431KB devices PFO and PF1 are disconnected. SB11 and SB8 must be connected for this schematic to work.

Sheet: /STM32 MCU/

Decoupling capacitor info can be found on datasheet and application note AN5093

File: STM32\_MCU.kicad\_sch

Title: STM32G431KB Schematic

Size: A4 Date: 2024-11-19 Rev: v02

Kicad E.D.A. 8.0.6 Id: 4/5

