

## PES PROJECT 2 WBS

Subject: Project 2 WBS

Class: ECEN 5813

Contributors:

Jack Campbell, Troy Davis

Estimation Scale:

Small (S) = less than 2 hours

Medium (M) = 2 to less than 4 hours

Large (L) = 4 to less than 8 hours

Extra Large = 8 hours or more

PES PROJECT 2							
1	WBS	2	DOCUMENTATION	3	CODE MODULES	4	MAKE
1.1	Initial WBS	2.1	Comments	3.1	LED	4.1	Configs
1.1.1	Task breakdown - Troy (M)	2.1.1	Function comments - Jack (S)	3.1.1	Design interface - Jack (S)	4.1.1	fb_run - Jack (L)
1.1.2	Task assign - Jack (S)	2.1.2	File comments - Jack (S)	3.1.2	Implement LED state changes - Troy (M)	4.1.2	fb_debug - Jack (L)
1.2	Post-Mortem WBS	2.1.3	Makefile comments - Jack (S)	3.1.3	Color switching logic - Jack (M)	4.1.3	pc_run - Jack (L)
1.2.1	Task assessment - Jack (M)	2.2	PDF	3.1.4	LED Types - Jack (S)	4.1.4	pc_debug - Jack (L)
1.2.2	Spreadsheet creation - Jack (S)	2.2.1	Generate code snippets - Troy (S)	3.1.5	Initialize LED - Troy (M)	4.1.5	clean - Jack (S)
		2.2.2	WBS - Jack (M)	3.1.6	Initialize board - Troy (S)	4.2	FB Boilerplate Wrangling
		2.2.3	Makefiles - Troy (S)	3.2	MAIN	4.2.1	Per-subdir Generated Makefiles - Jack (M)
		2.2.4	Readme - Troy (S)	3.2.1	Control LED state changes - Jack (S)	4.2.2	Configuring Project Targets - Jack (M)
		2.3	Readme	3.2.2	Control LED color changes - Jack (S)	4.2.3	Invoking generated files
		2.3.1	Repo contents - Jack (S)	3.2.3	Write timing loop - Jack (S)	4.3	Makefiles
		2.3.2	FB Instructions - Jack (S)	3.3	DEBUGGING	4.3.1	CFLAGS - Jack (S)
		2.3.3	PC Instructions - Jack (S)	3.3.1	FB Debug Printing - Jack (S)	4.3.2	Sub-target compilation steps - Jack (M)
				3.3.2	PC Debug Printing - Jack (S)	4.3.3	Sub-target linking steps - Jack (M)
				3.3.3	Initialize FB Debug Console - Troy (S)		
				3.4	Delay		
				3.4.1	PC Delay Implementation - Jack (M)		
				3.4.2	FB Delay Implementation - Troy (M)		