GPUs Assisted Malware Detection for Mobile Devices

Jared Carlson

CONTENTS

I	ntroduction	4	
II	Overview of Algorithms I-A Signature Checks I-B Memory Analysis I-C Dynamic Disassembly I-D Encryption	4	
III	Signature Checking	5	
111	II-A General Techniques	7	
IV	Memory Analysis V-A Texture Atlas	7 9 9	
V	Dynamic Disassembly V-A Why This Works Even Better for Mobile		
VI	Encryption	18	
VII	Environment Considerations /II-A Restrictive Environments	18 18 18	
VIII	Device and Platform Information	19	
IX	Optimizations and Other Tricks X-A Generalities		
X	Performance		
XI	Hardware Integration (I-A Shader Trends	21	

E-mail: jared.carlson23@gmail.com

Manuscript developed as part of DARPA CFT grant

J. Carlson is an independent consultant in the Boston area.

XII	II Strategies				
XIII GPU Assisted Malware					
XIV	IV Summary				
XV	Landscape and Likely DevelopmentsXV-ACUDAXV-BRenderScriptXV-COpenCL	24 24 24 24			
XVI	Future Research	25			
XVII	Conclusion	25			
XVII	I Acknowledgment	25			
Refer	rences	25			
Appe	ndix A: Example Code	26			
1 2 3 4 5 6	B-A iOS				
	LIST OF TABLES				
Ι	Device Characteristics	19			
1 2 3 4 5 6 7 8 9 10	Python Script to Exercise Disassembler	6 6 7 9 10 10 11 11 12 14 14 15			

13	Simple Shader for Square Matrix Multiplication	17
14	Code Fragment from rsdProgram.cpp within frameworks/base/libs/rs/driver	22
15	Render Method for iOS project	26
16	Sample disassembly using the Accelerate framework	27
17	Simple NDK usage for rendering	31
18	To Move into Vectorized C++ for the NEON Processor	36
19	"Cuda Example Program"	36
20	"OpenCL Example"	37
21	"OpenCL via Python"	40

Abstract

This paper, produced as part of a DARPA CFT grant, discusses how to use the GPU and other vectorized processors for cyber tasks, starting with defensive measures. We address mobile devices for their novelties, tight integration with CPUs and the difficulty of coming up with OTA updates or other vendor concerns.

The views expressed are those of the author and do not reflect the official policy or position of the Department of Defense or the U.S. Government. This is in accordance with DoDI 5230.29, January 8, 2009

Index Terms

Security, ARM, GPU, iOS, Android, OpenGL, Vector, SIMD, DARPA

I. Introduction

PUs have seen rapid growth over the past decade, with rapid improvement in their integration into mobile devices. This has been powered by gaming sales, and other user interface desires from the consumer market. But within the hacking world we can see how capable these chips are and how they've recently been used for aiding malware proliferation [12]. What's even more interesting in mobile devices is the integration within a System-on-Chip (SoC) or Package-on-Package (PoP) chipset.

Mobile devices, with a diverse set of communication channels have RF for bluetooth, cellular and other bands such as NFC for certain configurations. Current top-of-the-line system chips will typically be comprised of an ARM CPU, GPU as well as a SIMD Accelerator, a NEON chip for example. Our goal was to see what can we do with these other chips could offer for malware defense schemes. By doing this, we are effectively increasing the defense surface, forcing an attacker to understand the system and not just the CPU. Naturally the first choice is the GPU but recent SoC's (System on a Chip) include a NEON chip, and SDK's and other tools are being developed to allow developers increasing access and capabilities to leverage the processors outside of the the CPU. Examples of these on iOS and Android are the Accelerate framework as well as the Renderscript toolset provided on Android.

II. OVERVIEW OF ALGORITHMS

The main challenge when dealing with the GPU, or the NEON processor for that matter, is that we need to focus on SIMD (Single Instruction Multiple Data) based algorithms and how to incorporate them into malware defense. This means that we want to explore some existing capabilities and see how they map to the GPU, as well as tasks that seem especially suited for GPU assistance. With this in mind, we explored the following schemes to see what could be done on the GPU or other SIMD processors.

A. Signature Checks

A staple of anti-virus software, signature checks are a basic capability that we want to explore using the GPU. We also want to establish that we can reuse this basic premise for other, related tasks such as pattern recognition.

B. Memory Analysis

More advanced attacks could alter in-memory objects, spray a payload into the heap or other tasks to exploit the program. We want to show how small modifications in code management can lend itself to allow GPU tracking and integrity checks.

C. Dynamic Disassembly

Being able to disassemble incoming instructions would allow for a variety of analysis tasks to consume the disassembled instruction sets and make decisions or alter existing parameters. Here we want to show that we can leverage vectorizable code and if necessary that the building blocks to handle this on the GPU are present today, and likely increasingly accessible tomorrow.

D. Encryption

Malware decryption, the unpacking of a virus by the GPU, was one of the first noted uses for the GPU within the hacking community. However, here we explore encrypting live memory objects, data blobs and other sensitive parameters to obfuscate the code and help mitigate an attack.

III. SIGNATURE CHECKING

Signature checking is a staple of malware detection schemes often used in commercial anti-virus software. In our work, we investigate how can use the GPU to augment our ability to find and detect malicious signatures with a payload or other binary fragment.

It's worth mentioning a significant advantage of using a shader is our ability to dynamically compile the shader and create a GPU program to perform the signature check. In other words, even within a third party application we can replace text or insert text into a database. This text, can be used in the form of a shader and would then be compiled when the application next launches, giving the application a mechanism for Over-The-Air updates or other less intrusive ways to modify an existing defense.

As an example of recognizing a malicious signature, let's look at the star jailbreak, developed by @comex. While the vulnerability has been fixed, we can still use this an example for signature checking. If we look at the payload using emacs and a hex editor (in this case, 0xED), we can quickly see the binary payload to trigger the exploit.

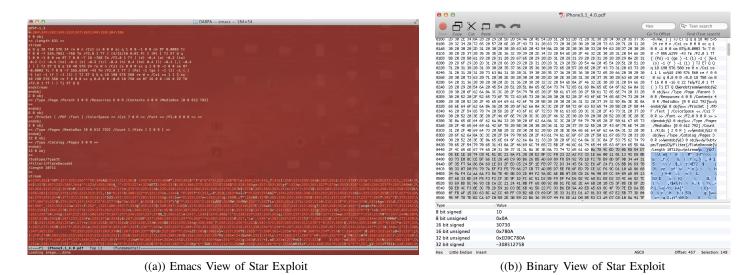


Fig. 1: Looking at an Attack Signature

The basic approach can be thought of as a sliding mask where we compare the result of texture operations. With this in mind, it's a relatively straightforward task to see if we can detect the binary signature of the exploit using a shader. The procedure can be outlined as:

- 1) Bind data, in the form of a payload or raw memory, to scan to texture
- 2) Bind signatures to masking texture atlas for testing
- 3) Use a shader to sweep our mask over the payload, using texture operations to signal for a match
- 4) If a suspicious entry, then inform/act

To test, on iOS for example we can use a method to create a texture from a payload.

Listing 1: Sample ObjC code to Generate Texture From File

```
// load a signature
  - (GLuint) loadSignatureFromFile: (NSString*)file
2
3
  {
                               = [[NSBundle mainBundle] pathForResource:file ofType:@"bin"];
       NSString * pathToFile
4
       NSData * contentsOfFile = [[NSData alloc] initWithContentsOfFile:pathToFile];
5
       NSUInteger numBytes = [contentsOfFile length];
6
       NSUInteger pow = [self fitPowerOf2:(4 * numBytes / 3 )] * 2;
8
9
       if ( pow > (dimension*dimension) )
10
11
           NSLog(@"File contents doesn't fit inside texture...");
12
           return 0;
13
       }
14
15
       unsigned char * data = (unsigned char*) calloc( sizeof(unsigned char) * dimension*←
16
           dimension*4, 1);
       unsigned char * buffer = (unsigned char*) [contentsOfFile bytes];
17
18
       NSUInteger index = 0;
19
       for (NSUInteger y = 0; y < dimension; y++ ) {</pre>
20
21
           for (NSUInteger x = 0; x < dimension; x++) {
22
                int byteIndex = (dimension*4 * y) + x * 4;
23
24
                if ( index > numBytes ) goto endloop;
               memcpy(&data[byteIndex], &buffer[index], 3);
25
                data[byteIndex+3] = 0xff;
26
                index += 3;
27
28
           }
29
30
   endloop:
31
32
33
       GLuint texName;
       glGenTextures(1, &texName);
34
       glBindTexture(GL_TEXTURE_2D, texName);
35
       glTexParameteri(GL_TEXTURE_2D, GL_TEXTURE_WRAP_S, GL_REPEAT);
36
       glTexParameteri(GL_TEXTURE_2D, GL_TEXTURE_WRAP_T, GL_REPEAT);
37
       glTexParameteri(GL_TEXTURE_2D, GL_TEXTURE_MIN_FILTER, GL_NEAREST);
38
       glTexImage2D(GL\_TEXTURE\_2D, 0, GL\_RGBA, dimension, dimension, 0, GL\_RGBA, \leftrightarrow
39
           GL_UNSIGNED_BYTE, data);
40
41
       return texName;
42
  }
```

Using this method we simply create two textures and perform difference operations to see if we can find the signature. The shader is extremely simple, simply subtracting the signature texture from the payload. If we watch the application, while within GDB, (and truncating some of @comex's payload) we can see the effect.

Listing 2: Signature Check on Star Exploit

```
// relevant part of frag shader
// offset is a uniform we control from the CPU, allowing us to "sweep"
gl_FragColor = texture2D( Texture, TexCoordOut ) - texture2D( Mask, TexCoordOut + offset );

// placing breakpoint immediately after:
glReadPixels(0, 0, dimension, dimension, GL_RGBA, GL_UNSIGNED_BYTE, bytes);
// offset (0,0)
```

```
10 (gdb) x/20 bytes
11 0x6f33000: 0xff000000 0xffac420f 0xff002047 0xff000000

      12
      0x6f33010:
      0xff090000
      0xff5b1a00
      0xff000000
      0xff1c002c

      13
      0x6f33020:
      0xff761900
      0xff080900
      0xff000001
      0xff250f00

14 0x6f33030: 0xff000e00 0xff0d0013 0xff432500
                                                                    0xff00005e
16
17
   // offset (x, 0)
   (qdb) x/20 bytes
18
   0x6f33000: 0xff070503 0xff000000 0xff000000 0xff000000
0x6f33010: 0xff000000 0xff000000 0xff000000
0x6f33020: 0xff000000 0xff000000 0xff000000
19
20
21
   0x6f33030: 0xff000000 0xff000000 0xff000000
                                                                    0xff000000
23 0x6f33040: 0xff000000 0xff520000 0xffc36bb4
                                                                    0xffbdc2b5
```

We can see that we quickly find a matching "black" region where the signature fragment matches the payload across those pixels. At this point the application could decide to dismiss the payload, labeling it as "suspicious" or run another malware detection scheme.

A. General Techniques

We can borrow graphical techniques to optimize our simple shader. Using techniques such as a texture atlas we can check for multiple signatures, not just the one we showed above, within a single texture fragment. In OpenGL we can even get periodic boundary (or repeating) boundary conditions making our texture atlas scheme more efficient.

Algorithm 1 Signature Checking Algorithm

```
1: texture ← payload-to-check
2: Create atlas of various signatures
3: second-texture ← atlas-of-signatures
4: draw-call initiates GPU calculation
5: for each texel do
6: The FOR loop is implicit as shader takes care of this
7: for each atlas region do
8: Frag Operation with i-th atlas region
9: end for
10: end for
```

An illustration serves best...

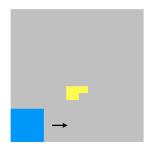
IV. MEMORY ANALYSIS

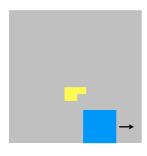
A common tactic for exploitation is a heap spray or some other data corruption. With access to dynamic memory we can investigate structures and other objects that may be compromised due to a bug, heap spray, and eventually lead to exploitation due to a NULL dereferencing or similar bug. This strategy is basically a generalization of a "guard", but we also mention that visualization and other runtime information could be gathered by using the GPU to analyze dynamic memory.

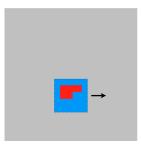
Let's take a quick look at an example of how we can set up our application to allow GPU integrity checking.

Listing 3: Simple Memory Object to Manage Dynamic Memory

```
class MemoryObject
```







- ((a)) We start sweeping our masking texture along the byte sequence
- ((b)) The mask continues along...
- ((c)) The mask encounters the signature as texture operations reveal an indicator

Fig. 2: We sweep a masking texture over our payload or other memory region, performing texture operations to find our signature

```
4
       unsigned char leading[8];
5
       vector<string> objects;
       // other objects
6
       unsigned char trailing[8];
7
8
9
        // static methods
10
       static void Generator(unsigned char ptr[8]) {
11
            static unsigned char start = 0;
12
            const unsigned char interval = 0x02;
13
            start += interval;
14
            for (int i=0; i<8; i++)</pre>
15
                ptr[i] = start;
16
17
18
19
   public:
20
21
       // constructor
22
       MemoryObject(void) { Generator(leading); objects.push_back("Testing"); Generator(←
23
           trailing); }
24
       // destructor
25
        ~MemoryObject(void) {
26
            memset(leading, '\0', 8);
27
            memset (trailing, ' \setminus 0', 8);
28
29
31
  };
```

To build our understanding, we start with a look at dynamic memory in a simple example.

Listing 4: Watching the Heap

```
(gdb) list 295
2
3 290
4 291 // populate the heap..
  292 - (void) populateHeap
5
   293 {
   294
             // current pointer to dynamic memory, here, I'll actually just use the first \leftrightarrow
       object I want to track.
   295
            heapPtr = (void*) malloc( 0 );
8
   296
9
   297
             // create a couple of objects following our pointer so that we can track them...
10
             first = new MemoryObject;
11
   2.98
   299
            second = new MemoryObject;
12
13
   (qdb) x/20 first
14
   0x6d40330: 0x02020202 0x02020202 0x06d40350 0x06d40354
15
   0x6d40340: 0x06d40354 0x04040404 0x04040404 0x00000000
16

        0x6d40350:
        0x06d3fc5c
        0x00000000
        0x00000000
        0x00000000

        0x6d40360:
        0x07b91364
        0x07b91364
        0x07b91378
        0x07b91378
        0x07b91378
        0x07b91378
        0x07b91378

21
22
   (qdb) x/20 second
   0x6d3fc70: 0x06060606 0x06060606 0x06d40460 0x06d40464
23
24 0x6d3fc80: 0x06d40464 0x08080808 0x08080808 0x00020000
25  0x6d3fc90:  0x012adb70  0x010012c0  0x01000009  0x00000002
26  0x6d3fca0:  0x00000000  0x00040000  0x012b0b40  0x06d3fcc0
27 0x6d3fcb0: 0x06d3f210 0x00000000 0x00000000 0x00000000
```

One of the advantages of this approach is that similar to a texture atlas we can map various regions of memory, they don't need to be contiguous! For the GPU, we can populate a texture, or several textures, based on the heap or some other pointers in memory and allow the GPU to look for object signatures, or pattern searches with differing memory locations that would verify memory integrity, and help protect against an attack such as a heap spray. For example if we find similar patterns in two differing locations on the heap this may signal a payload being sprayed.

While this may seem somewhat contrived to some readers, this is not an uncommon way to represent contiguous memory. LLVM (Low Level Virtual Machine) has a *MemoryObject*, which is used within memory transactions - here we take a simplistic representation and tie this into our cyber analysis. What this allows us to do is offload tracking dynamic memory objects during execution, with only minimal interaction to track changes.

A. Texture Atlas

In the above discussion we brought up the concept of an atlas, which is familiar to those in the graphics world where one image has several pieces that will be used in a game or other application, so for example a single image file may have a character in various poses to allow a single image to contain the "running" or other actions the character might have. Similar to a texture atlas we can mark various regions of memory (within limits) and store them to a texture. We can mark region boundaries with a byte signature, such as a single color channel, and within the region we store our more used functions, data or other models and references.

Returning to our example, in listing 4, the first and subsequent objects we create, have a leading and trailing signature we can find these using a fragment shader (discussed and shown later), we can at some point move these objects into a texture to watch them.

For example in the object,

```
(gdb) x/20 first
0x6d40330↔
: 0x02020202 0x02020202
                          0x06d40350 0x06d40354
0x6d40340↔
: 0x06d40354 0x04040404
                          0x04040404
                                     0x00000000
0x6d40350↔
: 0x06d3fc5c 0x00000000
                          0x0000000
                                     0x00030000
0x6d40360↔
: 0x07b91364
              0x07b91364
                          0x07b91000
                                     0x07b91378
0x6d40370↔
: 0x07b91378 0x07b91378 0x07b91378 0x07b91378
```

If we have a passthrough shader, then we can read back the pixel data and investigate the bytes after the draw call. A brief code snippet shows just how simple this can be.

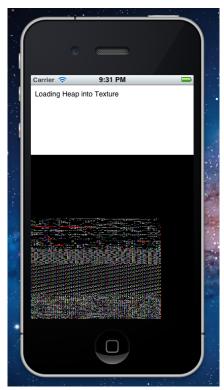


Fig. 3: Sample Screenshot of Memory as a Texture

Listing 5: GL code to read in our GPU results

```
1 // capture the data for analysis...
2 memset(bytes,'\0',dimension*dimension*4+1);
3 glReadPixels↔
(0, 0, 256, 256, GL_RGBA, GL_UNSIGNED_BYTE, bytes);
```

Listing 6: Simple GDB Capture of GPU results

```
1 (gdb) x/10 bytes
2 0xb7fb000←
    : 0xff020202 0xff020202 0xff204000 0xff06de5b
3 0xb7fb010←
    : 0xffde5b24 0xff5b2406 0xff0406de 0xff040404
4 0xb7fb020: 0xff040404 0xff000000
```

We have a signature beginning at 0x6d40330, followed by the start and end of the vector; string; iterators (start, and finish), and then finally we have our trailing signature marking the boundary of the object of interest. To use this in practice, we can add a destructor that alters these signatures and so mark the texture as having invalidated the object. With this in mind, if we test the texture for a valid object being manipulated then we can see that the manipulation will likely result in an invalid object being used, and a possible target for a heap spray.

Listing 7: Sample Shader that can test for a simple byte signature

```
varying lowp vec4 DestinationColor;
2
  varying lowp vec2 TexCoordOut;
3
  uniform sampler2D Texture;
4
5 uniform sampler2D Mask;
  uniform lowp vec2 offset;
  // assume we have our signature in four bytes or less...
  bool isValidSignature( lowp vec4 pixel )
9
10
  {
11
       lowp float norm;
       norm = dot(pixel.rgb , pixel.rgb );
12
       if ( norm > 0.0 )
13
14
         // for now we just see if the all RGB channels match, this means
15
         if ( pixel.r == pixel.g && pixel.g == pixel.b )
16
17
             return true:
18
19
       return false;
20
21
   // basic shader to operate
  // on multiple texture operations..
   void main(void) {
25
       if ( isValidSignature( texture2D(Texture, TexCoordOut) ) )
26
27
           gl_FragColor
                           = vec4(1, 0, 0, 1);
28
       }
29
30
       else
31
       {
                          = vec4( 0, 0, 0, 1 ) * texture2D( Texture, TexCoordOut);
           gl_FragColor
32
33
34
35
```

With this very simple shader we can see immediate results, as the leading and trailing signatures become red pixels, marked for analysis!

Listing 8: Simple Shader Identifies Signature as Red

```
1 (gdb) x/100 bytes
2 0x6f99000: 0xff0000ff 0xff00000f 0xff000000
3 0x6f99010: 0xff000000 0xff000000 0xff000000
4 0x6f99020: 0xff0000ff 0xff000000 0xff000000 0xff000000
```

So in this case, we know we can use a vectorized algorithm to find multiple signatures, markers or other interesting features within the region-of-interest.

V. DYNAMIC DISASSEMBLY

This is fairly straightforward in that we make the assumption that, simply put, if we better understand a payload, dataset, and other objects that may be executed than we can use higher level algorithms such as artificial intelligence and a variety of other techniques.

Inspired by the paper by [13], we decided that mobile systems might be a better target than the original idea applied to x86. This is because x86 has variable length instruction codes where as ARM (if we disregard Thumb for the moment) has a fixed instruction length.

A. Why This Works Even Better for Mobile

The approach we've taken works particularly well for mobile or ARM, because with our alpha, green, blue and red channels available in the texture, we have 4 bytes, or 32 bits. Well of course ARM instructions are 32 bit! Thumb is 16 and so for any instruction we can operate on both ARM and Thumb instruction inside a pixel of the fragment shader. This is in contrast to x86, with variable length instructions, which seriously challenges vectorization attempts of the algorithms on a GPU or other SIMD processor as we might have to deal with data loss. In other words, the convenience of an ARM instruction fitting into a texel is a very happy convenience and not something to take for granted with other instruction sets.

B. Implementation

To generate a table that we can include in our project we can build a variety of ARM codes, especially relevant to iOS/Android and then dump the text section to an instruction file which we can process for statistical relationships.

Listing 9: Script to grab ARM opcodes from Executables

```
#!/bin/bash
2
   # test to make sure the instructions directory is here..
3
  if [ ! -d "instruction-contents" ]; then
4
       echo "[+] Creating instruction contents directory"
5
       mkdir "instruction-contents"
6
  fi
7
8
9
  # otool -t simple | cut -d " " -f 2-4
10
11 for i in 'ls'
12 do
       type='file $i'
13
       needle='executable arm'
14
       if [[ "$type" == *"$needle"* ]]; then
15
           echo "Found arm executable -> $type"
16
           # parse out the file name we'll write to...
17
18
           fileprefix='echo $type | cut -d':' -f 1'
           filename="${fileprefix}.instr"
19
           'otool -t $fileprefix | cut -d " " -f 2-5 > "instruction-contents/${filename}"'
20
21
  done
```

With the instructions in a simple text file, we can create a Trie and a generic controller to find the opcodes, as well as the order of these instructions and create probabilistic tables of what likely instruction sequences should be. With our known opcodes we can construct a matrix to identify incoming operations. One item of interest is that we don't have to have this for the entire platform. For example if we were going to employ dynamic disassembly for a browser, we would want probability tables that are representative of what we should expect, so generate tables from WebKit source for example. If we started encountering lower probability sequences we might suspect that we have foreign code. With this in mind, let's explain the algorithm in a bit more detail.

We want to find the maximal probability of an opcode given the incoming instruction, or in matrix form, we have:

$$\begin{pmatrix} Op_{str} & 0 & 0 & 0 \\ 0 & Op_{add} & 0 & 0 \\ 0 & 0 & Op_{mov} & 0 \\ 0 & 0 & 0 & Op_{ldr} \end{pmatrix} \times \begin{pmatrix} inst_1 & inst_2 & inst_3 & inst_4 \\ inst_1 & inst_2 & inst_3 & inst_4 \\ inst_1 & inst_2 & inst_3 & inst_4 \\ inst_1 & inst_2 & inst_3 & inst_4 \end{pmatrix} = \begin{pmatrix} p_{1,str} & p_{2,str} & p_{3,str} & p_{4,str} \\ p_{1,add} & p_{2,add} & p_{3,add} & p_{4,add} \\ p_{1,mov} & p_{2,mov} & p_{3,mov} & p_{4,mov} \\ p_{1,ldr} & p_{2,ldr} & p_{3,ldr} & p_{4,ldr} \end{pmatrix}$$

To find our best match, or if perhaps our table is incomplete, we can take the maximal value for each row, which corresponds to the best matching operation, and if this value is above some threshold we declare that we "know" the opcode. If not, we declare we have an unknown operation.

To look at the instruction in a contextual sense, we look at patterns within subregions. First we can construct a transition matrix (details to follow) and use this to guess our unknown instructions, or we could look holistically at a "best fit" instruction giving known operations prior to and following an unknown.

The former case is fairly straightforward in that we walk our currently constructed sequence and when we reach a given unknown we multiply the against the transition matrix to obtain the most probable instruction path. The best way to make use of resources in this fashion is to populate a matrix of transitions, each row corresponding to a state entering transition, and then multiply this matrix against the transition matrix.

$$\begin{pmatrix} 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \end{pmatrix} \times \begin{pmatrix} p_{11} & p_{12} & p_{13} & p_{14} \\ p_{21} & p_{22} & p_{23} & p_{24} \\ p_{31} & p_{32} & p_{33} & p_{34} \\ p_{41} & p_{42} & p_{43} & p_{44} \end{pmatrix} = \begin{pmatrix} p_{31} & p_{32} & p_{33} & p_{34} \\ p_{21} & p_{22} & p_{23} & p_{24} \\ p_{41} & p_{42} & p_{43} & p_{44} \\ p_{11} & p_{12} & p_{13} & p_{14} \end{pmatrix}$$

In the latter case, we use the powers of the transition matrix in reverse.

State Transition

$$S \cdot P = S'_p$$
$$S \cdot P^2 = S''_p$$

But if we are only missing a single operation we know the outcome and hence some properties of $S_p^{"}$

This means we can look forwards or backwards

$$S \cdot P = S_p'' \cdot P^{-1}$$

This means that we can calculate the most likely path to our now known state, and deduce a reasonable approximation for our prior operation. Any component of S_p'' that doesn't include a path to our known operation can be eliminated, and then we take the maximal probability remaining as our accepted conclusion. Each column then represents either a known instruction or an equally weighted unknown. We can

then create a transition matrix, which contains our probabilities of the next instruction given the previous. We can then find our successive probability of a sequence by collapsing the matrix product, in essence giving us a "best match" over instruction paths.

Let's test this using a simple source and then using Python we'll remove various opcodes and let our disassemble predict the correct instruction.

Listing 10: Sample Disassembly

```
1 $ python simulate_dynamic_dissembly.py
  Read 44 instructions from the file sim.instructions
   replacing 0x3c
   with 0x00
   replacing 0xaf
   with Oxaf
  replacing 0x00
9
   with 0x00
10
11
  replacing 0x28
12
   with 0x28
13
14
15 replacing 0x58
   with 0x58
16
17
  replacing 0x08
18
   with 0x08
19
20
21 replacing 0x00
   with 0x00
22
  * * * 1 Errors in 8 unknown operations * * *
```

And the Python code to exercise our disassembler for testing and enhancements is straightforward.

Listing 11: Python Script to Exercise Disassembler

```
#!usr/env python
2
  import subprocess, random, simplejson
   # to install simplejson: pip install simplejson
6 # First we read in our instruction file that we want to use
7 filename = 'sim.instructions'
  instructions = open(filename).readlines()
  print "Read ", len(instructions), " instructions from the file ", filename
10
11 # now let's get rid of ~8 instructions (roughly 20% from our given file)
12 	 n = 8
random.seed(24) # for reproducability, if needed
14 samples = list(instructions)
15 for xx in xrange (0, n):
     index = random.randint(0,len(instructions)-1)
16
       samples[index] = 'xxx'
17
18
19 # Now we have a few unknown ops, let's use our C-code to on-the-fly generate
20 # disassembly using probability tables
21 last_instruction = False
22 application = './optimization'
23 args = ''
24 digraph = 'simple.digraph'
25 recreated = []
```

```
26 \quad index = 0
27 for instr in samples:
       if ( instr == 'xxx' ):
28
           # call out...
29
           try:
30
               prob_instruction = subprocess.check_output([application,last_instruction, ↔
31
                   digraph])
                instr = prob_instruction
32
                print "replacing %s with %s " % (instructions[index],instr)
33
           except:
34
               print "Error in our C-code, time to debug..."
35
       # append...
36
37
       recreated.append(instr)
       last_instruction = instr
38
       index+=1
39
40
   # Now we test our disassembly to see how much we got right...
41
   errors = 0
   for i in xrange(0,len(instructions)):
       if recreated[i] != instructions[i]:
45
           errors+=1
46
47 print "* * * ",errors," Errors in ",n," unknown operations * * *"
```

Listing 12: Main for creating disassembly tables

```
#include <iostream>
2 #include <algorithm>
3 #include <fstream>
   #include <unistd.h>
5 #include <getopt.h>
6 #include "trie.h"
   #include "util.h"
   #include "controller.h"
   #include "matrix.h"
11
using std::fstream;
  using std::cout;
13
  using std::endl;
14
15
   using std::string;
16
17
   int main( int argc, char *argv[] )
18
   {
19
20
      * Argument error check
21
      */
22
     if ( argc< 2 )
23
24
         cout << "Usage: " << argv[0] << " <infile> [additional file or file patterns]" << \leftrightarrow
25
             endl;
         exit(0);
26
       }
27
28
29
     * Parse argument
30
      */
31
     int option_index, c;
32
     bool dump_opcode = false;
33
     bool combine_files = false;
34
     bool gen_digraph = false;
35
     string outFileName = "temp.txt";
36
     string treeName
                      = "";
```

```
bool dump_all
                        = false;
38
39
     static struct option options[] = {
40
       {"opcodes", 0, 0, 0},
41
        {"outfile", 1, 0, 0},
42
       {"combine", 0, 0, 0},
43
       {"digraph",0,0,0},
44
       {"all",1,0,0},
45
        {NULL, 0, NULL, 0},
46
47
     };
48
49
     while( (c = getopt_long( argc, argv, "a:cgo:x", options, &option_index) ) != -1 )
50
          switch ( c )
51
52
     case 'a':
53
       cout << "Dumping all instructions.." << endl;</pre>
54
55
       dump_all = true;
       treeName = optarg;
56
57
       break;
     case 'x':
58
       cout << "opcode output..." << endl;</pre>
59
       dump_opcode = true;
60
       break:
61
     case 'o':
62
       cout << "outfile named..." << endl;</pre>
63
       outFileName = optarg;
64
       break;
65
     case 'c':
66
       cout << "combine files..." << endl;</pre>
67
       combine_files = true;
68
69
       break;
     case 'q':
70
       cout << "generating digraph..." << endl;</pre>
71
       gen_digraph = true;
72
       break;
73
74
     default:
       cout << "unknown " << c << endl;</pre>
75
       break;
76
77
79
80
81
82
83
     vector<string> files_to_process = parse_args( argc, argv );
84
     Controller * jobController = new Controller;
85
86
     Trie * tree;
87
       for (auto fstring = files_to_process.begin();
88
       fstring != files_to_process.end(); ++fstring)
89
90
     if (!jobController->ProcessFile( (*fstring) ) )
91
        cout << "Error processing file " << (*fstring) << endl;</pre>
92
     else {
93
       cout << "Processed file " << (*fstring) << endl;</pre>
94
       tree = jobController->TreeForFile( (*fstring) );
95
       cout << "Processed " << tree->NumberOfInstructions() << " instructions" << endl;</pre>
96
       cout << "Processed " << tree->NumberOfUniqueInstructions() << " unique instructions" \leftrightarrow
97
            << endl;
     }
98
99
100
          }
101
```

```
102
103
         * look across our tries
104
105
        size_t ninstructions = jobController->NumberOfUniqueInstructionAcrossTrees();
106
        cout << "Batch: " << ninstructions << " unique instructions" << endl;</pre>
107
        cout << "Writing to file: " << outFileName << endl;</pre>
108
109
        if ( dump_all )
110
111
          {
      if ( dump_opcode ) jobController->WriteOpcodes();
112
      jobController->ListAllInstructionsToFile( treeName, outFileName );
113
114
        else if ( dump_opcode && !gen_digraph )
115
116
      jobController->WriteOpcodes();
117
      cout << "Dumping opcodes - mode: " << jobController->Mode() << endl;</pre>
118
119
      jobController->ListInstructionsToFile( outFileName );
120
        else if ( gen_digraph )
121
122
123
      cout << "Writing DiGraph..." << endl;</pre>
      if (!jobController->WriteDiGraphToFile(outFileName))
124
        cout << "Unable to write digraph to " << outFileName << endl;</pre>
125
126
      else
        cout << "Written..." << endl;</pre>
127
128
          }
129
        else
130
      jobController->ListInstructionsToFile( outFileName );
131
132
        cout << "\n";
133
134
135
      * Cleanup
136
137
138
      delete jobController;
139
140
141
      return 0;
```

Using this code-set we can create a table of transitional probabilities.

C. In Practice

Dynamic disassembly is clearly best with floating point precision and other numerical routines available to the system. For example if floating point precision isn't available, in libraries such as Renderscript, Accelerate or in an optimized C++ library such as Eigen, then we have to be careful for signed/unsigned overflow operations and how to round our decimal representations to a byte, for example we could round 0.50 to 0x7f as an unsigned byte that can be used a fractional number. An example of how to perform generic matrix multiplication on the GPU follows.

Listing 13: Simple Shader for Square Matrix Multiplication

```
void main() {
// we invert, row <-> column
lowp vec2 transpose = TexCoordOut.ts;
lowp vec2 original = TexCoordOut.st;
// multiply the components of the two textures..
gl_FragColor = texture2D( matrix_one, original ) * texture2D( matrix_two, transpose );
}
```

With the above shader, if the diagonals RGB were 0xff and 0x7f, then the resultant fragment would have 0x7f along it's diagonals as well. It's simply $0.5 \cdot 1.0 = 0.5$. Keep in mind that we could perform the calculations on separate channels, giving us a separate range [0,1] for each of the four channels within RGBA.

VI. ENCRYPTION

Especially within mobile applications, live memory manipulation and capture has been done. On iOS, cycript can be used to inspect the objective-c runtime. In that case, we could use the GPU to vectorize our encryption process, as well as dynamically loading a new encryption scheme by updating the shader (OTA for example). A basic algorithm is outlined as:

Algorithm 2 Encryption

- 1: INPUT: Uniforms as keys, etc; Shaders as encryption methods
- 2: Bind data to be encrypted to texture
- 3: Link frag shader as a "block" encryption call
- 4: Draw call, renders encrypted data to off screen texture
- 5: if insufficient rounds then
- 6: use newly rendered texture as input to above
- 7: repeat
- 8: end if
- 9: if want to change block method then
- 10: link new frag shader
- 11: repeat using new shader program
- 12: **end if**
- 13: End

Employing encryption can help hide sensitive information from malware, or if signed we could follow where the encrypted payload moves throughout the operating system, performing a taint analysis with the encrypted data to minimize exposure.

VII. ENVIRONMENT CONSIDERATIONS

For simplicity's sake we consider beta algorithms for two environments and actually implement in one. The two environments are termed *restrictive*, meaning an environment where we are forced to play by the Vendor's policies; and *unrestrictive*, meaning an environment where we have control and are not forced to adhere to any vendor policies.

In the commercial environment we would expect developers and the vast majority of development efforts to work in the *restrictive* environment. This is contrast to the DoD, for example which could contract with a vendor for a particular Android version and include various mechanisms into the firmware, meaning an *unrestrictive environment*.

A. Restrictive Environments

Restrictive environments are the focus for the Milestone 3 deliverable as it demonstrates feasible approaches within the current marketplace. The details can be seen in the source code which is attached as part of the deliverable.

B. Unrestrictive Environments

An unrestrictive environment has a number of advantages, primarily being able to manipulate the kernel and graphics driver code for additional resource capabilities. For example, on Android this means that compiled shaders could be cached for reuse as needed, allowing an optimization not available within the SDK.

VIII. DEVICE AND PLATFORM INFORMATION

TABLE I: Device Characteristics

Characteristic	iOS (iPhone 4S)	ASUS Transformer Prime
Max. Number of Textures	8	16
Max. texture size	4096	2048
Version	OpenGL ES 2.0 IMGSGX543-63.24	OpenGL ES 2.0 14.01002
Vendor	Imagination Technologies	NVIDIA Corporation
Renderer	PowerVR SGX 543	NVIDIA Tegra 3
		GL_NV_platform_binary
		GL_OES_rgb8_rgba8
		GL_OES_EGL_sync
		GL_OES_fbo_render_mipmap
		GL_NV_depth_nonlinear
	CL OES death tenture CL OES death24	GL_NV_draw_path
	GL_OES_depth_texture GL_OES_depth24	GL_NV_texture_npot_2D_mipmap
	GL_OES_element_index_uint	GL_OES_EGL_image
	GL_OES_fbo_render_mipmap	GL_OES_EGL_image_external
	GL_OES_mapbuffer GL	GL_OES_vertex_half_float
	_OES_packed_depth_stencil	GL_OES_mapbuffer
	GL_OES_rgb8_rgba8	GL_NV_draw_buffers
	GL_OES_standard_derivatives GL_OES_texture_float GL_OES_texture_half_float GL_OES_texture_half_float_linear GL_OES_vertex_array_object GL_EXT_blend_minmax GL_EXT_color_buffer_half_float GL_EXT_debug_label GL_EXT_debug_marker GL_EXT_discard_framebuffer GL_EXT_occlusion_query_boolean GL_EXT_read_format_bgra GL_EXT_separate_shader_objects GL_EXT_shader_texture_lod GL_EXT_shadow_samplers GL_EXT_texture_filter_anisotropic GL_EXT_texture_rg GL_APPLE_framebuffer_multisample GL_APPLE_rgb_422 GL_APPLE_texture_format_BGRA8888 GL_APPLE_texture_max_level GL_IMG_read_format GL_IMG_texture_compression_pvrtc	GL_NV_multiview_draw_buffers
		GL_EXT_Cg_shader
		GL_EXT_packed_float
		GL_OES_texture_half_float
		GL_OES_texture_float
		GL_EXT_texture_array
		GL_OES_compressed_ETC1_RGB8_texture
Extensions		GL_EXT_texture_compression_latc
Extensions		GL_NV_texture_compression_latc
		GL_EXT_texture_compression_dxt1
		GL_EXT_texture_compression_s3tc
		GL_NV_texture_compression_s3tc
		GL_EXT_texture_filter_anisotropic
		GL_NV_get_tex_image
		GL_NV_read_buffer
		GL_NV_shader_framebuffer_fetch
		GL_NV_fbo_color_attachments
		GL_EXT_bgra
		GL_EXT_texture_format_BGRA8888
		GL_EXT_unpack_subimage
		GL_NV_pack_subimage
		GL_NV_texture_compression_s3tc_update
		GL_NV_read_depth GL_NV_read_stencil
		GL_EXT_robustness
		GL_OES_standard_derivatives
		GL_NV_EGL_stream_consumer_external
		GL_NV_coverage_sample
		GL_EXT_occlusion_query_boolean

As the reader can see from the demonstration, our example iOS code only uses a small texture as we want to create a demonstration application that allows for visualization of our intent and actions. However, in practice we have more resources at our disposal, as we can augment our 256 square texture to a 4096 $(x16^2)$ square texture as well as rendering off screen. In addition, one has to be careful with background interference. In our sample application we limit this by setting the alpha channel to 0xff(1.0f) and only using the RGB (Red/Green/Blue) channels for cyber tasks. This isn't a significant hurdle but something to be aware of.

IX. OPTIMIZATIONS AND OTHER TRICKS

A. Generalities

One advantage of the GPU environment is that many of the tricks that we might like, such as repeating boundaries (often necessary in numerical environments such as PDE's) are available via simple API calls. As we sweep our mask through the region-of-interest we can use the repeating background as a performance gain, meaning that we only need to sweep half-way through the region in some cases (it depends on sizing, etc).

There are other graphical techniques that might allow us to "zoom in" and out of various regions of interest. For example, if we take a block of memory to be executed, and let's assume for the moment that this is sufficiently large that we would need a compression method or perhaps mipmap leveling (or both!) to store as much as possible. If we focus on approximate disassembly, which is our basis for the idea, then if we choose a compression method which leaves enough of the high frequency instructions or opcodes then we can use our probabilistic detection schemes on the compressed data. The losses, or inaccuracies, would be independent, due to the nature of the design (because it's probabilistic!). As we said, if we thought we wanted to "zoom in" on a specific region, then we could do a vertex transformation to bring that mipmap level into view. Each region-of-interest could then be checked for signatures, investigated for instructions, etc.

B. Our Code

In our code we use off screen rendering to hold results in a texture that we can quickly process. The metaphor or a workspace of a texture is appropriate here, and so an engineer could decide whether certain applications might benefit from a larger workspace or more workspaces. Some very quick thoughts to provoke the reader are, for example in our two devices where the iPhone 4S has larger texture sizes available to us and the ASUS tablet has more textures, if we have a set of signatures we want to test against the the workspace it likely the higher value as we can tile the signatures and not have to exchange framebuffers and texture objects. In contrast if we have more complicated algorithms that might require intermediate staging then the number of textures might make ease the development process. An example here might be a complicated encryption scheme that uses a texture to hold intermediate results in a chained block cipher.

X. Performance

Performance can be investigated by running more developed, "Beta" quality applications, running GPU cyber tasks via the aforementioned off-screen rendering technique. Our two applications of choice were chosen to contrast how a developer might use the GPU; namely continuously for application monitoring via shader memory tacking or as single event to scan for malicious signatures after an event however costly that shot might be.

Figure 4 summarize the power cost of continuously monitoring an area of application memory, Figure 4(b), as well as a performance of a vectorized scan of a payload after download, Figure 4(a).

As we can see the cost of the both of these techniques is somewhat minimal and over repeated usage (on the order of tens of app launches) the power cost is minimal, comparable to other applications. Scanning a payload obviously has a time cost but we should remind the reader that we're not just able to scan a single signature in that time period, we're able to scan many. Speaking to monitoring memory via the GPU, this is another benefit of a mobile device in that we expect application usage to be brief, less than a minute and so with a simple OpenGL surface being rendered offscreen we would estimate the cost to be similar to a simple game. With the event driven model, the payload is scanned effectively, with a small cost to the setup of the OpenGL context and scanning, which involves sweeping our set of signatures-of-interest

¹For example, using a DCT, Direct Cosine Transform, generally causes a loss of high frequency or background information. This is not what our scheme would want as the background information, or opcodes give context to our guess as to what opcodes would be between

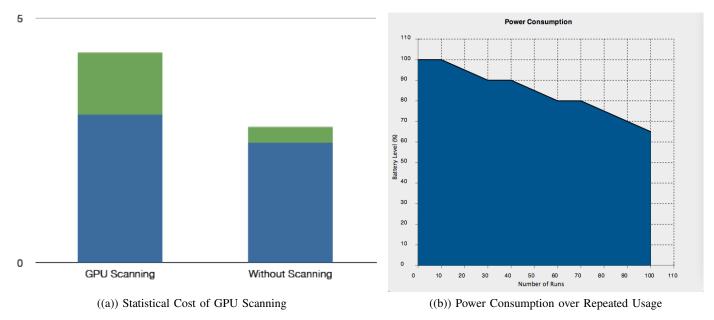


Fig. 4: Performance Characteristics

through the payload. The average time to load our payload is seen in blue (comparing with/without a scan) and the green represents a single standard deviation. The expected time increase is the cost of an additional operation and the variance can be understand in terms of when we find a signature-of-interest.

In summary, the performance costs are acceptable today and will likely be even more advantageous in upcoming years.

XI. HARDWARE INTEGRATION

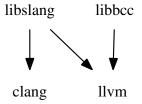
How vectorized processors are being used in every day applications is of great interest and the state of the art is currently Android's Renderscript. Renderscript uses LLVM to compile C99 code into LLVM bitcode (a .bc file) as well as Java reflection classes. These are all packaged as part of the apk and then the device itself will compile the bitcode into machine code as well as cache it for later use. As part of the OS, libbcc is the on-device compiler that will generate code for the GPU or DSP processor as well as integration glue for the Dalvik JVM [14].

For best results the ideal placement would be allow for "on-the-fly" alteration of the bitcode (this would necessitate a recache of the newly compiled machine code for ICS as this a feature to save on-load time) and then update the application, or system. From the operating system this should not be a problem and would allow Android to fully utilize the other processors for cyber tasks however this also means that code insertion could be that much easier for malicious tasks.

The call to libbcc and the related tasks are handled with the on device renderscript framework. Renderscript contains a runtime, which helps to bind variables to reflected Java classes from generated shaders or other GPU programs. The runtime has a related driver code which is linked to the libbcc code, this bridge allows for compilation, propagation of metadata and other information to pass between the JVM and GPU code. The actual device code is generated from within an ExecutionEngine, the details can be found within, frameworks/compile/libbcc/lib/ExecutionEngine/ see the files bcc.pp and Compiler.cpp for instance. It's of note that this is a heavily stripped down compiler as compared to many LLVM backend systems. The engine links the bitcode to system libraries as well as extract the necessary information to create vectorized code for the GPU/DSP. The process can be summarized in Figure 5, which is a slide from Liao Shih-wei's presentation at *Linux Foundation: Collab-Summit*[14], and outlines the interplay of bitcode and reflected Java for interplay.

Online JIT Compiler Flow GPUs/DSPs foo.bc libbcc ScriptC_foo.java System Library .bc files Multicore CPUs ScriptField_s.java Dalvik JIT App Java source files Google*

Fig. 5: Flow diagram of online JIT



APK file

Fig. 6: Relationships between LLVM and Renderscript Core Libs

llvm-rs-cc is a driver on top of libslang and is run on the host and performs many aggressive optimizations. As a result, libbcc on the device can be lightweight and focus on machine-dependent code generation for the input bitcode. It would be of interest to investigate the security of this lightweight compiler with obvious privileges. The architecture of libslang and libbcc is depicted in figure 6.

The renderscript runtime functions as you might expect in that it creates and binds vertex and frag programs to the GPU from the bitcode. The runtime is bound to the libbcc code, being able to extract meta data from the bcc (BitCode Compiler) as needed as well as creating generic GPU materials. For example, we see the following snippet within the runtime creating a vertex shader.

Listing 14: Code Fragment from rsdProgram.cpp within frameworks/base/libs/rs/driver

```
bool rsdProgramVertexInit(const Context *rsc, const ProgramVertex *pv,
                             const char* shader, uint32_t shaderLen) {
      RsdShader *drv = new RsdShader(pv, GL_VERTEX_SHADER, shader, shaderLen);
      pv->mHal.drv = drv;
5
      return drv->createShader();
```

8 }

These frameworks are non-existent in other parts of mobile platforms and there's reason to believe that like the desktop, capabilities and runtime access will be expanded for more general capabilities. Here again we can see the potential of a graphics based capability: a dynamic architecture capable of altering itself on the device; fast, powerful processing capabilities; with an increasingly broad set of capabilities.

While it's out of scope for this project, if there was sufficient time and resources the RenderScript framework could be used to allow OTA shader delivery that use GPU processing for cyber tasks. For a quality product this needs to be integrated with the hardware capabilities of the device but the framework does allow for shader caching and recall.

A. Shader Trends

The reason for the run-time compilation of shaders for both iOS and Android is to mitigate the device specifics, allowing the developer to work with the core technology, namely OpenGL ES, and not worry about the details and varieties of driver versions, etc. OpenGL developers have and do complain about not being to work directly with the driver and not being allowed to load the pre-compiled shader binaries directly into the graphics hardware. While allowing for binary access to a graphics chip and the device driver, residing in the kernel, may sound like a worrisome security choice it's important to note that the graphics industry is by far the largest market genre for revenue within the mobile application industry - in other words games mean money and this is highly influential. It's fairly easy to make the case that iOS's security measures have been largely brought about, not for device security but for an economic monopoly and therefore if money is a driver this is an area to watch in upcoming years.

XII. STRATEGIES

While each of these techniques adds some level of protection, if we want to truly gain substantially from GPU malware assistance, it's more likely to employ a combination of ideas. Let's explore some of this here.

As we know we can detect signatures, inspect memory and other relevant objects directly on the GPU, as well as take advantage of the GPU and other SIMD processors for analysis tasks such as dynamic disassembly. From these tasks we can start to form a strategy. For example, if we don't want to consume too much power, we could use the GPU/NEON processors while tethered (as power is not concern there and security is) to disassemble incoming instructions. When the browser is about to relinquish control to a Flash application or PDF viewer, then a burst of activity could be used to both perform some signature matching as well as set up memory tracking. Similarly if a password or other scheme is needed, it could be encrypted using the GPU.

Most significantly, these checks can be done by outside processing power, and dynamically. As shader can be compiled at runtime, and a new shader could be introduced simply be over-writing to an existing file and recompiling (which is typically done by simply restarting the parent process) this allows for a very flexible strategy that could be updated Over-The-Air (OTA).

XIII. GPU ASSISTED MALWARE

It's noteworthy that this same defensive strategy could be employed by an attacker for offensive capabilities. As we've proven that we do pattern searching, even if somewhat basic, along with dynamic disassembly then we can imagine constructing ROP gadgets for exploitation using similar techniques. While this is worrisome, the essence of the work is to increase the defensive surface and we've done this. Attackers will continue to be creative and come up with new exploitation methods and to play our part in the game we augment the resources at our disposal.

XIV. SUMMARY

This research shows that the GPU, as well as other processors, could be used for cyber tasks that could make attacks more difficult. While we have shown a series of algorithms and code that demonstrate how to do this, this is likely just a starting point. GPU and other SIMD processors will only get more powerful as mobile devices continue to evolve and in particular the demand for compelling user interfaces and high-end graphics. This means that while this paper might be state-of-the-art today, it will likely serve as an entry point for future research for tomorrow.

XV. LANDSCAPE AND LIKELY DEVELOPMENTS

Expanding on the minimal components we've discussed to this point can be extrapolated by looking at desktop SDK's such as CUDA and OpenCL. The same parallelization and general techniques are the core of these APIs but there are, of course, more access and control points in the desktop environment. However, one significant point to make is that in the development of heuristic algorithms, not only can we prototype using these more generalized APIs but we can also use them to train input or other peripheral sets that can be used by the GPU - for example using optimization algorithms for pseudo-disassembly, often uses probability tables which could be generated on any machine. By leveraging graphics kernels (operations) and data sets made for vectorized algorithms this offers a faster path to migrate general cyber algorithms to the GPU itself.

A. CUDA

CUDA is NVIDIA's offering to generalize GPU capabilities for tasks outside of graphics and rendering. A couple of key concepts that CUDA uses have become common place within the graphics community.

- kernel An operation to be applied throughout the geometry, in other words to all elements.
- Device and Host Memory interaction. CUDA has various calls to transfer memory from the host (assumed to be a PC) and the GPU.
- Thread Batching A batch of threads that operates with a limited amount of shared memory.

One of the noteworthy items of CUDA is the utilization of memory. Of particular interest is CUDA's Uniform Virtual Addressing (UVA) which uses the address as a way to detect which device houses a region of memory. This is designed to aid in scaling GPU tasks. CUDA hardware uses a set of SIMD multiprocessors with on-chip shared memory, the SIMD behavior can be grouped by threads into units called warps. Further details for hardware and software can be found at the following CUDA Overview [8].

A sample CUDA program can be seen in Listing 19 of Appendix B.

B. RenderScript

Android's migration to Honeycomb meant a leap forward in Renderscript, which now moves more towards a CUDA-like API - see http://android-developers.blogspot.com/2011/02/introducing-renderscript. html. This is another reason to pursue the current state of GPGPU research on the desktop. While a guess, it looks like Android will be an active research and development area for third party graphics and chip makers to investigate embedded and mobile development.

C. OpenCL

OpenCL is not solely dedicated to the GPU but more broadly about balancing the GPU and CPU to optimize performance. OpenCL takes advantage of vectorized libraries that will run efficiently on the GPU, a reason why many within the iOS community have concluded that OpenCL will come to iOS based on the libraries within the Accelerate Framework.

OpenCL also uses a terminology similar to CUDA, where a kernel is typically a name given to a parallelized operation. see Listings 20 and 21 within Appendix B for examples.

XVI. FUTURE RESEARCH

This project was inspired by the work others have shown in hacking embedded chips in cars, appliances and other electronics. The conversation sprung up in terms of what could be utilized to exploit or aid the CPU; naturally the GPU was and is the first option, but others are likely potential resources as well. NEON processors and similar vectorized, auxiliary capabilities will be available in future mobile devices.

XVII. CONCLUSION

This project has demonstrated that GPUs and other vectorized processors can play a vital role in cyber tasks for mobile devices. We have presented psuedo-algorithms and alpha implementations for a variety of tasks, such as signature checking, memory tracking, encryption, dynamic disassembly. We have demonstrated that the landscape for GPU driven cyber tasks is likely to provide more opportunity and that with the existing capabilities we can provide meaningful systems to mitigate malware and monitor the device. Research in this area is highly recommended and will likely continue to be driven by everincreasing GPU capabilities.

XVIII. ACKNOWLEDGMENT

The author is thankful to the following contributors: Alan Stone, Rob Dingwell and Seth Landsman of the *MITRE Corporation*. In addition the author wishes to thank Zachary Carlson of *Lycos*, Ayal Spitz of *PatientKeeper*, George Gal and Dan Rosenberg of *Virtual Security Research* and Thomas Cannon and Andrew Hoog of *viaForensics* for comments and thoughts as well as support regarding this project. We certainly want to thank *DARPA* for its support and interest in the research.

REFERENCES

- [1] Apple, Apple Guidelines for OpenGL ES https://developer.apple.com/library/ios/#documentation/3DDrawing/Conceptual/OpenGLES_ProgrammingGuide/BestPracticesforShaders/BestPracticesforShaders.html#//apple_ref/doc/uid/TP40008793-CH7-SW3
- [2] Ubergizmo, NVIDIA Tegra 3 http://www.ubergizmo.com/2011/11/nvidia-tegra-3/
- [3] Streamcomputing.eu, Is OpenCL coming to Apple iOS? http://www.streamcomputing.eu/wp-content/uploads/kalins-pdf/singles/is-opencl-coming-to-apple-ios.pdf
- [4] ARM Holdings, Mali Graphics Hardware http://www.arm.com/products/multimedia/mali-graphics-hardware/index.php
- [5] Qualcomm Developer Network, Adreno Graphics Processing Units https://developer.qualcomm.com/discover/chipsets-and-modems/adreno
- [6] NVIDIA, Meet 'CARMA' The CUDA on ARM Development Kit http://blogs.nvidia.com/2011/12/meet-carma-the-cuda-on-arm-development-kit/
- [7] anandtech.com Motorola Droid X: Thoroughly Reviewed http://www.anandtech.com/show/3826/motorola-droid-x-thoroughly-reviewed/5
- [8] Bastien Chopard, CUDA Overview http://cui.unige.ch/~chopard/GPGPU/2-cuda-overview.pdf
- [9] Davi, L., Sadeghi, A.R., Winandy, M. Sytem Security Lab Ruhr University Bochum, Germany ROPdefender: A Detection Tool to Defend Against Return-Oriented Programming Attacks
- [10] Polychronakis, M., Keromytis, A.D., Columbia University ROP Payload Detection Using Speculative Code Execution
- [11] Apple, Apple Guidelines for OpenGL ES https://developer.apple.com/library/ios/#documentation/3DDrawing/Conceptual/OpenGLES_ ProgrammingGuide/BestPracticesforShaders/BestPracticesforShaders.html#/apple_ref/doc/uid/TP40008793-CH7-SW3
- [12] G. Vasiliadis, M. Polychronakis, and S. Ioannidis GPU-Assisted Malware http://dcs.ics.forth.gr/Activities/papers/gpumalware.malware10.pdf
- [13] Shah, Abhishek, Approximate Diassembly using Dynamic Programming, 2010, Master's Projects. Paper 8. http://scholarworks.sjsu.edu/etd_projects/8
- [14] Shih-wei Liao, Linux Foundation: Collab-Summit, April 7th, 2011 https://events.linuxfoundation.org/slides/2011/lfcs/lfcs2011_llvm_liao.pdf

APPENDIX A EXAMPLE CODE

APPENDIX B PROJECT TEMPLATES

A. iOS

Apple's iOS is fairly straightforward to set up for GPU experimentation. The simplest form simply incorporates the OpenGLES framework.

Listing 15: Render Method for iOS project

```
1 - (void) render: (CADisplayLink*) displayLink
2 {
       glBlendFunc(GL_ONE, GL_ONE_MINUS_SRC_ALPHA);
3
       glEnable(GL_BLEND);
4
5
       glClearColor(0, 104.0/255.0, 55.0/255.0, 1.0);
6
       glClear( GL_COLOR_BUFFER_BIT | GL_DEPTH_BUFFER_BIT );
7
       glEnable(GL_DEPTH_TEST);
8
9
       float top = 320.0f;
10
       float bottom = 0.0f;
11
       float left = 0.0f;
12
       float right = 320.0f;
13
       float aspect = 1.0f; //(float) self.frame.size.width / (float) self.frame.size.height;
14
       float projection[16] = {
15
           2.0/(right-left), 0.0f, 0.0f, 0.0f,
16
           0.0f, 2.0f/((top-bottom) *aspect), 0.0f, 0.0f,
17
           0.0f, 0.0f, -2.0f/1000.0f, -3.0f,
18
           0.0, 0.0, 0.0, 1.0f
19
20
       float modelview[16] = {
21
         1.0f, 0.0f, 0.0f, 0.0f,
22
         0.0f, 1.0f, 0.0f, 0.0f,
23
         0.0f, 0.0f, 1.0f, 0.0f,
24
         0.0f, 0.0f, 0.0f, 1.0f
25
26
27
       GetGLError();
28
29
30
       glUniformMatrix4fv(_projectionUniform, 1, GL_FALSE,
31
32
                           projection);
       glUniformMatrix4fv(_modelViewUniform, 1, GL_FALSE,
33
34
                           modelview);
35
       GetGLError();
36
37
       glViewport(0, 0, self.frame.size.width, self.frame.size.width);
38
       glBindBuffer(GL_ARRAY_BUFFER, vertexBuffer);
39
       glBindBuffer(GL_ELEMENT_ARRAY_BUFFER, indexBuffer);
40
41
       GetGLError();
42
43
44
       qlVertexAttribPointer(_positionSlot, 2, GL_FLOAT, GL_FALSE, sizeof(Vertex), 0);
       qlVertexAttribPointer(_colorSlot, 4, GL_FLOAT, GL_FALSE, sizeof(Vertex), (GLvoid*)(←
45
           sizeof(float)*2) );
       glVertexAttribPointer(_texSlot, 2, GL_FLOAT, GL_FALSE, sizeof(Vertex),
46
                               (GLvoid*) (sizeof(float)*6) );
47
48
       GetGLError();
49
50
```

```
glActiveTexture(GL_TEXTURE0);
51
       glBindTexture(GL_TEXTURE_2D, texture);
52
       glUniform1i(sampler, 0);
53
54
       GetGLError();
55
56
57
       glDrawElements(GL_TRIANGLES, 6,
58
                        GL_UNSIGNED_BYTE, 0);
59
60
61
       // capture the data for analysis...
62
63
       glReadPixels(0, 0, 320, 480, GL_RGBA, GL_UNSIGNED_BYTE, bytes);
64
65
       // analyze our results
66
       [self analyzePixelData:bytes];
       [_context presentRenderbuffer:GL_RENDERBUFFER];
70
71
```

The key points, for experimentation is to setup a frame area, which could be offscreen but in this case is not, which can be easily scanned (via *glReadPixels*. Once this area is setup we can send various objects, in form of texture and vertex data to the GPU via draw calls while reading results.

Sample code will illustrate the finer details but before the reader does this, it's important to contrast this with a simple example using the Accelerate framework. This framework is used to vectorize various mathematical calculations, leveraging the GPU as well as the NEON processor.

Listing 16: Sample disassembly using the Accelerate framework

```
#include <stdio.h>
   #include <string.h>
   #include <math.h>
   #include <stdlib.h>
   #include <Accelerate/Accelerate.h>
   typedef struct {
    char **opcodes;
8
    unsigned int numberOpcodes;
9
10
    float * elements;
    unsigned int numberElements;
11
  } matrix;
12
13
natrix *read_matrix_from_file( const char * filename );
  void print_matrix( FILE * file, matrix * m );
15
  int destroy_matrix( matrix * m );
  void populate_vector( float * vector, int nels, char ** opcodes, char * code );
17
   void print_vector(float * result, int nels);
   void print_result(float * result, int nels, char **opcodes );
19
  char *choose_opcode( float * result, int nels, char **opcodes );
20
21
22 int main( int argc, char * argv[] )
23
  {
24
25
     * BLAS documentation
26
27
      * https://developer.apple.com/library/ios/#DOCUMENTATION/Accelerate/Reference/BLAS_Ref/↔
         Reference/reference.html
28
29
30
31
      * Here's a really good source...
```

```
* http://www.prism.gatech.edu/~ndantam3/cblas-doc/doc/html/cblas_8h.html
32
33
      */
34
    /*
35
     * doc's for LAPACK functions can be found at:
36
     * http://www.netlib.org/lapack/lug/node147.html#22228
37
      */
38
     int solve_for_instruction = 0;
39
     char * matrixfile = NULL;
40
     matrix * m; // = read_matrix_from_file( "matrix.txt" );
41
     // print_matrix( stdout, m );
42
43
    // destroy_matrix( m );
     if (argc == 3) {
46
       solve_for_instruction = 1;
47
       matrixfile = argv[2];
48
49
     } else if ( argc == 1 ) {
       matrixfile = NULL;
50
     } else {
51
       printf("Usage %s [previous opcode] [matrix file]\n",argv[0]);
52
53
       exit(1);
54
55
      _CLPK_integer info;
56
     float alpha = 1.0f;
57
     float beta = 1.0f;
58
59
     if ( solve_for_instruction )
60
61
       m = read_matrix_from_file( matrixfile );
62
63
        _CLPK_integer mn = (__CLPK_integer) m->numberOpcodes;
64
       float * vec, * result;
65
       vec = (float*) malloc( sizeof(float) * m->numberOpcodes );
66
       result=(float*)malloc( sizeof(float) * m->numberOpcodes );
67
68
       // populate the vector...
69
       populate_vector( vec, m->numberOpcodes, m->opcodes, argv[1] );
70
71
       cblas_sgemv( CblasColMajor, CblasNoTrans, mn, mn, alpha, m->elements,
       mn, vec, 1, beta, result, 1);
73
74
       char * chosen_opcode = choose_opcode( result, m->numberOpcodes, m->opcodes );
75
       printf("%s\n", chosen_opcode);
76
       // print_vector( result, m->numberOpcodes );
77
78
79
       free ( vec );
       free ( result );
80
81
       destroy_matrix( m );
82
       return 0;
83
     }
84
       matrix * a = read_matrix_from_file( "matrix.txt" );
85
       __CLPK_integer dim = (__CLPK_integer) a->numberOpcodes;
86
       float * testA
                         = a->elements;
87
88
       \__{CLPK\_integer n = 3;}
89
90
       float A[9] = \{ 2.0f, 0.0f, 0.0f, // first column \}
91
                       0.0f, -1.0f, 0.0f, // second column
92
                       0.0f, 0.0f, 2.0f };
93
94
       __CLPK_integer ipiv[3];
95
96
```

```
sgetrf_(&n, &n, A, &n, ipiv, &info);
97
        printf(" dim = %d, n = %d\n", dim, n);
98
        if ( info != 0 ) {
99
            printf("sqetrf failed with error code %d\n", (int) info);
100
            return 0;
101
102
103
        __CLPK_integer inc = 1;
104
105
        float c[3] = {1.0f, 1.0f, 1.0f };
106
        float b[3] = \{2.0f, -3.0f, 4.0f\};
107
108
109
        char transpose = 'N';
        __CLPK_integer nrhs = 1;
110
111
        sgetrs_(&transpose, &n, &nrhs, A, &n, ipiv, b, &n, &info);
112
        printf(" n = %d \setminus n", n);
113
        if ( info != 0 ) {
114
            printf("sgetrs failed with error code %d\n", (int) info);
115
            return 0;
116
117
118
        printf("b = [ f f f f f [n], b[0], b[1], b[2]);
119
        printf("c = [ f f f f f ] n", c[0], c[1], c[2]);
120
        printf("\n");
121
122
        printf(" testA * b \n");
123
        cblas_sgemv(CblasColMajor, CblasTrans, n, n, alpha, testA, n, b, 1, beta, c, 1);
124
        printf("x = [ %f %f %f ]\n",c[0],c[1],c[2]);
125
126
        printf(" b \n");
127
128
        printf("x = [ %f %f %f ]\n",b[0],b[1],b[2]);
129
        printf(" scaling b by 1.5 \n");
130
        cblas_sscal( n, 1.5f, b, 1 );
131
        printf("x = [ %f %f %f ]\n", b[0], b[1], b[2]);
132
133
        printf(" A * b \n");
134
        cblas_sgemv(CblasColMajor, CblasNoTrans, n, n, alpha, A, n, b, 1, beta, c, 1);
135
        printf("x = [ %f %f %f ]\n",c[0],c[1],c[2]);
136
        destroy_matrix( a );
        return 0;
138
139
   }
140
   matrix *read_matrix_from_file( const char * filename )
143
     FILE * fhandle = fopen( filename, "r" );
144
     if ( fhandle == NULL )
145
        return NULL;
146
147
     matrix * pmtrx = (matrix*) malloc( sizeof(matrix) );
148
     float num;
149
150
     // read in the first line, containing the opcodes...
151
     unsigned int commas = 0, linelength = 0;
152
     int c;
153
154
     while ( (c = fgetc(fhandle)) != EOF ) {
155
        linelength++;
156
        if ( c == ',' ) commas++;
157
158
        if ( c == '\n') break;
159
160
      // rewind the file...
```

```
rewind(fhandle);
162
      pmtrx->numberOpcodes = commas+1;
163
      pmtrx->numberElements= pmtrx->numberOpcodes * pmtrx->numberOpcodes;
164
      pmtrx->opcodes = (char**) malloc( sizeof(char*) * pmtrx->numberOpcodes );
165
      pmtrx->elements= (float*) malloc( sizeof(float) * pmtrx->numberElements );
166
167
      char * cptr, * line = (char*) malloc( sizeof(char) * linelength );
168
      fgets( line, linelength, fhandle );
169
170
      // read in our opcdoes...
171
172
      cptr = strtok( line, "," );
173
      c = 0;
174
      while ( cptr != NULL )
175
          pmtrx->opcodes[c++] = strdup( cptr );
176
           cptr = strtok(NULL, ", ");
177
178
179
180
      free( line );
181
      c = 0;
182
183
      while ( fscanf(fhandle, " %f ", &num) )
184
           pmtrx->elements[c++] = num;
185
          if ( c == pmtrx->numberElements )
186
      break;
187
188
189
      fclose(fhandle);
190
      // done...
191
192
193
      return pmtrx;
194
195
196
    void print_matrix( FILE * file, matrix * m )
197
198
199
      int i, j;
      int num = (int) sqrtf( m->numberElements );
200
      for (i=0; i<m->numberOpcodes; i++)
201
        fprintf(file, " %8s ", m->opcodes[i] );
      fprintf(file, "\n");
      for (i=0; i<num; i++)</pre>
205
206
207
           for (j=0; j<num; j++)</pre>
      fprintf(file, " %8f ", m->elements[i*num+j]);
208
           fprintf(file, "\n");
209
210
211
    }
212
    int destroy_matrix( matrix * m )
213
214
    {
      int i;
215
      for (i=0; i<m->numberOpcodes; i++)
216
        free( m->opcodes[i] );
217
218
      free( m->elements );
219
      free( m->opcodes );
220
221
      m->numberOpcodes = 0;
222
223
      m->numberElements= 0;
224
      m = NULL;
225
```

```
return 0;
227
228
   }
229
    void populate_vector( float * vector,
230
               int nels,
231
               char ** opcodes,
232
               char * code )
233
234
      int i;
235
      for (i=0; i<nels; i++) {</pre>
236
237
        if ( strncmp(opcodes[i], code, 4) == 0 )
          vector[i] = 1.0f;
238
239
           vector[i] = 0.0f;
240
241
      // end of function
242
243
244
245
    void print_vector(float * result, int nels)
246
247
248
      int i;
      for (i=0; i<nels; i++)</pre>
249
        printf(" %8f ",result[i]);
250
251
252
    void print_result(float * result, int nels, char **opcodes )
253
254
      int i;
255
     // print out json result, can import into python easily...
256
      printf("{");
257
      for ( i=0; i<nels; i++ )</pre>
258
259
           if ( result[i] > 0.0f )
260
      printf("'%s':%f,",opcodes[i],result[i]);
261
262
      printf("}");
263
    }
264
265
266
267
    char *choose_opcode( float * result, int nels, char **opcodes )
268
      // get a rand num [0,1]
      float num = ( (float) rand() / (float) RAND_MAX );
270
271
     float sum = 0.0f;
272
      int i;
      for (i=0; i<nels; i++)</pre>
273
274
275
           sum += result[i];
           if ( num < sum )</pre>
276
277
      return opcodes[i];
278
      return NULL;
279
280
   }
```

Note the use of the BLAS routines, for matrix calculations. The *cblas* routines allow hardware acceleration, allowing us to leverage SIMD processors.

B. Android

```
2 #include <stdlib.h>
3 #include <math.h>
5 #include <jni.h>
6 #include <android/log.h>
7 #include <GLES2/gl2.h>
8 #include <GLES2/gl2ext.h>
10 #include <sys/types.h>
#include <android/asset_manager.h>
#include <android/asset_manager_jni.h>
#include <assert.h>
   #define LOG_TAG "libgls12"
15
   #define LOGI(...) __android_log_print(ANDROID_LOG_INFO,LOG_TAG,__VA_ARGS__)
16
   #define LOGE(...) __android_log_print(ANDROID_LOG_ERROR,LOG_TAG,__VA_ARGS__)
   static void printGLString( const char *name, GLenum s )
20
21
     const char *v = (const char*) glGetString(s);
22
23
     LOGI("GL %s = %s\n", name, v);
24
25
  static void checkGlError( const char * op )
26
27
     GLint error;
28
    for (error = glGetError(); error; error = glGetError() ) {
29
       LOGI("after %s() glError (0x%x)\n", op, error );
30
31
  }
32
33
34 int screenWidth;
35 int screenHeight;
36
  int analyzeFrameBuffer( unsigned char * data )
37
38
  {
     int i, j;
39
     FILE * tmpfile = fopen("/sdcard/tmp/results.txt","w");
40
     if ( tmpfile == NULL )
41
42
       return 1;
43
     for (i=0; i<screenHeight; i++)</pre>
44
45
         for (j=0; j<screenWidth; j++)</pre>
46
47
       fprintf(tmpfile, " %c ", data[i*screenWidth+j]);
48
49
     }
         fprintf(tmpfile, "\n");
50
51
     LOGI("WROTE GPU buffer to file");
52
    fclose( tmpfile );
53
54
55
     return 0;
56
  }
57
  GLuint loadShader(GLenum shaderType, const char* pSource) {
58
   GLuint shader = glCreateShader(shaderType);
59
     if ( shader ) {
60
       glShaderSource(shader, 1, &pSource, NULL);
61
       glCompileShader( shader );
62
       GLint compiled = 0;
63
       glGetShaderiv(shader, GL_COMPILE_STATUS, &compiled);
64
65
       if (!compiled) {
         GLint infoLen = 0;
66
```

```
glGetShaderiv(shader, GL_INFO_LOG_LENGTH, &infoLen);
67
          if ( infoLen ) {
68
     char * buf = (char*) malloc( infoLen );
69
     if ( buf ) {
70
        glGetShaderInfoLog(shader,infoLen,NULL,buf);
71
        LOGE("Could not compile shader %d:\n%s\n", shaderType, buf);
72
        free (buf);
73
74
     glDeleteShader(shader);
75
     shader = 0;
76
77
78
79
     return shader;
80
81
82
   GLuint createProgram( const char * pVertexSource, const char * pFragmentSource )
83
     GLuint vertexShader = loadShader( GL_VERTEX_SHADER, pVertexSource );
85
     if (!vertexShader) {
86
87
       return 0;
88
89
     GLuint pixelShader = loadShader( GL_FRAGMENT_SHADER, pFragmentSource );
90
     if (!pixelShader) {
91
       return 0;
92
93
94
     GLuint program = glCreateProgram();
95
     if ( program ) {
96
        glAttachShader(program, vertexShader);
97
98
        checkGlError("glAttachShader");
        glAttachShader(program, pixelShader);
99
       checkGlError("glAttachShader");
100
        glLinkProgram(program);
101
        GLint linkStatus = GL_FALSE;
102
103
        glGetProgramiv(program, GL_LINK_STATUS, &linkStatus);
        if ( linkStatus != GL_TRUE ) {
104
          GLint bufLength = 0;
105
          glGetProgramiv(program, GL_INFO_LOG_LENGTH, &bufLength);
106
          if ( bufLength ) {
     char * buf = (char*) malloc( bufLength );
     if ( buf ) {
        glGetProgramInfoLog(program, bufLength, NULL, buf);
110
111
        LOGE ("Could not ink program: \n%s\n", buf);
112
        free (buf);
113
114
          glDeleteProgram(program);
115
          program = 0;
116
117
118
     }
     return program;
119
120
121
   }
122
   GLuint gProgram;
123
   GLuint gvPositionHandle;
124
125
   char *readShader( const char *filename )
126
127
   {
128
129
130
   char * gVertexShader;
```

```
char * gFragmentShader;
132
133
   int setupGraphics(int w, int h) {
134
135
     printGLString("Version", GL_VERSION);
136
     printGLString("Vendor",GL_VENDOR);
137
     printGLString("Renderer",GL_RENDERER);
138
     printGLString("Extensions",GL_EXTENSIONS);
139
140
     screenWidth = w;
141
142
     screenHeight= h;
143
144
     LOGI ("setupGraphics (%d, %d)", w, h);
     gProgram = createProgram( gVertexShader, gFragmentShader );
145
     if ( !gProgram ) {
146
        LOGE ("Could not create program.");
147
        return 0;
148
149
150
     gvPositionHandle = glGetAttribLocation(gProgram, "vPosition");
151
      checkGlError("glGetAttribLocation");
152
153
     LOGI("glGetAttribLocation(\"vPosition\") = %d\n", gvPositionHandle);
154
     glViewport(0, 0, w, h);
155
     checkGlError("glViewport");
156
     return 1;
157
158
159
160
   const GLfloat gVertices[] = {0.0f, 0.5f, -0.5f, -0.5f, 0.5f, -0.5f };
161
162
163
   void renderFrame() {
164
     unsigned char * bufferdata;
165
166
     glClearColor(0,0,0,1.0f);
167
     checkGlError("glClearColor");
168
     glClear( GL_DEPTH_BUFFER_BIT | GL_COLOR_BUFFER_BIT );
169
     checkGlError("glColor");
170
171
     glUseProgram(gProgram);
     checkGlError("gluseProgram");
173
174
     glVertexAttribPointer( gvPositionHandle, 2, GL_FLOAT, GL_FALSE, 0, gVertices );
175
     checkGlError("glVertexAttribPointer");
176
177
     glEnableVertexAttribArray(gvPositionHandle);
     checkGlError("glEnableVertexAttribArray");
178
     glDrawArrays(GL_TRIANGLES, 0, 3);
179
     checkGlError("glDrawArrays");
180
181
     LOGI("Called draw functions");
182
     LOGI("Screen width, height are %d, %d", screenWidth, screenHeight);
183
     // post draw call, we call read pixels to obtain our result
184
     // note that we need depth of 4\ldots
185
     bufferdata = (unsigned char*) malloc( sizeof(unsigned char) * screenWidth * screenHeight↔
186
           * 4 );
     qlReadPixels( 0, 0, screenWidth, screenHeight, GL_RGBA, GL_UNSIGNED_BYTE, bufferdata );
187
188
     LOGI("Read Pixels");
189
190
     // pass this buffer data for analysis...
191
192
     analyzeFrameBuffer( bufferdata );
     LOGI("Analyzed...");
193
194
195
     // clean up
```

```
free ( bufferdata );
196
197
198
    }
199
200
      JNIEXPORT void JNICALL Java_com_gototheboard_glsl2_NativeGLLib_init(JNIEnv * env, \leftrightarrow
201
          jobject obj,
                            jint width, jint height);
202
      JNIEXPORT void JNICALL Java_com_gototheboard_glsl2_NativeGLLib_step(JNIEnv * env, \leftrightarrow
203
          jobject obj );
      JNIEXPORT void JNICALL Java_com_gototheboard_glsl2_NativeGLLib_setupShaders(JNIEnv * env↔
204
205
                              jstring vshader,
                              jstring fshader);
206
207
208
    JNIEXPORT void JNICALL Java_com_gototheboard_nativegl_NativeGLLib_init(JNIEnv * env, \leftarrow
209
        jobject obj,
                            jint width, jint height)
210
211
      LOGI("Setting up native graphics code.");
212
213
      setupGraphics( width, height );
214
      LOGI ("Finished setting up native graphics code.");
215
216
   JNIEXPORT void JNICALL Java_com_gototheboard_nativegl_NativeGLLib_step(JNIEnv * env, ←
217
        jobject obj )
218
      LOGI("Step...\n");
219
      renderFrame();
220
   }
221
222
    JNIEXPORT void JNICALL Java_com_gototheboard_nativegl_NativeGLLib_setupShaders(JNIEnv * ↔
223
        env, jobject obj,
                            jobject assetManager,
224
                            jstring vshader,
225
                            jstring fshader)
226
227
    {
      int vid, fid;
228
      off_t start, length;
229
230
      LOGI("Starting to read shaders...");
232
      const jbyte * utf_vshader = (*env)->GetStringUTFChars( env, vshader, NULL );
233
      const jbyte * utf_fshader = (*env)->GetStringUTFChars( env, fshader, NULL );
234
235
236
      LOGI("Created UTF strings..");
237
      // create an asset manager
238
      AAssetManager * mgr = AAssetManager_fromJava(env, assetManager);
239
240
      assert (NULL != mgr );
      if ( mgr == NULL )
241
        LOGI("Unable to create asset manager");
242
243
        LOGI ("Created asset manager");
244
245
      // read in vertex fragment
246
      LOGI("Going to read %s", (char*)utf_vshader);
247
      LOGI("Going to read %s", (char*)utf_fshader);
248
      AAsset * vertex_asset = AAssetManager_open( mgr, (const char*) utf_vshader, ←
249
          AASSET_MODE_UNKNOWN );
      AAsset * fraq_asset
                              = AAssetManager_open( mgr, (const char*) utf_fshader, ←
250
          AASSET_MODE_UNKNOWN );
251
      LOGI ("Created asset objects");
      if ( vertex_asset == NULL )
```

```
LOGI("Vertex asset could not be opened.");
253
     if ( frag_asset == NULL )
254
       LOGI("Frag asset could not be opened.");
255
256
     // release the Java string and UTF
257
     (*env) ->ReleaseStringUTFChars( env, vshader, utf_vshader );
258
      (*env)->ReleaseStringUTFChars( env, fshader, utf_fshader );
259
     LOGI("Released UTF8 strings");
260
261
     // get file descriptor -- failing here with the vid = -1
262
     vid = AAsset_openFileDescriptor(vertex_asset, &start, &length );
263
     LOGI("vid = %d", vid);
264
265
     assert( 0 <= vid );
     gVertexShader = (char*) malloc( sizeof(char) * length + 1 );
266
     memset (gVertexShader, '\0', length+1);
267
     AAsset_read( vertex_asset, gVertexShader, length );
268
     AAsset_close(vertex_asset);
269
270
     LOGI("Read in vertex shader:\n %s\n",gVertexShader);
271
     fid = AAsset_openFileDescriptor( frag_asset, &start, &length );
     assert( 0 <= fid );
273
274
     gFragmentShader = (char*) malloc( sizeof(char) * length + 1 );
     memset (gFragmentShader, '\0', length+1);
275
     AAsset_read( frag_asset, gFragmentShader, length );
276
     AAsset_close(frag_asset);
277
     LOGI("Read in frag shader:\n %s\n",gFragmentShader);
278
279
280
```

To use the eigen library, a similar effort to leverage the NEON processor, we must include the C++ libraries, which is fairly trivial but does require an *Application.mk* file

Listing 18: To Move into Vectorized C++ for the NEON Processor

```
1 APP_STL:=stlport_static
```

Along with the inclusion of the Eigen header files (Eigen is enirely made up of header files) allows us to better leverage vectorized calculations.

Listing 19: "Cuda Example Program"

```
#include <cuda.h>
   #include <cuda_runtime.h>
   #include <driver_types.h>
   #include <stdio.h>
5
    nvcc square.cu -o square
  results in:
  0 0.000000
10
11 1.000000
12 2 4.000000
13 3 9.000000
14 4 16.000000
15 5 25.000000
16 6 36.000000
7 49.00000
18 8 64.000000
9 81.000000
20
  */
21
22
```

```
23 __global__ void
24 square_array(float *a, int N) {
     int idx = blockIdx.x * blockDim.x + threadIdx.x;
25
     if ( idx < N ) a[idx] = a[idx] * a[idx];
26
27
28
  int main( void ) {
29
    float *a_h, *a_d;
30
     int i;
31
     const int N = 10;
32
    size_t size = N * sizeof(float);
33
     a_h = (float*) malloc( size );
34
35
     cudaMalloc( (void**) &a_d, size );
36
     // initialize and send to device
37
     for (i=0; i<N; i++)</pre>
38
       a_h[i] = (float) i;
39
40
     cudaMemcpy(a_d, a_h, size, cudaMemcpyHostToDevice);
41
     // do the device calculation
42
     int block_size = 4;
43
44
     int n_blocks = N / block_size + ( N % block_size == 0 ? 0 : 1 );
     square_array<<<n_blocks, block_size>>>(a_d,N);
45
46
     // get result
47
     cudaMemcpy(a_h,a_d, sizeof(float)*N, cudaMemcpyDeviceToHost);
48
49
     // print the results
50
     for (int i=0; i<N; i++)</pre>
51
       printf("%d %f\n",i,a_h[i]);
52
53
     free(a_h);
54
     cudaFree (a_d);
55
56
     return 0;
57
  }
58
```

OpenCL is part of the developer's environment for Mac OS X; and is part of the XCode download.

```
Listing 20: "OpenCL Example"
#include <fcntl.h>
2 #include <stdio.h>
3 #include <stdlib.h>
4 #include <string.h>
5 #include <math.h>
6 #include <unistd.h>
7 #include <sys/types.h>
8 #include <sys/stat.h>
  #include <OpenCL/opencl.h>
9
10
11
   * minimal OpenCL program... to build (on Mac OS X (lion)
12
   * g++ example.cpp -o example -framework OpenCL
13
14
   * Execution results in squaring of the input array [0,1,2,3...]
15
   * Becomes [0,1,4,9,...]
16
17
18
   // simple example, just use static data size
19
20
   #define DATA_SIZE 1024
21
  // simple kernel that computes the square of an input array
23 const char *KernelSource = "\n" \
                                                        \n" \
   "__kernel void square(
```

```
__global float* input,
                                                          \n" \
25
         __global float* output,
                                                          \n" \
26
         const unsigned int count)
                                                          \n" \
27
                                                          \n" \
28
         int i = get_global_id(0);
                                                          \n" \
29
         if ( i < count )
                                                          \n" \
30
             output[i] = input[i]*input[i];
                                                          \n" \
31
     " }
                                                          \n" \
32
     "\n";
33
34
  // main
35
36
37 int main(int argc, char *argv[] )
38
     int err; // error code
39
40
     float data[DATA_SIZE];
41
     float results[DATA_SIZE];
     unsigned int correct;
43
44
45
    size_t global;
46
    size_t local;
47
    cl_device_id device_id;
48
    cl_context context;
49
     cl_command_queue queue;
50
     cl_program program;
51
    cl_kernel kernel;
52
53
    cl_mem input;
54
    cl_mem output;
55
56
    // get data to operate on...
57
    int i=0;
58
     unsigned int count = DATA_SIZE;
59
     for (i=0; i<count; i++)</pre>
60
       data[i] = i;
61
62
     // get an id for the device
63
     int gpu = 1;
64
     err = clGetDeviceIDs(NULL, gpu ? CL_DEVICE_TYPE_GPU : CL_DEVICE_TYPE_CPU,
            1, &device_id, NULL);
     if ( err != CL_SUCCESS )
68
69
70
         printf("Unable to obtain device id\n");
71
         return -1;
72
73
     // create device context
74
    context = clCreateContext(0,1,&device_id,NULL,NULL,&err);
75
     if (!context)
76
77
         printf("Unable to create device context\n");
78
         return -2;
79
80
81
     // create queueing context
82
     queue = clCreateCommandQueue(context, device_id, 0, &err);
83
     if ( !queue )
84
85
         printf("Unable to create CL queue\n");
86
         return -3;
87
88
89
```

```
// create program
90
      program = clCreateProgramWithSource(context, 1,
91
                 (const char * *) & Kernel Source,
92
                NULL, &err);
93
94
      // build the program executable
95
      err = clBuildProgram(program, 0, NULL, NULL, NULL, NULL);
96
      if ( err != CL_SUCCESS )
97
98
          size_t len;
99
          char buffer[1024];
100
101
102
          printf("Unable to build CL program\n");
103
          clGetProgramBuildInfo(program, device_id, CL_PROGRAM_BUILD_LOG,
104
              sizeof(buffer), buffer, &len);
105
          printf("%s\n",buffer);
106
          return -4;
108
109
110
111
      // create the kernel
112
     kernel = clCreateKernel( program, "square", &err );
113
     if (!kernel || err != CL_SUCCESS )
114
115
          printf("Unable to create kernel\n");
116
          return -5;
117
118
119
      // create input and output arrays in device memory..
120
121
      input = clCreateBuffer(context, CL_MEM_READ_ONLY,
           sizeof(float) * count,
122
           NULL, NULL);
123
      output = clCreateBuffer(context, CL_MEM_WRITE_ONLY,
124
            sizeof(float) * count,
125
            NULL, NULL );
126
127
      if (!input || !output )
128
129
          printf("Failed to create input or output streams\n");
          return -6;
131
132
133
      // write our inputs into device memory...
134
135
      err = clEnqueueWriteBuffer( queue, input, CL_TRUE, 0,
                 sizeof(float) * count,
136
                data, 0, NULL, NULL);
137
      if ( err != CL_SUCCESS )
138
139
          printf("Unable to write inputs to device\n");
140
          return -7;
141
142
143
     // set arguments
144
     err = 0;
145
     err = clSetKernelArg(kernel, 0, sizeof(cl_mem), &input );
146
      err |= clSetKernelArg(kernel, 1, sizeof(cl_mem), &output );
147
      err |= clSetKernelArg(kernel, 2, sizeof(unsigned int), &count);
148
      if ( err != CL_SUCCESS )
149
150
          printf("Unable to set kernel arguments\n");
151
          return -8;
152
153
154
```

```
// get maximum work-group size...
155
     err = clGetKernelWorkGroupInfo( kernel, device_id,
156
              CL_KERNEL_WORK_GROUP_SIZE,
157
              sizeof(size_t), &local, NULL);
158
     if ( err != CL_SUCCESS )
159
160
          printf("Unable to obtain maximum work group size\n");
161
          return -9;
162
163
164
     // execute kernel over range of data set!
165
     global = count;
166
167
     err = clEnqueueNDRangeKernel(queue, kernel, 1, NULL,
            &global, &local, 0, NULL, NULL);
168
169
     // wait fo rthe command queue to get servied before reading
171
172
     // back results
     clFinish (queue);
173
174
175
176
     // read results from the device...
     err = clEnqueueReadBuffer(queue, output, CL_TRUE, 0,
177
              sizeof(float) *count, results,
178
              0, NULL, NULL);
179
     if ( err != CL_SUCCESS )
180
181
          printf("Unable to read results back from device\n");
182
          return -10;
183
184
185
186
     printf("results[1]
                                = f\n", results[1]);
                                = %f\n", results[10]);;
     printf("results[10]
187
     printf("results[11]
                                = %f\n", results[11]);
188
189
     // shut down and clean up...
190
191
     clReleaseMemObject(input);
     clReleaseMemObject(output);
192
     clReleaseProgram(program);
193
     clReleaseKernel(kernel);
194
     clReleaseCommandQueue(queue);
     clReleaseContext(context);
197
     return 0;
198
199
200
   }
```

Listing 21: "OpenCL via Python"

```
#!/bin/env python
import pyopencl as cl
import numpy
import numpy.linalg as la

a = numpy.random.rand(50000).astype(numpy.float32)
b = numpy.random.rand(50000).astype(numpy.float32)

ctx = cl.create_some_context()
queue = cl.CommandQueue(ctx)

mf = cl.mem_flags
a_buf = cl.Buffer(ctx, mf.READ_ONLY | mf.COPY_HOST_PTR, hostbuf=a)
b_buf = cl.Buffer(ctx, mf.READ_ONLY | mf.COPY_HOST_PTR, hostbuf=b)
dest_buf = cl.Buffer(ctx, mf.READ_ONLY | mf.COPY_HOST_PTR, hostbuf=b)
dest_buf = cl.Buffer(ctx, mf.READ_ONLY | mf.COPY_HOST_PTR, hostbuf=b)
```

```
16
prg = cl.Program(ctx, """
     __kernel void sum(__global const float * a,
18
      __global const float *b, __global float *c)
19
20
         int gid = get_global_id(0);
21
22
         c[gid] = a[gid] + b[gid];
23
      """).build()
24
25
26 prg.sum( queue, a.shape, None, a_buf, b_buf, dest_buf )
27
28 a_plus_b = numpy.empty_like(a)
29 cl.enqueue_read_buffer(queue, dest_buf, a_plus_b).wait()
30 print la.norm(a_plus_b - (a+b))
```