Test platform introduction:

Development board: Apollo STM32F4/F7, F429_V1

MCU: STM32F429IGT6, STM32F767IGT6, STM32H743IIT6

Main frequency: 180MHz、216MHz、400MHz (Corresponding to the above MCU)

Crystal frequency: 25MHz \, 25MHz \, 25MHz \ (Corresponding to the above MCU)

Wiring instructions:

1. select the display resolution (Applicable to atomic programs)

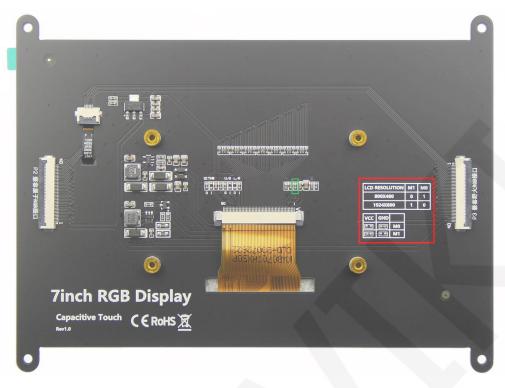
If 800x480 resolution is selected, M0 resistance is welded to VCC and M1 resistance is welded to GND;

If the resolution of 1024x600 is selected, M0 resistance is welded to GND and M1 resistance is welded to VCC;

When the resolution is selected in hardware (as shown in Picture 1), the display screen with the same resolution needs to be connected, so that the selected resolution is consistent with the resolution of the connected display screen. If you do not need to change the resolution, this step can be omitted.

2. Precautions for connecting to wildfire development board

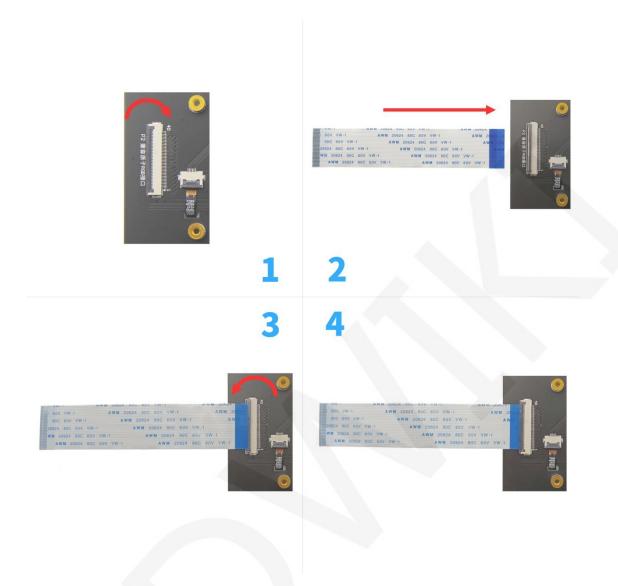
If connected to wildfire i When using the MX6ULL ARM Linux development board, you need to remove the resistance in the green box and M0 and M1 resistance in the red box in Figure 1, otherwise the development board will not run;



Picture 1. Select screen resolution

Wiring is carried out in two steps:

A. Use 40pin flexible cable to connect the RGB interface on the display module.
Among them, P2 interface is compatible with punctual atomic development board, and P3 interface is compatible with wildfire development board (as shown in Picture 3, the connection method of P3 interface is the same as that of P2 interface).

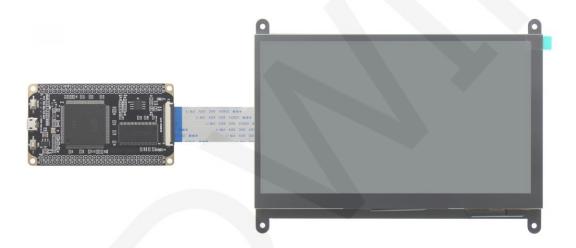


Picture 3. Connect RGB display module

B. After the display module is connected successfully, connect the other end of the flexible cable to the development board (as shown in Picture 4 and Picture 5). It should be noted that the flat cable should not be inserted reversely, so that the 1 ~ 40 pins of the display module interface and the 1 ~ 40 pins of the development board interface should be connected one by one.



Picture 4. Connect atomic core development board



Picture 5. Connect wildfire core development board

STM32F429 / STM32F767 / STM32H743 core board RGB LCD connection instructions				
Number	Module Pin	Corresponding to STM32F429IGT6, STM32F767IGT6, STM32H743IIT6 microcontroller internal connection pin	Remarks	
1	VCC5	5V	Power input pin (connect to 5V)	
2	VCC5	5V	Power input pin (connect to 5V)	

3	R0	GND	
4	R1	GND	
5	R2	GND	8-bit RED data pin (where R0 ~ R2 i not used)
6	R3	PH9	
7	R4	PH10	
8	R5	PH11	
9	R6	PH12	
10	R7	PG6	
11	GND	GND	power ground pin
12	G0	GND	
13	G1	GND	
14	G2	PH13	
15	G3	PH14	8-bit GREEN data pin (where G0, G1 is
16	G4	PH15	not used)
17	G5	PIO PIO	
18	G6	PI1	
19	G7	PI2	
20	GND	GND	power ground pin
21	В0	GND	
22	B1	GND	
23	B2	GND	
24	В3	PG11	8-bit BLUE data pin (where B0 ~ B2 is
25	В4	PI4	not used)
26	В5	PI5	
27	В6	PI6	
28	В7	PI7	
29	GND	GND	power ground pin
30	PCLK	PG7	Pixel clock control pin
31	HSYNC	PI10	Horizontal synchronous signal control pin
32	VSYNC	PI9	Vertical synchronous signal control pin
33	DE	PF10	Data enable signal control pin
34	BL	PB5	LCD backlight control pin

35	TP_CS	PI8	Capacitor touch screen reset pin (resistance touch screen chip selection pin)
36	TP_MOSI	PI3	Data pin of IIC bus of capacitance touch screen (write data pin of SPI bus of resistance touch screen)
37	TP_MISO	PG3	Resistance touch screen SPI bus read data pin (capacitance touch screen not used)
38	TP_CLK	PH6	IIC bus clock control pin of capacitive touch screen (SPI bus clock control pin of resistance touch screen)
39	TP_PEN	PH7	Touch screen interrupt control pin
40	RST	NRST	LCD reset control pin (effective at low level)

STM32F429_V1 core board **RGB LCD connection instructions Corresponding to** STM32F429IGT6、 Number Module Pin **Remarks** microcontroller internal connection pin IIC bus clock control pin of capacitive 1 TP_SCL PH4 touch screen Data pin of IIC bus of capacitance touch TP_SDA PH5 3 TP_PEN PD13 Touch screen interrupt control pin 4 Capacitor touch screen reset pin TP_RST PD11 5 **GND GND** power ground pin BL PD7 6 LCD backlight control pin DISP PD4 LCD display enable pin (not used) 8 Data enable signal control pin DE PF10 Horizontal synchronous signal control 9 **HSYNC** PI9 pin 10 **VSYNC** PI10 Vertical synchronous signal control pin 11 **PCLK** PG7 Pixel clock control pin

12	В7	PB9	
13	В6	PB8	0 hit DI IIE data nin
14	B5	PA3	
15	B4	PI4	
16	В3	PG11	8-bit BLUE data pin
17	B2	PD6	
18	B1	PG12	
19	В0	PE4	
20	G7	PI2	
21	G6	PC7	
22	G5	PIO	8-bit GREEN data pin
23	G4	PH15	
24	G3	PG10	
25	G2	PH13	
26	G1	PE6	
27	G0	PE5	
28	R7	PG6	
29	R6	PB1	
30	R5	PA12	
31	R4	PA11	8-bit RED data pin
32	R3	PB0	o-bit NED data piri
33	R2	PH8	
34	R1	PH3	
35	R0	PH2	
36	GND	GND	power ground pin
37	VCC3.3	3.3V	Power input pin (connect to 3.3V)
38	VCC3.3	3.3V	Power input pin (connect to 3.3V)
39	VCC5	5V	Power input pin (connect to 5V)
40	VCC5	5V	Power input pin (connect to 5V)

Demo function description:

- This test program can be used for RGB LCD test, in which F429_V1_Core_
 STM32F429IGT6 test program is used to test RGB LCD of wildfire development board (supports rgb565 and rgb888), and other test procedures are used for RGB LCD (only supports rgb565) test of punctual atomic development board;
- The test program is applicable to three STM32 MCU platforms: STM32F429IGT6.
 STM32F767IGT6. STM32H743IIT6;
- Please follow the wiring instructions above to find the corresponding development board and MCU for wiring;
- 4. The test supports four directions of display switching. For specific methods, please refer to the following display direction switching instructions;
- The test program supports the switching between resistance touch screen and capacitive touch screen (the display module only supports capacitive touch screen).
 For specific methods, please refer to the following description of touch screen type switching;
- This test program supports the touch screen working mode switching (polling and interrupt). For specific methods, please refer to the following description of touch screen working mode switching;
- 7. This set of test procedures contains the following test items:
 - A. the main interface displays the test;
 - B. read ID and color value test;
 - C. simple brush test;
 - D. rectangular drawing and filling test;
 - E. circular drawing and filling test;
 - F. triangle drawing and filling test;
 - G. English display test;
 - Chinese display test;
 - picture display test;

- J. rotating display test;
- K. touch screen handwriting test;

Display direction switching instructions:

Find the macro definition USE_HORIZONTAL in lcd.h as shown below:

```
USE_HORIZONTAL 0 //0° Rotate

USE_HORIZONTAL 1 //90° Rotate

USE_HORIZONTAL 2 //180° Rotate

USE_HORIZONTAL 3 //270° Rotate
```

Description of touch screen type switching

Find the macro definition TP_TYPE in touch.h as shown below:

```
//电容触摸屏和电阻触摸屏选择定义
#define TP_TYPE 1 //0-电阻触摸屏, 1-电容触摸屏
TP_TYPE 0 // Using the resistive touch screen
TP_TYPE 1 // Use capacitance touch screen
```

Description of touch screen operation mode switching

Find the macro definition SCAN TYPE in touch.h as shown below:

```
//触摸屏工作模式选择定义
#define SCAN_TYPE 0 //0-轮询模式,1-中断模式
SCAN_TYPE 0 // Use polling mode
SCAN_TYPE 1 // Use interrupt mode
```