Spatial Distance Histogram Processing of Large Scale Data

University of South Florida CIS 4930 - Summer 2016 Programming on Massively Parallel Systems

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1 Abstract

Graphic Processing Units (GPUs) today have a great deal of global memory a programmer can use for whatever reason deemed necessary. But what if the project and data at hand is too large even for the 2GB of the GTX 960, the 6GB of the GTX 980 TI, or even 12GB of the TITAN X card? The design an implementation of memory management systems or mechanisms to artificially limit the GPUs global memory in an attempt to evaluate the effect of large data sets on the running time of a piece of kernel code.

Dividing and loading the data into global memory as blocks is unequivocally the easiest approach but, how the data is loading is going to be the challenging part of this experiment. Since data transfer from a host to a device's global memory is extremely costly, the efficiency of the data transfer is paramount.

To evaluate the kernel, the CUDA program will be run and the running time of the kernel will be plotted against data of different sizes.

2 Introduction

The objective of this project is to simulate the scenario where the data at hand is too large to all fit into global memory at once. This is a very large data set since some GPUs today come with up to 12GB of global memory on board. Loading the data is the trivial part of this project. The efficient loading of data is what this project is about.

3 Strategies

We thought about starting with the code that we wrote for Project 2. Even with our best attempts

the code was still 10x slower than what the Teaching Assistant(TA) had produced. Taking that into consideration, we started with the TA's code in order to improve on that already optimized code. This code of course did not take into consideration if the data set was too large to fit into global memory of the GPU itself.

4 Algorithms and Code Examples

```
//Test code for listing package
#include <stdio.h>

int main(){

unsigned int counter = 1;

while(counter <= 10{

printf("%u", counter);

++counter;

puts("");

puts("");
```

5 Results

This section highlights the results obtained from our experiments and the control data we used to compare our data to.

5.1 Control

For a baseline to compare our experiments to we are using the data posted on CANVAS from the

TA. Number of Atoms versus the Running time in seconds.

Num Atoms	Run Time (s)	Num Atoms	Run Time (s)
512	0.00101494	1638912	91.99478912
102912	0.34378347	1741312	104.07542420
205312	1.36084175	1843712	117.17488860
307712	3.05233979	1946112	130.36553960
410112	5.48577261	2048512	144.33065800
512512	8.57051182	2150912	159.09375000
614912	12.34655285	2253312	174.61938480
717312	16.97405243	2355712	191.60043330
819712	22.75370407	2458112	207.86152650
922112	29.15684891	2560512	226.56158450
1024512	36.23521423	2662912	244.75129700
1126912	43.85413361	2765312	263.50692750
1229312	51.80871201	2867712	282.38305660
1331712	60.74640274	2970112	303.45535280
1434112	70.61425018	3072512	325.80584720
1536512	80.98942566		

5.2 Experimental

This section has the results we obtained in the project when we assume that the global memory is not big enough to hold all of the data that we have to to offer. We have to feed in the data a little at a time (say 200MB).

5.3 Test Cases

Thes

6 Conclusion

Go on about what we expected versus what we saw in testing. Does the loading of data the way we implemented it a success or failure? Go into some great detail why or why not.

References

- [1] N. Corporation, "CUDA Programming Guide." http://docs.nvidia.com/cuda/ cuda-c-programming-guide/index.html, 2015. [Online; accessed Summer 2016].
- [2] N. Corporation, "CUDA Runtime API." http://docs.nvidia.com/cuda/cuda-runtime-api/index.html, 2015. [Online; accessed Summer 2016].