

Asynchronous Exceptions As An Effect[★]

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Abstract

Asynchronous interrupts abound in computing systems, yet they remain a thorny concept for both programming and verification practice. The ubiquity of interrupts underscores the importance of developing programming models to aid the development and verification of interrupt-driven programs. The research reported here recognizes asynchronous interrupts as a computational effect and encapsulates them as a building block in modular monadic semantics. The resulting modular semantic model can serve as both a guide for functional programming with interrupts and as a formal basis for reasoning about interrupt-driven computation as well.

1 Introduction

The asynchronous interrupt, according to Dijkstra [4], was a great invention:

...but also a Box of Pandora. Because the exact moments of the interrupts were unpredictable and outside our control, the interrupt mechanism turned the computer into a nondeterministic machine with a non-reproducible behavior, and could we control such a beast?

The construction and verification of programs in the presence of asynchronous exceptions is notoriously difficult. Certainly, one cause of this difficulty is that a setting with interrupts is necessarily concurrent, there being, at a minimum, distinct interrupting and interrupted entities. But, more fundamentally, the notion of computation for asynchronous interrupt is inherently non-deterministic and controlling the non-determinism “beast” remains a challenge.

What do we mean by an asynchronous interrupt? It is a form of exception thrown by the environment external to a thread. A synchronous exception, by contrast, arises from an event internal to a thread’s execution; for example, they can originate from run-time errors (e.g., division-by-zero or pattern match failure) and OS system calls. Asynchronous exceptions arise from events external to a thread and may occur at any point between the atomic actions that comprise the thread. Examples of asynchronous exceptions are hardware signals arising non-deterministically from I/O devices as well as the language feature of the

[★] This research was supported in part by the Gilliom Cyber Security Gift Fund.

same name in Concurrent Haskell [17]. The terms *interrupt* and *asynchronous exception* are used interchangeably throughout this article.

Is there an modular model that can serve as both a guide for programming with exceptions and a formal semantics for reasoning about them as well? In the past, language semanticists and functional programmers have both turned to monads and monadic semantics when confronted with non-functional effects (e.g., state, exceptions, etc.). This paper argues that one way to understand asynchronous exceptions is as a computational effect encapsulated by a monadic construction. This paper explores precisely this path by decomposing the asynchronous exception effect into monadic components. Surprisingly enough, although all of the monadic components applied here are well-known [19, 22], this work is apparently the first to put them together to model interrupts.

This paper argues that monadic semantics [19] (and, particularly, modular monadic semantics (MMS) [16]) is an appropriate foundation for a modular model of asynchronous exceptions. With the monadic approach, one may have one’s cake and eat it, too: formal specifications in monadic style are easily rendered as executable functional programs. The contributions of this paper are:

- A semantic building block for asynchronous behaviors (Section 3). This building block follows the same lines as other, well-known building blocks in MMS: by a straightforward extension of the underlying monad, we may define a set of operators that, in turn, may be used to define asynchronous interrupts. In the lingo of MMS, asynchronicity becomes a “building block” that may be added to other monadic specifications.
- A denotational framework for modeling a member of the infamous “Awkward Squad” [23]. These are behaviors considered difficult to accommodate within a pure, functional setting. This framework is applied in Section 4 and proved equivalent to a recently published natural semantics of asynchronous exceptions.
- An extension of published formal models of OS kernels with asynchronous behavior (Section 5). We apply the interrupts building block to monadic kernels [9], thereby extending the expressiveness of these models of concurrent systems.

As we use it, the term *interrupt* should not be limited to the hardware mechanism by which a CPU communicates with the external world. Hardware interrupts are a special form of asynchronous exception; they can arise at any time and result in a temporary transfer of control to an interrupt service routine. Asynchronous exceptions do not, in general, involve such a temporary control transfer. The case study we present concerns a hardware interrupt mechanism, although the model we use applies equally well for asynchronous exceptions in general. In particular, the current model has been applied in another significant case study in the semantic specification of the typed interrupt calculus of Palsberg [2, 21]. This specification will be published in a sequel.

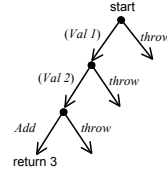
1.1 Summary of the MMAE

This section summarizes two applications of the monadic model of asynchronous exceptions (MMAE)—one in denotational semantics and the other in functional programming—and will provide the reader with helpful intuitions about the MMAE. Asynchronous exceptions according to this model are really a composite effect, combining non-determinism, concurrency, and some notion of interactivity. Each of these effects corresponds to well-known monads, and the MMAE encapsulates them all within a single monad. Haskell renderings of all the examples in this paper are available online [8].

Non-determinism is inherent in asynchronous exceptions. The reason is simple: when (or if) a computation is interrupted is not determined by the computation itself and so exceptions are, therefore, by definition non-deterministic with respect to the computation. That a notion of concurrency is necessary is less obvious. Computation here is assumed to be “interruptible” only at certain identified points within its execution. These breakpoints (implicitly) divide the computation up into un-interruptible units or atoms. This is tantamount to a theory of concurrency. Finally, a notion of interactivity is required for obvious reasons: the “interruptee” and “interrupter” interact according to some regime.

In Section 4, we consider Hutton and Wright’s language of arithmetic expressions with synchronous and asynchronous exceptions [13]. This language is defined

in detail there, but, for the time being, consider the simple arithmetic expression, $(Add\ (Val\ 1)\ (Val\ 2))$. In Hutton and Wright’s operational semantics, there are four possible evaluations of this expression, assuming that exceptions are “on”. These evaluations may be described as a tree (see inset) in which each breakpoint “•” represents a place at which the arithmetic computation may be interrupted. The evaluation which returns 3 proceeds along the leftmost path. An asynchronous exception may occur at any of the breakpoints, corresponding to the three evaluations ending in *throw*. In the MMAE, the semantics of $(Add\ (Val\ 1)\ (Val\ 2))$ and its interaction with exceptions is represented in closed form as a single term in a monadic algebra of effects:



$$merge\{merge\{merge\{\eta\ 3,\ throw\},\ throw\},\ throw\} \quad (\dagger)$$

The intuitive meaning of this term is clear from its correspondence to the inset tree: the *merge* operator combines two “possible histories”, rather like a branch constructor in a tree data type; the *throw* “leaf” is the result of an asynchronous exception; the monadic unit “ $(\eta\ 3)$ ” leaf computation returns the final value.

Figure 1 gives an example application of the MMAE. Recent research has demonstrated how kernels with a broad range of OS behaviors (e.g., message-passing, synchronization, forking, etc.) may be formulated in terms of resumption monads [9] and generalized with monad transformers. The application presented in Section 5 describes how such kernels may be extended to include asynchronous exception behaviors as well. This extension manifests itself in Figure 1 in the

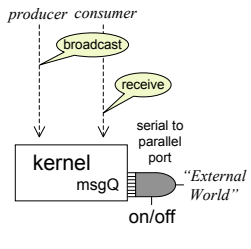
	No Port	With Port & Interrupts on
	$K = \text{StateT Sys } (\text{StateT Sto Id})$ $R = \text{ResT } K$	$K = \text{StateT Sys } (\text{StateT Sto N})$ $R = \text{ResT } K$
	<pre>Haskell> producer_consumer broadcasting 1001 broadcasting 1002 receiving 1001 broadcasting 1003 receiving 1002 ...</pre>	<pre>Haskell> producer_consumer broadcasting 1001 new datagram: 179 broadcasting 1002 receiving 179 new datagram: 204 ...</pre>

Fig. 1. Kernels with interrupt-driven input port. The kernel design (left) supports an interrupt-driven serial-to-parallel input port and synchronous message-passing primitives. The FIFO message queue, *msgQ*, stores messages in flight between threads. The input port receives bits non-deterministically from the external world, buffers them, and enqueues a one byte message on *msgQ* when possible. The baseline kernel (middle) has no such input port; the modified asynchronous kernel (right) extends the baseline kernel with non-determinism, thereby supporting the interrupt-driven port. When a producer-consumer application is run on both kernels, the datagrams received through the port are manifest. See text for further description.

creation of the monads K and R underlying the kernel specification. For the kernel without the interrupt-driven port (Figure 1, middle), the monad transformers constructing K and R (i.e., StateT and ResT) are applied to the identity monad, Id ; in the kernel with the interrupt-driven port (Figure 1, middle), they are applied instead to the non-determinism monad, N . The addition of asynchronous behaviors requires little more than this refinement of the monad underlying the kernel specification. What precisely all this means and why is the subject of this paper.

2 Background on Monads and Monad Transformers

This section outlines the background material necessary to understand the present work. We must assume of necessity that the reader is familiar with monads. Readers requiring more background should consult the related work (especially, Liang et al. [16]); other readers may skip this section. Section 2.1 contains an overview of the non-determinism monad.

A structure (M, η, \star) is a *monad* if, and only if, M is a type constructor (functor) with associated operations *bind* ($\star : M a \rightarrow (a \rightarrow M b) \rightarrow M b$) and *unit* ($\eta : a \rightarrow M a$) obeying the well-known “monad laws” [15].

$$\begin{array}{lll}
 \text{(left-unit)} & (\eta v) \star k & = k v \\
 \text{(right-unit)} & x \star \eta & = x \\
 \text{(assoc)} & x \star (\lambda v. (k v \star h)) & = (x \star k) \star h
 \end{array}$$

Two such monads are the identity and state monads:

$$\begin{array}{ll} \text{Id } a &= a & \text{S } a &= s \rightarrow a \times s \\ \eta_{\text{Id}} v &= v & \eta_{\text{S}} v &= \lambda \sigma. (v, \sigma) \\ x \star_{\text{Id}} f &= f x & \varphi \star_{\text{S}} f &= \lambda \sigma_0. \text{let } (v, \sigma_1) = \varphi \sigma_0 \text{ in } f v \sigma_1 \end{array}$$

Here, s is a fixed type argument, which can be replaced by any type which is to be “threaded” through the computation. What makes monads interesting is not their bind and unit (these are merely computational glue) but, rather, the operations one can define in terms of the extra computational “stuff” they encapsulate. For example, one may define read and write operations, g_{S} and u_{S} , to manipulate the underlying state s :

$$\begin{array}{ll} \text{g}_{\text{S}} : \text{S } s & \text{u}_{\text{S}} : (s \rightarrow s) \rightarrow \text{S}() \\ \text{g}_{\text{S}} = \lambda \sigma. (\sigma, \sigma) & \text{u}_{\text{S}} f = \lambda \sigma. ((), f \sigma) \end{array}$$

The morphisms, g_{S} and u_{S} , are pronounced *get* and *update*, respectively. Additional operations on a monad (e.g., u_{S} on S) are referred to as *non-proper morphisms* to distinguish them from the monadic bind and unit.

Given two monads, M and M' , it is natural to ask if their composition, $\text{M} \circ \text{M}'$, is also a monad, but it is well-known that monads generally do not compose in this simple manner [6]. However, *monad transformers* do provide a form of monad composition [6, 16, 18]. When applied to a monad M , a monad transformer T creates a new monad M' . The monad $(\text{StateT } s \text{ Id})$ is identical to the state monad S . The state monad transformer, $(\text{StateT } s)$, is shown below.

$$\begin{array}{ll} \text{S } a &= \text{StateT } s \text{ M } a = s \rightarrow \text{M}(a \times s) & \text{u } f &= \lambda \sigma. \eta_{\text{M}}((), f \sigma) \\ \eta_{\text{S}} x &= \lambda \sigma. \eta_{\text{M}}(x, \sigma) & \text{g} &= \lambda \sigma. \eta_{\text{M}}(\sigma, \sigma) \\ x \star_{\text{S}} f &= \lambda \sigma_0. (x \sigma_0) \star_{\text{M}} (\lambda(a, \sigma_1). f a \sigma_1) & \text{lift}_{\text{S}} x &= \lambda \sigma. x \star_{\text{M}} \lambda y. \eta_{\text{M}}(y, \sigma) \end{array}$$

In a composed or *layered* monad, $\text{M}' = \text{TM}$, any non-proper morphisms on M must be redefined or *lifted* to monad M' . Lifting of non-proper morphisms is frequently performed in terms of a *lift* operator (e.g., lift_{S} above).

2.1 Non-determinism as a Monad

Semantically, non-deterministic programs (i.e., those with more than one possible value) may be viewed as returning sets of values rather than just one value [1, 27]. Consider, for example, the *amb* operator of McCarthy (1963). Given two arguments, it returns either one or the other; for example, the value of $1 \text{ amb } 2$ is either 1 or 2. The *amb* operator is *angelic*: in the case of the non-termination of one of its arguments, *amb* returns the terminating argument. For the purposes of this exposition, however, we ignore this technicality. According to this view, the meaning of $(1 \text{ amb } 2)$ is simply the set $\{1, 2\}$ and the meaning of $(\text{let } x = (1 \text{ amb } 2); y = (1 \text{ amb } 2) \text{ in } x + y)$ is $\{2, 3, 4\}$. The computational theory of non-determinism employed here is based on a monadic form of the powerdomain construction [26, 28]. Encoding non-determinism as sets of values is

expressed monadically via the finite powerset monad:

$$\begin{array}{ll} \eta & : a \rightarrow \mathcal{P}_{\text{fin}}(a) & \star & : \mathcal{P}_{\text{fin}}(a) \rightarrow (a \rightarrow \mathcal{P}_{\text{fin}}(b)) \rightarrow \mathcal{P}_{\text{fin}}(b) \\ \eta x & = \{x\} & S \star f & = \bigcup (f S) \end{array}$$

where $f S = \{f x \mid x \in S\}$ and $\mathcal{P}_{\text{fin}}(-)$ is set of finite subsets drawn from its argument. In the finite set monad, the meaning of $(e \text{ amb } e')$ is the union of the meanings of e and e' .

That lists are similar structures to sets is familiar to any functional programmer; a classic exercise in introductory functional programming courses represents sets as lists and set operations as functions on lists (in particular, casting set union (\cup) as list append ($++$)). Some authors [31, 6, 15] have made use of the “sets as lists” pun to implement non-deterministic programs within functional programming languages via the list monad; this approach seems to have originated with Hughes and O’Donnell [12]. The list monad (written “`[]`” in Haskell) is defined by the instance declaration:

```
instance Monad [] where
  return x      = [x]
  (x : xs) >>= f = f x ++ (xs >>= f)
  [] >>= f      = []
```

This straightforward implementation suffices for our purposes, but it is known to contain an inaccuracy when the lists involved are infinite [30]. Specifically, because $l ++ k = l$ if the list l is infinite, append ($++$) loses information that set union (\cup) would not.

The non-determinism monad has a morphism, *merge*, that combines a finite number of non-deterministic computations, each producing a finite set of values, into a single computation returning their union. For the finite powerset monad, it is union (\cup), while with the list implementation, *merge* is concatenation:

$$\begin{array}{l} \text{merge}_{[]} :: [[a]] \rightarrow [a] \\ \text{merge}_{[]} = \text{concat} \end{array}$$

Note that the finiteness of the argument of *merge* is assumed and is not reflected in its type.

3 A Monadic Model for Asynchronous Exceptions

This section presents a monadic model for asynchronous exceptions (MMAE). The MMAE is an algebra of effects with monads and associated operators expressing notions of non-determinism, concurrency and interactivity. Section 3.1 first defines this algebra of effects and, then, Section 3.2 presents a number of theorems specifying the interactions between the algebraic operators. The convention that operators associated with a monad are referred to as *effects* is followed throughout this paper.

3.1 Monadic Algebra of Effects

This section presents the effect algebra underlying the semantics in Section 4. In Section 5, this algebra will be extended and generalized. The left hand column in Definition 1 below specifies the functor parts of three monads using a category-theoretic notation while the right hand column presents them as data type declarations in the Haskell language. The intention in doing so is to appeal to the widest audience. One should note, however, that the Haskell representations are really approximate (e.g., lists in Haskell may be infinite).

Definition 1 (Functors for monads $\mathbf{N}, \mathbf{E}, \mathbf{R}$).

$$\begin{array}{lll}
 \text{Non-deter.} & \mathbf{N} A = \mathcal{P}_{fin}(A) & \text{type } \mathbf{N} a = [a] \\
 & & \text{data } Err\ a = Ok\ a \mid Error \\
 \text{Exceptions} & \mathbf{E} A = \mathbf{N}(A + Error) & \text{type } \mathbf{E} a = \mathbf{N}(Err\ a) \\
 \text{Concur.} & \mathbf{R} A = \text{fix } X. A + \mathbf{E} X & \text{data } \mathbf{R} a = Done\ a \mid Pause\ (\mathbf{E} (\mathbf{R} a))
 \end{array}$$

Technical note: In the categorical definition of \mathbf{R} (left column, bottom), the binder $\text{fix } X$ can be taken to indicate either the least or greatest fixed point solution to the corresponding recursive domain equation. For the semantics in Section 4, either will suffice as there will be no need to represent infinite computations. In Section 5, it will represent the greatest fixed point.

There is a codebase with Haskell implementations of the monadic constructions presented in this paper [8]. Each of the operator definitions below is followed by Haskell examples demonstrating the operators. For consistency, Haskell concrete syntax is eschewed in favor of the mathematical syntax of this paper. So, for example, Haskell lists representing sets are written with set brackets “{” and “}” rather than with Haskell list constructors “[” and “]” and any other Haskell syntactic details inessential to the presentation are struck altogether.

Definition 2 specifies the unit (η) and bind (\star) operators for the \mathbf{N} , \mathbf{E} and \mathbf{R} monads. Discussion motivating the definitions of $\star_{\mathbf{N}}$ and $\eta_{\mathbf{N}}$ can be found in Section 2.1.

Definition 2 (η, \star for monads $\mathbf{N}, \mathbf{E}, \mathbf{R}$). *The unit (η) and bind (\star) operations have type $\eta_m : a \rightarrow \mathbf{m} a$ and $(\star_m) : \mathbf{m} a \rightarrow (a \rightarrow \mathbf{m} b) \rightarrow \mathbf{m} b$ for monads $\mathbf{m} = \mathbf{N}, \mathbf{E}, \mathbf{R}$ and are defined by the following equations:*

$$\begin{array}{lll}
 \eta_{\mathbf{N}} x = \{x\} & \eta_{\mathbf{E}} = \eta_{\mathbf{N}} \circ Ok & \eta_{\mathbf{R}} = Done \\
 \{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{N}} f & \varphi \star_{\mathbf{E}} f = & (Done\ v) \star_{\mathbf{R}} f = f\ v \\
 = \bigcup (f\ \varphi_i) & \varphi \star_{\mathbf{N}} \lambda v. & (Pause\ \varphi) \star_{\mathbf{R}} f = \\
 & \text{case } v \text{ of} & Pause(\varphi \star_{\mathbf{E}} \lambda \kappa. \eta_{\mathbf{E}}(\kappa \star_{\mathbf{R}} f)) \\
 & (Ok\ x) \rightarrow f\ x & \\
 & Error \rightarrow \eta_{\mathbf{N}} Error &
 \end{array}$$

Below are Haskell examples demonstrating the unit and bind operators of the \mathbf{N} and \mathbf{E} monads. Binding \mathbf{N} -computation $\{1, 2, 3\}$ to the function $(\lambda v. \eta_{\mathbf{N}}(v + 1))$

with \star_N has the effect of incrementing each element. Binding E-computation $\{Ok\ 1, Ok\ 2, Error\}$ to the function $(\lambda v. \eta_E(v + 1))$ with \star_E performs a similar “mapping” to the previous case: each $(Ok\ x)$ element is incremented while the *Error* is unchanged.

```
Haskell>  $\eta_N\ 9$ 
          { 9 }
Haskell>  $\eta_E\ 9$ 
          { Ok 9 }
Haskell>  $\{1, 2, 3\} \star_N (\lambda v. \eta_N(v + 1))$ 
          {2, 3, 4}
Haskell>  $\{Ok\ 1, Ok\ 2, Error\} \star_E (\lambda v. \eta_E(v + 1))$ 
          {Ok 2, Ok 3, Error}
```

The *step* operator takes an E-computation and produces an R-computation; it is an example of a *lifting* [16] from E to R. The lifted computation, *step x*, is atomic in the sense that there are no intermediate *Pause* breakpoints and is, in that sense, indivisible. The *run* operator projects computations from R to E. An R-computation may be thought of intuitively as an E-computation with a number (possibly infinite) of inserted *Pause* breakpoints; *run* removes each of those breakpoints.

Definition 3 (Operators relating E and R).

$$\begin{array}{ll}
step : E\ a \rightarrow R\ a & run : R\ a \rightarrow E\ a \\
step\ x = Pause(x \star_E (\eta_E \circ Done)) & run(Pause\ \varphi) = \varphi \star_E run \\
& run(Done\ v) = \eta_E\ v
\end{array}$$

The Haskell session below shows that applying *run* to the lifting of an E-computation makes no change to that computation. This suggests that *run* is an inverse of *step*, which is, in fact, the case (see Theorem 5 below).

```
Haskell> run (step {Ok 1, Ok 2, Error})
          {Ok 1, Ok 2, Error}
```

The *merge* operators on N, E and R are given in Definition 4. As discussed in Section 2.1 above, $merge_N$ is a non-proper morphism in the non-determinism monad. Its definition is lifted to the E and R monads below:

Definition 4 (Merge operators).

$$\begin{aligned}
\text{merge}_{\mathbf{N}} &: \mathbf{N}(\mathbf{N} a) \rightarrow \mathbf{N} a \\
\text{merge}_{\mathbf{N}} X &= \cup_{(x \in X)} x \\
\text{merge}_{\mathbf{E}} &: \mathbf{N}(\mathbf{E} a) \rightarrow \mathbf{E} a \\
\text{merge}_{\mathbf{E}} &= \text{merge}_{\mathbf{N}} \\
\text{merge}_{\mathbf{R}} &: \mathbf{N}(\mathbf{R} a) \rightarrow \mathbf{R} a \\
\text{merge}_{\mathbf{R}} \{\varphi_1, \dots, \varphi_n\} &= \text{Pause}(\text{merge}_{\mathbf{E}} \{\eta_{\mathbf{E}} \varphi_1, \dots, \eta_{\mathbf{E}} \varphi_n\})
\end{aligned}$$

The effect of merging some finite number of computations in \mathbf{N} or \mathbf{E} together is to collect the sets of their outcomes into a single outcome set. Merging in \mathbf{R} has a similar effect, but, rather than collecting outcomes, $\text{merge}_{\mathbf{R}} \{\varphi_1, \dots, \varphi_n\}$ creates a single \mathbf{R} -computation that branches out with n “sub-computations”.

```

Haskell> mergeN {{1, 2}, {4}}
{1, 2, 4}
Haskell> mergeE {{Ok 1, Ok 2, Error}, {Ok 4, Error}}
{Ok 1, Ok 2, Ok 4, Error} — dupl. Error not shown

```

An equation like “ $\langle \text{raise exception} \rangle \star f = \langle \text{raise exception} \rangle$ ” will hold in any exception monad like \mathbf{E} . That is, a raised exception trumps any effects that follow. The *status* operators for \mathbf{E} and \mathbf{R} catch an exception producing computation and “defuse” it. So, if $\varphi : \mathbf{E} a$ produces an exception, then the value returned by $\text{status}_{\mathbf{E}}(\varphi) : \mathbf{E}(a + \text{Error})$ will be *Error* itself. The *status* operators may be used to discern when an exception has occurred in its argument while isolating the exception’s effect.

Definition 5 (Status).

$$\begin{array}{ll}
\text{status}_{\mathbf{E}} : \mathbf{E} a \rightarrow \mathbf{E}(a + \text{Error}) & \text{status}_{\mathbf{R}} : \mathbf{R} a \rightarrow \mathbf{R}(a + \text{Error}) \\
\text{status}_{\mathbf{E}} \varphi = & \text{status}_{\mathbf{R}} (\text{Pause } \varphi) = \\
\quad \varphi \star_{\mathbf{N}} \lambda v. & \text{Pause } (\text{status}_{\mathbf{E}} \varphi \star_{\mathbf{E}} \lambda v. \\
\quad \text{case } v \text{ of} & \text{case } v \text{ of} \\
\quad \quad (\text{Ok } y) \rightarrow \eta_{\mathbf{E}}(\text{Ok } y) & \quad (\text{Ok } x) \rightarrow \eta_{\mathbf{E}}(\text{status}_{\mathbf{R}} x) \\
\quad \quad \text{Error} \rightarrow \eta_{\mathbf{E}} \text{Error} & \quad \text{Error} \rightarrow \eta_{\mathbf{E}}(\text{Done Error}) \\
& \text{status}_{\mathbf{R}} (\text{Done } v) = (\text{Done } (\text{Ok } v))
\end{array}$$

The $\text{throw}_{\mathbf{E}} : \mathbf{E} a$ operator raises an exception (it is defined below in Definition 6). Note how (in the second example) it “trumps” the remaining computation. The third example, $(\text{status}_{\mathbf{E}} \text{throw}_{\mathbf{E}})$, shows how the effect of the $\text{throw}_{\mathbf{E}}$ exception is isolated. Instead of returning the exception $\{\text{Error}\}$, the $(\text{status}_{\mathbf{E}} \text{throw}_{\mathbf{E}})$ computation returns the *Error* token as its value. With a non-exception throwing computation (e.g., $(\eta_{\mathbf{E}} 9)$), $\text{status}_{\mathbf{E}}$ returns the value produced by its argument wrapped in an *Ok*.

```

Haskell> throwE

```

```

{ Error }
Haskell> throwE ★E λv. ηE (v + 1)
{ Error }
Haskell> statusE throwE
{ Ok Error }
Haskell> statusE (ηE 9)
{ Ok (Ok 9) }

```

Definition 6 gives the specification for the exception-raising and -catching operations, *throw* and *catch*, in **E** and **R** and the branching operation, *fork*, in the **R** monad. The *fork* operation is particularly important in the semantic framework of the next section. If $\varphi : \mathbf{R} a$, then $(\text{fork } \varphi) : \mathbf{R} a$ is a computation that, roughly speaking, will do either whatever φ would do or be asynchronously interrupted by exception *throw_R*.

Definition 6 (Control flow operators).

$\text{throw}_E : E a$	For monad $\mathbf{m} = E, R$,
$\text{throw}_E = \eta_N \text{Error}$	$\text{catch}_m : \mathbf{m} a \rightarrow \mathbf{m} a \rightarrow \mathbf{m} a$
$\text{throw}_R : R a$	$\text{catch}_m \varphi \gamma = (\text{status}_m \varphi) \star_m \lambda s.$
$\text{throw}_R = \text{step } \text{throw}_E$	case s of
$\text{fork} : R a \rightarrow R a$	$(Ok v) \rightarrow \eta_m v$
$\text{fork } \varphi = \text{merge}_R \{\varphi, \text{throw}_R\}$	$Error \rightarrow \gamma$

The first two examples in the following Haskell transcript illustrate the behavior of *catch_E* and *throw_E*. If the first argument to *catch_E* raises an exception (as, obviously, *throw_E* does), then the second argument is returned. If the first argument to *catch* does not raise an exception, then its second argument is ignored. The third and fourth examples illustrate the *fork* effect. The effect of *fork* ($\eta_R 9$) is to create a single **R**-computation with two “branches” (both underlined below). The first branch is just the argument to *fork* (recall that $\eta_R = Done$) and the second branch is equal to $(\text{step } \text{throw}_E)$. This simple example exposes an important construction within the MMAE: an asynchronous exception thrown to a computation is modeled as an alternative branch from the computation. The fourth example shows the application of *run* to the previous example. The result is to collect all possible outcomes from each branch. Following a tree data type analogy, *run* is used to calculate the *fringe*.

```

Haskell> catchE throwE (ηE 9)
{ Ok 9 }
Haskell> catchE (ηE 9) throwE
{ Ok 9 }
Haskell> fork (ηR 9)
  Pause ({ Ok (Done 9), Ok (Pause ({ Error } ) )})
Haskell> run (fork (ηR 9))
{ Ok 9, Error }

```

3.2 Interactions Between Effects

This section presents a number of theorems characterizing the algebraic effects developed in the previous section and their relationships to one another. These theorems are used in the next section to prove an equivalence between the MMAE semantics given there and recently published semantics for asynchronous exceptions [13].

Theorem 1 gives a distribution rule for \star over *merge*. This distribution exposes the tree-like structure of computations in \mathbf{R} .

Theorem 1. *For monads $\mathbf{m} = \mathbf{N}, \mathbf{E}, \mathbf{R}$, $\star_{\mathbf{m}}$ distributes over $\text{merge}_{\mathbf{m}}$:*

$$\text{merge}_{\mathbf{m}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{m}} f = \text{merge}_{\mathbf{m}}\{\varphi_1 \star_{\mathbf{m}} f, \dots, \varphi_n \star_{\mathbf{m}} f\}$$

Proof.

$$\begin{aligned} & \text{merge}_{\mathbf{N}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{N}} f \\ \{\text{def } \text{merge}_{\mathbf{N}}\} &= (\cup \varphi_i) \star_{\mathbf{N}} f \\ \{\text{def } \star_{\mathbf{N}}\} &= f (\cup \varphi_i) \\ &= \cup (f \varphi_i) \\ \{\text{def } \text{merge}_{\mathbf{N}}\} &= \text{merge}_{\mathbf{N}}\{f \varphi_1, \dots, f \varphi_n\} \\ \{\text{def } \star_{\mathbf{N}}\} &= \text{merge}_{\mathbf{N}}\{\varphi_1 \star_{\mathbf{N}} f, \dots, \varphi_n \star_{\mathbf{N}} f\} \end{aligned}$$

$$\begin{aligned} & \text{merge}_{\mathbf{E}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{E}} f \\ &= \text{merge}_{\mathbf{E}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{N}} \hat{f} \\ & \quad \text{where } \hat{f} v = \text{case } v \text{ of} \\ & \quad \quad (Ok\ x) \rightarrow \eta_{\mathbf{N}}(f\ x) \\ & \quad \quad Error \rightarrow \eta_{\mathbf{N}}\ Error \\ \{\text{def } \text{merge}_{\mathbf{E}}\} &= \text{merge}_{\mathbf{N}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{N}} \hat{f} \\ \{\text{prev. case}\} &= \text{merge}_{\mathbf{N}}\{\varphi_1 \star_{\mathbf{N}} \hat{f}, \dots, \varphi_n \star_{\mathbf{N}} \hat{f}\} \\ \{\text{def } \star_{\mathbf{E}}\} &= \text{merge}_{\mathbf{N}}\{\varphi_1 \star_{\mathbf{E}} f, \dots, \varphi_n \star_{\mathbf{E}} f\} \\ \{\text{def } \text{merge}_{\mathbf{E}}\} &= \text{merge}_{\mathbf{E}}\{\varphi_1 \star_{\mathbf{E}} f, \dots, \varphi_n \star_{\mathbf{E}} f\} \end{aligned}$$

$$\begin{aligned} & \text{merge}_{\mathbf{R}}\{\varphi_1, \dots, \varphi_n\} \star_{\mathbf{R}} f \\ \{\text{def } \text{merge}_{\mathbf{R}}\} &= \text{Pause}(\text{merge}_{\mathbf{E}}\{\eta_{\mathbf{E}} \varphi_1, \dots, \eta_{\mathbf{E}} \varphi_n\}) \star_{\mathbf{R}} f \\ \{\text{def } \star_{\mathbf{R}}\} &= \text{Pause}(\text{merge}_{\mathbf{E}}\{\eta_{\mathbf{E}} \varphi_1, \dots, \eta_{\mathbf{E}} \varphi_n\} \star_{\mathbf{E}} \lambda \kappa. \eta_{\mathbf{E}}(\kappa \star_{\mathbf{R}} f)) \\ \{\text{prev. case}\} &= \text{Pause}(\text{merge}_{\mathbf{E}}\{\varphi'_1, \dots, \varphi'_n\}) \\ & \quad \text{where } \varphi'_i = (\eta_{\mathbf{E}} \varphi_i) \star_{\mathbf{E}} \lambda \kappa. \eta_{\mathbf{E}}(\kappa \star_{\mathbf{R}} f) \\ \{\text{left unit}\} &= \text{Pause}(\text{merge}_{\mathbf{E}}\{\eta_{\mathbf{E}}(\varphi_1 \star_{\mathbf{R}} f), \dots, \eta_{\mathbf{E}}(\varphi_n \star_{\mathbf{R}} f)\}) \\ \{\text{def } \text{merge}_{\mathbf{R}}\} &= \text{merge}_{\mathbf{R}}\{\varphi_1 \star_{\mathbf{R}} f, \dots, \varphi_n \star_{\mathbf{R}} f\} \end{aligned}$$

□

Theorem 2 gives a distribution law for *run* over $\text{merge}_{\mathbf{R}}$. This distribution law is something like an inverse “lifting” as it projects resumption-based merged computations into a single computation in \mathbf{E} .

Theorem 2. $run(merge_R\{\varphi_1, \dots, \varphi_n\}) = merge_E\{run\ \varphi_1, \dots, run\ \varphi_n\}$

Proof.

$$\begin{aligned}
& run(merge_R\{\varphi_1, \dots, \varphi_n\}) \\
\{\text{def } merge_R\} &= run(Pause(merge_E\{\eta_E \varphi_1, \dots, \eta_E \varphi_n\})) \\
\{\text{def } run\} &= (merge_E\{\eta_E \varphi_1, \dots, \eta_E \varphi_n\}) \star_E run \\
\{\text{thm 1}\} &= merge_E\{(\eta_E \varphi_1) \star_E run, \dots, (\eta_E \varphi_n) \star_E run\} \\
\{\text{left unit}\} &= merge_E\{run\ \varphi_1, \dots, run\ \varphi_n\}
\end{aligned}$$

□

Theorem 3 shows how *run* distributes over \star_R to produce an **E**-computation. Theorem 3 may be proved easily by induction on the number of *Pause* constructors in its argument if that number is finite. If it is not, then the property is trivially true, because, in that case, both sides of Theorem 3 are \perp .

Theorem 3. $run(x \star_R f) = (run\ x) \star_E (run \circ f)$

Theorem 4 shows that the *throw* exception overrides any effects that follow it. A consequence of this theorem is that, for any $f, g : a \rightarrow E\ b$

$$throw_E \star_E f = throw_E = throw_E \star_E g$$

Theorem 4. $throw_m \star_m f = throw_m$, for monad $m = E, R$.

Proof.

$$\begin{aligned}
& throw_E \star_E f \\
\{\text{def } throw_E\} &= (\eta_N Error) \star_E f \\
\{\text{def } \star_E\} &= (\eta_N Error) \star_N \lambda v. \\
& \quad \text{case } v \text{ of } \{ (Ok\ x) \rightarrow \eta_N (f\ x); Error \rightarrow \eta_N Error \} \\
\{\text{left unit}\} &= \text{case } Error \text{ of } \{ (Ok\ x) \rightarrow \eta_N (f\ x); Error \rightarrow \eta_N Error \} \\
&= \eta_N Error = throw_E
\end{aligned}$$

$$\begin{aligned}
& throw_R \star_R f \\
\{\text{def } throw_R\} &= (step\ throw_E) \star_R f \\
\{\text{def } step\} &= (Pause(throw_E \star_E (\eta_E \circ Done))) \star_R f \\
\{\text{def } \star_R\} &= Pause(throw_E \star_E \lambda \kappa. (\eta_E (\kappa \star_R f))) \\
\{\text{prev. case}\} &= Pause(throw_E \star_E (\eta_E \circ Done)) = step\ throw_E = throw_R
\end{aligned}$$

□

Theorem 5 states that the *run* operation is the inverse of *step*. A consequence of this theorem is that $run\ throw_R = throw_E$.

Theorem 5. $run(step\ \varphi) = \varphi$

Proof.

$$\begin{aligned}
& \text{run}(\text{step}\varphi) \\
\{\text{def step}\} &= \text{run}(\text{Pause}(\varphi \star_E (\eta_E \circ \text{Done}))) \\
\{\text{def run}\} &= (\varphi \star_E (\eta_E \circ \text{Done})) \star_E \text{run} \\
\{\text{assoc.}\} &= \varphi \star_E \lambda v. \eta_E(\text{Done } v) \star_E \text{run} \\
\{\text{left unit}\} &= \varphi \star_E \lambda v. \text{run}(\text{Done } v) \\
\{\text{def run}\} &= \varphi \star_E \lambda v. \eta_E v \\
\{\text{eta red}\} &= \varphi \star_E \eta_E \\
\{\text{rt unit}\} &= \varphi
\end{aligned}$$

□

4 The MMAE as a Semantic Framework

This section presents the semantics for the exception language of Hutton and Wright [13] in terms of the MMAE. Hutton and Wright define a natural semantics for a small language (called henceforth *Expr*) combining arithmetic expressions and synchronous exceptions (e.g., *Catch* and *Throw*). The natural semantics of this synchronous fragment of the language is just what one would expect. What makes the language interesting is the presence of an asynchronous interrupt and its manifestation within the semantics. The *Expr* language and its natural semantics are found in Figure 2. A monadic semantics for *Expr* is given below and then the equivalence of both semantics is formulated in Theorem 6.

$\frac{}{\text{Val } n \Downarrow^i \text{Val } n} \text{Val}$	$\frac{}{\text{Throw } \Downarrow^i \text{Throw}} \text{Throw}$	$\frac{x \Downarrow^i \text{Val } n \quad y \Downarrow^i \text{Val } m}{\text{Add } x \ y \Downarrow^i \text{Val } (n+m)} \text{Add1}$
$\frac{x \Downarrow^i \text{Throw}}{\text{Add } x \ y \Downarrow^i \text{Throw}} \text{Add2}$	$\frac{y \Downarrow^i \text{Throw}}{\text{Add } x \ y \Downarrow^i \text{Throw}} \text{Add3}$	$\frac{y \Downarrow^i v}{\text{Seqn } x \ y \Downarrow^i v} \text{Seqn1}$
$\frac{x \Downarrow^i \text{Throw}}{\text{Seqn } x \ y \Downarrow^i \text{Throw}} \text{Seqn2}$	$\frac{x \Downarrow^i \text{Val } n}{\text{Catch } x \ y \Downarrow^i \text{Val } n} \text{Catch1}$	$\frac{x \Downarrow^i \text{Throw} \quad y \Downarrow^i v}{\text{Catch } x \ y \Downarrow^i v} \text{Catch2}$
$\frac{x \Downarrow^B v}{\text{Block } x \Downarrow^i v} \text{Block}$	$\frac{x \Downarrow^U v}{\text{Unblock } x \Downarrow^i v} \text{Unblock}$	$\frac{}{x \Downarrow^U \text{Throw}} \text{Int}$

Fig. 2. Hutton and Wright’s Expression Language, *Expr*, with Asynchronous Exceptions. The essence of asynchronous exceptions in this model is captured in the *Int* rule.

This section presents the formulation of Hutton and Wright’s language within the monadic framework of Section 3. The evaluation relation is annotated by a *B* or *U*, indicating whether interrupts are blocked or unblocked, respectively. Ignoring this flag for the moment, the first three rows in Figure 2 are a conventional, natural semantics for a language combining arithmetic with synchronous exceptions. Note, for example, that the interaction of *Add* with *Throw* is specified by

$\frac{}{Val\ 1 \Downarrow^U Val\ 1} Val$		$\frac{}{Val\ 2 \Downarrow^U Val\ 2} Val$		
$\frac{}{Add\ (Val\ 1)\ (Val\ 2) \Downarrow^U Val\ 3} Add1$		$\frac{}{Add\ (Val\ 1)\ (Val\ 2) \Downarrow^U Throw} Int$		
$\frac{}{Val\ 1 \Downarrow^U Throw} Int$		$\frac{}{Val\ 2 \Downarrow^U Throw} Int$		
$\frac{}{Add\ (Val\ 1)\ (Val\ 2) \Downarrow^U Throw} Add2$		$\frac{}{Add\ (Val\ 1)\ (Val\ 2) \Downarrow^U Throw} Add3$		

Fig. 3. There are four possible evaluations of $(Add\ (Val\ 1)\ (Val\ 2))$ according to the unblocked semantics (i.e., with U annotation) in Figure 2.

three rules ($Add1$, $Add2$, $Add3$), the first for the case of exception-free arguments and the second two for the cases when an argument evaluates to $Throw$. The effect of $(Block\ e)\ [(Unblock\ e)]$ is to turn off [on] asynchronous exceptions in the evaluation of e .

To understand how Hutton and Wright’s semantics works and to compare it with the monadic semantics given here, the expression $(Add\ (Val\ 1)\ (Val\ 2))$ will be used as a running example. The expression $(Add\ (Val\ 1)\ (Val\ 2))$ has four possible evaluations when the interrupt flag is U (shown in Figure 3). The evaluation in the upper left is what one would expect. But the three other cases involve asynchronous exceptions, evaluating instead to $Throw$ via the Int rule from Figure 2. The bottom row shows what happens when the first and second arguments, respectively, evaluate to $Throw$ and, in the upper right corner, the evaluation is interrupted “before” any computation takes place. The Int rule may be applied because the flag is U and consequently there are four possible evaluations. When the flag is B , the Int rule may not be applied, and so exceptions are blocked. There is one and only one evaluation when the flag is B :

$$\frac{\frac{}{Val\ 1 \Downarrow^B Val\ 1} Val \quad \frac{}{Val\ 2 \Downarrow^B Val\ 2} Val}{Add\ (Val\ 1)\ (Val\ 2) \Downarrow^B Val\ 3} Add1$$

Figure 4 presents the semantics of $Expr$ using the MMAE framework. The semantics consists of two semantic functions, the “blocked” semantics $\mathcal{B}[-]$ and the “unblocked” semantics $\mathcal{U}[-]$. The blocked (unblocked) semantics provides the meaning of an expression when interrupts are off (on) and corresponds to the natural semantic relation \Downarrow^B (\Downarrow^U). The first five semantic equations for $\mathcal{B}[-]$ are a conventional monadic semantics for arithmetic, sequencing and synchronous exceptions [22]. The definitions for $Block$ and $Unblock$ require comment, however. The meaning of $(Block\ e)$ in the $\mathcal{B}[-]$ has no effect, because asynchronous exceptions are already blocked. The meaning of $(Unblock\ e)$, however, is $\mathcal{U}[e]$, signifying thereby that asynchronous exceptions are unblocked in the evaluation of e . The unblocked semantics is similar to the blocked, except that $fork$ is applied to each denoting computation. This has the effect of creating an asynchronous exception at each application of $fork$. $\mathcal{U}[Block\ e]$ is defined as $\mathcal{B}[e]$ to “turn off” asynchronous exceptions.

Example: Blocked Semantics. In the following example, the “blocked” denotation of $Add (Val\ 1) (Val\ 2)$ is simplified with respect to the theorems of Section 3.

$$\begin{aligned}
\mathcal{B}\llbracket Add\ (Val\ 1)\ (Val\ 2) \rrbracket & \\
\{\text{def } \mathcal{B}[-]\} &= (step(\eta_E\ 1)) \star_R \lambda v_1. (step(\eta_E\ 2)) \star_R \lambda v_2. \eta_R(v_1 + v_2) \\
\{\text{def } \star_R\} &= (step(\eta_E\ 1)) \star_R \lambda v_1. (Pause((\eta_E\ 2) \star_E (\eta_E \circ Done))) \star_R \lambda v_2. \eta_R(v_1 + v_2) \\
\{\text{left unit}\} &= (step(\eta_E\ 1)) \star_R \lambda v_1. (Pause(\eta_E (Done\ 2))) \star_R \lambda v_2. \eta_R(v_1 + v_2) \\
\{\text{def } \star_R\} &= (step(\eta_E\ 1)) \star_R \lambda v_1. Pause(\eta_E ((Done\ 2) \star_R \lambda v_2. \eta_R(v_1 + v_2))) \\
\{\text{def } \star_R\} &= (step(\eta_E\ 1)) \star_R \lambda v_1. (Pause(\eta_E (\eta_R(v_1 + 2)))) \\
&= Pause(\eta_E (Pause(\eta_E (Done\ 3))))
\end{aligned}$$

The last line follows by a similar argument to the first five steps. This last R-computation, $Pause(\eta_E (Pause(\eta_E (Done\ 3))))$, captures the operational content of $\mathcal{B}\llbracket Add\ (Val\ 1)\ (Val\ 2) \rrbracket$. It is a single thread with two steps in succession, corresponding to the evaluation of $(Val\ 1)$ and $(Val\ 2)$, followed by the return of the computed value 3.

Example: Unblocked Semantics. The next example considers the same expression evaluated under the “unblocked” semantics, $\mathcal{U}\llbracket - \rrbracket$. As in the previous example, the denotation is “normalized”, so to speak, according to the theorems of Section 3. Before beginning the example of the unblocked semantics, we state and prove a useful simplification in Lemma 1.

Lemma 1. $step(\eta_E\ v) \star_R f = Pause(\eta_E (f\ v))$.

Proof.

$$\begin{aligned}
step(\eta_E\ v) \star_R f & \\
\{\text{def } step\} &= Pause((\eta_E\ v) \star_E (\eta_E \circ Done)) \star_R f \\
\{\text{left unit}\} &= Pause(\eta_E (Done\ v)) \star_R f \\
\{\text{def } \star_R\} &= Pause((\eta_E (Done\ v)) \star_E \lambda \kappa. \eta_E(\kappa \star_R f)) \\
\{\text{left unit}\} &= Pause(\eta_E ((Done\ v) \star_R f)) \\
\{\text{def } \star_R\} &= Pause(\eta_E (f\ v))
\end{aligned}$$

□

Notational Convention. A notational convention is borrowed from Haskell. The “null bind” of a monad m , $(>>_m)$, is defined as:

$$\begin{aligned}
(>>_m) : ma \rightarrow mb \rightarrow mb \\
\varphi >>_m \gamma &= \varphi \star_m \lambda d. \gamma
\end{aligned}$$

where d is a dummy variable not occurring in γ . The effect of the computation, $\varphi >>_m \gamma$, is to evaluate φ , ignore the value it produces, and then evaluate γ .

The unblocked semantics for $Add (Val\ 1) (Val\ 2)$ unfolds as follows:

$\mathcal{B}[-] : Expr \rightarrow \mathbf{R} Int$	$\mathcal{U}[-] : Expr \rightarrow \mathbf{R} Int$
$\mathcal{B}[\text{Val } i] = \text{step}(\eta_E i)$	$\mathcal{U}[\text{Val } i] = \text{fork}(\text{step}(\eta_E i))$
$\mathcal{B}[\text{Add } e_1 \ e_2] = \mathcal{B}[e_1] \star_R \lambda v_1. \mathcal{B}[e_2] \star_R \lambda v_2. \eta_R(v_1 + v_2)$	$\mathcal{U}[\text{Add } e_1 \ e_2] = \text{fork} \left(\begin{array}{c} \mathcal{U}[e_1] \star_R \lambda v_1. \\ \mathcal{U}[e_2] \star_R \lambda v_2. \\ \eta_R(v_1 + v_2) \end{array} \right)$
$\mathcal{B}[\text{Seqn } e_1 \ e_2] = \mathcal{B}[e_1] \gg_R \mathcal{B}[e_2]$	$\mathcal{U}[\text{Seqn } e_1 \ e_2] = \text{fork}(\mathcal{U}[e_1] \gg_R \mathcal{U}[e_2])$
$\mathcal{B}[\text{Throw}] = \text{throw}_R$	$\mathcal{U}[\text{Throw}] = \text{fork } \text{throw}_R$
$\mathcal{B}[\text{Catch } e_1 \ e_2] = \text{catch}_R(\mathcal{B}[e_1])(\mathcal{B}[e_2])$	$\mathcal{U}[\text{Catch } e_1 \ e_2] = \text{fork}(\text{catch}_R(\mathcal{U}[e_1])(\mathcal{U}[e_2]))$
$\mathcal{B}[\text{Block } e] = \mathcal{B}[e]$	$\mathcal{U}[\text{Block } e] = \text{fork } \mathcal{B}[e]$
$\mathcal{B}[\text{Unblock } e] = \mathcal{U}[e]$	$\mathcal{U}[\text{Unblock } e] = \text{fork } \mathcal{U}[e]$

Fig. 4. Monadic Semantics for Hutton and Wright’s Language using MMAE Framework

$$\begin{aligned}
& \mathcal{U}[\text{Add } (\text{Val } 1) \ (\text{Val } 2)] \\
&= \text{fork}(\text{fork}(\text{step}(\eta_E 1)) \star_R \lambda v_1. \text{fork}(\text{step}(\eta_E 2)) \star_R \lambda v_2. \eta_R(v_1 + v_2)) \\
&\quad \{\text{thm 1, def 6}\} \\
&= \text{fork}(\text{fork}(\text{step}(\eta_E 1)) \star_R \lambda v_1. \text{merge}_R \{(\text{step}(\eta_E 2)) \star_R f, \text{throw}_R \star_R f\}) \\
&\quad \text{where } f = \lambda v_2. \eta_R(v_1 + v_2) \\
&\quad \{\text{thm 4}\} = \text{fork}(\text{fork}(\text{step}(\eta_E 1)) \star_R \lambda v_1. \text{merge}_R \{(\text{step}(\eta_E 2)) \star_R f, \text{throw}_R\}) \\
&\quad \{\text{lem 1}\} = \text{fork}(\text{fork}(\text{step}(\eta_E 1)) \star_R \lambda v_1. \text{merge}_R \{\text{Pause } (\eta_E (f 2)), \text{throw}_R\}) \\
&\quad \{\text{thm 1, def 6}\} \\
&= \text{fork}(\text{merge}_R \{(\text{step}(\eta_E 1)) \star_R f', \text{throw}_R \star_R f'\}) \\
&\quad \text{where } f' = \lambda v_1. \text{merge}_R \{\text{Pause } (\eta_E (f 2)), \text{throw}_R\} \\
&\quad \{\text{thm 4}\} = \text{fork}(\text{merge}_R \{(\text{step}(\eta_E 1)) \star_R f', \text{throw}_R\}) \\
&\quad \{\text{lem 1}\} = \text{fork}(\text{merge}_R \{\text{Pause } (\eta_E (f' 1)), \text{throw}_R\}) \\
&\quad \{\text{def } f, f'\} = \text{fork}(\text{merge}_R \{\text{Pause } (\eta_E (\text{merge}_R \{\text{Pause } (\eta_E 3), \text{throw}_R\})), \text{throw}_R\}) \\
&\quad \{\text{def fork}\} = \text{merge}_R \left\{ \begin{array}{l} \text{merge}_R \left\{ \begin{array}{l} \text{Pause } (\eta_E (\text{merge}_R \left\{ \begin{array}{l} \text{Pause } (\eta_E (\eta_R 3)), \\ \text{throw}_R \end{array} \right\})), \\ \text{throw}_R \end{array} \right\}, \\ \text{throw}_R \end{array} \right\}
\end{aligned}$$

The last term in the above evaluation is written in a manner that emphasizes the underlying tree-like structure of denotations in \mathbf{R} . The merge_R operators play the rôle of a “tree branch” constructor with throw_R and $\text{Pause } (\eta_E (\eta_R 3))$ as the “leaves”. This term exposes the operational content of $\mathcal{U}[\text{Add } (\text{Val } 1) \ (\text{Val } 2)]$. To wit, either an asynchronous exception occurs at *Add* or it doesn’t (left-most merge_R); or, an asynchronous exception occurs at *(Val 1)* or it doesn’t (middle merge_R); or, an asynchronous exception occurs at *(Val 2)* or it doesn’t (right-most merge_R); or, it returns the integer 3. Indeed, this single algebraic term captures all four evaluations from Figure 3. The term displayed in Example (†) in Section 1.1 can be recovered by projecting $\mathcal{U}[(\text{Add } (\text{Val } 1) \ (\text{Val } 2))]$ to the \mathbf{E} monad via *run*:

$$\text{run } \mathcal{U}[(\text{Add } (\text{Val } 1) \ (\text{Val } 2))]$$

$$\begin{aligned}
&= \text{run} (\text{merge}_R \{ \text{merge}_R \{ \varphi, \text{throw}_R \}, \text{throw}_R \}) \\
&\quad \text{where } \varphi = \text{Pause}(\eta_E(\text{merge}_R \{ \text{Pause}(\eta_E(\eta_R 3)), \text{throw}_R \})) \\
\{\text{thm } 2\} &= \text{merge}_E \{ \text{run} (\text{merge}_R \{ \varphi, \text{throw}_R \}), \text{run } \text{throw}_R \} \\
\{\text{thms } 2,5\} &= \text{merge}_E \{ \text{merge}_E \{ \text{run } \varphi, \text{run } \text{throw}_R \}, \text{throw}_E \}
\end{aligned}$$

By repeated applications of Theorems 2 and 5, the definition of *run*, and left unit monad law, $\text{run } \varphi = \text{merge}_E \{ \eta_E 3, \text{throw}_E \}$. Continuing:

$$\{\text{thm } 5\} = \text{merge}_E \{ \text{merge}_E \{ \text{merge}_E \{ \eta_E 3, \text{throw}_E \}, \text{throw}_E \}, \text{throw}_E \}$$

Theorem 6 establishes an equivalence between Hutton and Wright’s natural semantics (Figure 2) and the MMAE semantics in Figure 4. Due to the presence of non-determinism in both semantic specifications, there is more than one possible outcome for any expression. The theorem states that, for any expression e , a particular outcome (i.e., $(Ok\ v)$ or $Error$) may occur in the set of all possible outcomes for e (i.e., $\text{run}(\mathcal{U}[e])$ or $\text{run}(\mathcal{B}[e])$) if, and only if, the corresponding term (respectively, $(Val\ v)$ or $Throw$) can be reached via the natural semantics. The proof of Theorem 6 is straightforward and long, so rather than including it here, it has been made available online [8].

Theorem 6 (Semantic Equivalence). *For any expression e :*

$$\begin{aligned}
(Ok\ v) \in \text{run}(\mathcal{U}[e]) &\text{ iff } e \Downarrow^U (Val\ v) & (Ok\ v) \in \text{run}(\mathcal{B}[e]) &\text{ iff } e \Downarrow^B (Val\ v) \\
Error \in \text{run}(\mathcal{U}[e]) &\text{ iff } e \Downarrow^U Throw & Error \in \text{run}(\mathcal{B}[e]) &\text{ iff } e \Downarrow^B Throw
\end{aligned}$$

5 The MMAE as a Programming Model

This section demonstrates the use of the MMAE in the functional programming of applications with asynchronous behaviors. In particular, the extension of synchronous concurrent kernels with asynchronous, interrupt-driven features is described. The approach elaborated here develops along the same lines as modular interpreters [16] and compilers [11]: an existing specification is extended with a “building block” consisting of a monadic “module” and related operators. By applying the interrupts building block to a kernel without interrupts (middle, Figure 1), a kernel with interrupt-driven input is produced with virtually no other changes.

Recent research has demonstrated how kernels with a broad range of OS behaviors (e.g., message-passing, synchronization, forking, etc.) may be formulated in terms of resumption monads [9]. This section outlines the extension of such resumption-monadic kernels with asynchronous behaviors. The presentation is maintained at a high-level in order to expose the simplicity of the asynchronous extension. Interested readers may refer to the code base [8] or to Harrison [9] for the more details concerning kernel construction. The presentation here will frequently appeal to the reader’s intuition in order to remain at a high-level.

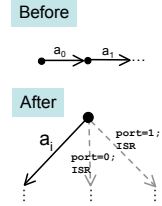
The kernel and its enhanced functionality are described in Figure 1. The enhanced kernel is kept as simple as possible. It contains no mechanism for blocking interrupts and there is only one interrupt service routine (ISR) connecting the kernel to the input port. Such enhancements are, however, quite simple to make.

Both kernels (Figure 1, middle and right) support synchronous message-passing, maintaining a message queue, `msgQ`, to hold messages “in-flight”. The extended kernel (Figure 1, right) adds an interrupt-driven serial-to-parallel input port. When a bit is ready at the port, an interrupt is thrown, causing the ISR to run. This ISR inserts the new bit into its local bit queue, `bitQ`. If there are eight bits in `bitQ`, then it assembles them into a byte and inserts this new datagram into `msgQ`, thereby allowing user threads to receive messages through the port. Pseudocode for the ISR is:

```
insertQ(port,bitQ);
if (length(bitQ) > 7) {
    make_datagram(x);
    insertQ(x,msgQ);
}
```

Here, `port` is the bit register in the port that holds the most recent input bit.

A resumption-monadic kernel is an $R()$ -valued function that takes as input a list of ready threads. In other words, $kernel \langle ready \rangle : R()$, where $\langle ready \rangle$ is a Haskell representation of the thread list. The output of the kernel is, then, a (possibly infinite) R -computation consisting of a sequence of atomic actions a_i executed in succession as shown in the inset figure (top, Before). These actions are drawn from user threads in $\langle ready \rangle$ that have been woven together into a schedule by the kernel. The notion of computation associated with R will also be changed as outlined in Section 5.1. The key to “turning on” asynchronous interrupts is to view each break-point “•” in the inset figure (top, Before) as a branching point. This branch contains three possible histories (inset figure (bottom, After)): one history in which the scheduled atom a_i executes, another where the ISR executes with the input bit 0, and a third where the ISR executes with the input bit 1. The branching can be achieved as in Section 4 with the use of an appropriate *merge* operator. The operation of the enhanced kernel is a computation that elaborates a tree.



There are only two changes necessary to introduce asynchronous interrupts to the synchronous kernel. The first is simply to refine the monad by incorporating non-determinism and state for the port device into the monad R . The second modification is to apply the appropriate *resumption map* to $(kernel \langle ready \rangle)$. Resumption maps arise from the structure of R in an analogous manner to the way the familiar list function, $map : (a \rightarrow b) \rightarrow [a] \rightarrow [b]$, operates over lists. The resumption map used here is discussed below in Section 5.2.

5.1 Monad Hierarchy

Two monads associated with the kernel are the kernel monad, K , and the concurrency monad R . K encapsulates the imperative aspects of the system. These are kernel-level operations that update system data structures (*Sys*) and user-level

operations that update the user-level storage (*Sto*). K is constructed with the application of two state monad transformers to the identity monad, Id .

$$K = \text{StateT } Sys (\text{StateT } Sto \text{ Id})$$

The concurrency monad R is then built on top of K with an application of the resumption monad transformer [22]:

$$\begin{aligned} R &= \text{ResT } K \\ \text{data ResT } m \ a &= Done \ a \mid Pause \ (m \ (ResT \ m \ a)) \end{aligned}$$

Note that this monad transformer provides an alternative means of defining the R monad from Section 4: $R = \text{ResT } E$.

5.2 Resumption Maps

There is a close analogy between computations in R and lists and streams. A non-trivial computation in R consists of a (possibly infinite) sequence of atomic actions in K separated by *Pause* constructors. Definition 7 specifies a resumption map that takes a function h and an R -computation γ and transforms γ one atom at a time:

Definition 7 (Map on R).

$$\begin{aligned} \text{map}_R &: (R \ a \rightarrow K(R \ a)) \rightarrow R \ a \rightarrow R \ a \\ \text{map}_R \ h \ (Done \ v) &= Done \ v \\ \text{map}_R \ h \ (Pause \ \varphi) &= Pause \ (h \ (Pause \ \varphi) \star_K (\eta_K \circ \text{map}_R \ h)) \end{aligned}$$

5.3 Adding Asynchronicity in Two Steps

Now, the stage is set to add asynchronicity to the synchronous kernel. The first step provides additional computational resources to K and the second uses these resources to “turn on” asynchronicity.

First Step: Adding Non-determinism and a Device to K . The first change to the synchronous kernel is to add non-determinism and storage for the port device to the K and R monads. This is accomplished by replacing Id with N in the definition of K and with another application of the state monad transformer:

$$\begin{aligned} K &= \text{StateT } Dev (\text{StateT } Sys (\text{StateT } Sto \ N)) \\ Dev &= [Bit] \times Bit \end{aligned}$$

The N is defined exactly as in Section 4. A device state is a pair, $(bitQ, new)$, consisting of a queue of bits, $bitQ$, and a single bit denoting the current input to the port. The K monad has separate operators for reading and writing the *Dev*, *Sys* and *Sto* states along with a merge operation, $\text{merge}_K : \mathcal{P}_{\text{fin}}(K \ a) \rightarrow K \ a$, that is defined in terms of merge_N . Note also that R is also affected by this refinement of K , but that the text of its definition is not (i.e., it is still the case that $R = \text{ResT } K$). Details may be found in the code base [8].

Second Step: Asynchronicity via Resumption Mapping. First, a resumption mapping is defined that creates a branch with three possible histories.

$$\begin{aligned}
& \text{branches} : \mathbf{R} a \rightarrow \mathbf{K}(\mathbf{R} a) \\
& \text{branches} (\text{Pause } \varphi) = \text{merge}_\kappa \left\{ \begin{array}{l} \varphi, \\ (\text{port}=0 ; \text{ISR}) \gg_\kappa \varphi, \\ (\text{port}=1 ; \text{ISR}) \gg_\kappa \varphi \end{array} \right\} \begin{array}{l} (1) \\ (2) \\ (3) \end{array}
\end{aligned}$$

Here, $(\text{port}=0 ; \text{ISR})$ represents a \mathbf{K} -computation that performs the indicated actions. History (1) is one in which no interrupt occurs. In history (2), an interrupt occurs when the input bit of the port is set to 0 and the ISR is then executed. Finally, the interrupted thread, φ , is executed. History (3) is analogous to (2).

Flipping the Port Off and On. It is now a simple matter to turn the port off and on. To operate the kernel with the port off, execute: $\text{kernel } \langle \text{ready} \rangle$. To operate the kernel with the port on, execute: $\text{map}_\mathbf{R} \text{branches } (\text{kernel } \langle \text{ready} \rangle)$. The two sample system runs from Figure 1 are repeated below. There are two threads, a producer and a consumer. The producer thread broadcasts integer messages starting at 1001 and incrementing successively with each broadcast. The consumer thread consumes these messages. When either thread performs a broadcast or receive, the instrumented kernel prints it out. Also, when the ISR creates a new datagram, it also announces the fact.

Port off: $\text{kernel } \langle \text{ready} \rangle$	Port on: $\text{map}_\mathbf{R} \text{branches } (\text{kernel } \langle \text{ready} \rangle)$
Haskell> producer_consumer	Haskell> producer_consumer
broadcasting 1001	broadcasting 1001
broadcasting 1002	new datagram: 179
receiving 1001	broadcasting 1002
broadcasting 1003	receiving 179
receiving 1002 ...	new datagram: 204 ...

6 Related Work

Implementations of threaded applications use monads to structure the threaded code. The Haskell libraries for concurrency use IO level primitives to provide a simple and robust user interface [23]. There have been other threading implementations that use other monads, including [14], which uses a continuation passing style monad, giving very high numbers of effectively concurrent threads.

There have been a number of efforts to model the concurrency provided by the Haskell IO monad. One successful effort is reported by Swierstra and Altenkirch [29], where they model the concurrency inside the Haskell IO monad using a small stepping scheduler combined with the QuickCheck framework [3] to provide random interleaving of pseudo-threads. In Dowse and Butterfield [5], an operational model of shared state, input/output and deterministic concurrency is provided. One fundamental difference between these models and our work is

granularity. Both of these models assume each computation can not be interrupted, and threads only are scheduled at the IO interaction points. Our model allows interrupts to happen inside computation, capturing the pre-emptive implementations of concurrency described in [17, 24], and provided by the Glasgow Haskell compiler.

The language modeled with MMAE in Section 4 was adopted from recent research on the operational semantics of asynchronous interrupts [13]. That research also identified an error within a published operational semantics for interrupts in Haskell [17]. Morris and Tyrrell [20] offer a comprehensive mathematical theory of nondeterminacy, encompassing both its demonic and angelic forms. Their approach adds operators for angelic and demonic choice to each type in a specification or programming language. They present refinement calculus axioms for demonic and angelic nondeterminacy and define a domain model to demonstrate the soundness of these axioms. One open question is whether there exists common ground between their work and the monadic approach presented here. In particular, if the domain theoretic constructions underlying Morris and Tyrrell’s semantics may be expressed usefully as monads.

7 Future Work & Conclusions

There has been considerable interest of late in using monads to structure system software [14, 7], model it formally [9], and to enforce and verify its security [10]. None of this previous research contains an model of asynchronous behavior and the MMAE was developed as a means of rectifying this situation. The present work is part of an ongoing effort to use monads as an organizing principle for formally specifying kernels. Currently, the direct compilation of monadic kernel designs to both stock and special purpose hardware is being investigated. This research agenda considers these monadic specifications as a source language for semantics-directed synthesis of high-assurance kernels. The ultimate goal of this agenda is to produce high-confidence systems automatically.

What possible applications might our specification methodology have? We have shown is that it is possible to take a MMS constructed executable specification, add a non-deterministic monad giving an accurate monadic based semantics for interrupts. Here we briefly sketch a possible way of using our semantics in practical applications.

Non-determinism is used to model *all* possible outcomes. In an implementation, a lower-level model could replace the use of non-determinism with the exception monad. The exception monad would be used to communicate an interrupt in exactly the same way it would be used to communicate an exception. The fork primitive in our semantics would be implemented in a lower-level model using a simple poll of an interrupt flag. Specifically, in our kernel example, we might use this MMS for K in the lower-level model.

$$\begin{aligned} K &= \text{StateT Dev (StateT Sys (StateT Sto Maybe))} \\ &\text{--- } K = \text{Dev} + \text{Sys} + \text{User} + \text{Exception} \end{aligned}$$

This gives us a basis for an efficient implementation from our monadic primitives. We use the code locations that perform interrupt polling in the lower-level model as *safe-points*. If an interrupt request is made of a specific thread, this thread can be single-stepped to any safe-point, a common implementation technique for interruptible code.

Thus as an implementation path we have our high-level model using non-determinism, a low-level model using exceptions to simulate interrupts, and a possible implementation with precisely specified safely interruptible points that can be implemented using direct hardware support. This chaining of specifications toward implementation is a long standing problem in building non-deterministic systems, and this research provides an important step toward a more formal methodology of software development of such systems.

The MMAE confronts one of the “impurities” of the infamous “Awkward Squad” [23]: language features considered difficult to accommodate within a pure, functional setting—concurrency, state, and input/output. Most of these impurities have been handled individually via various monadic constructions (consider the manifestly incomplete list [18, 25, 22]) with the exception of asynchronous exceptions. The current approach combines these individual constructions into a single *layered* monad—i.e., a monad created from monadic building blocks known as monad transformers [15, 6]. While it is not the intention of the current work to model either the Haskell *IO* monad or Concurrent Haskell, it is believed that the techniques and structures presented here provide some important building blocks for such models.

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