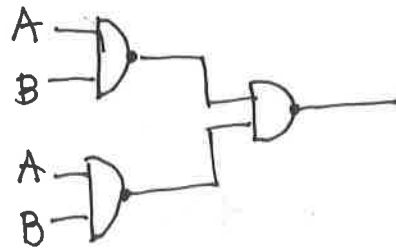


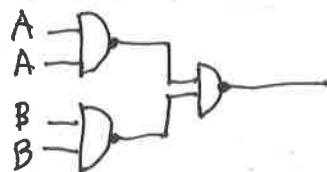
## Problem 1

### NAND Gates

1) A AND B



2) A OR B

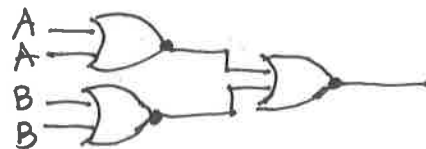


3) NOT A

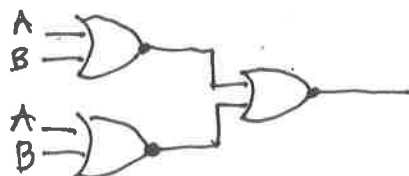


### NOR Gates

1) A AND B



2) A OR B



3) NOT A



CSF Assignment 1 Circuits

Diagrams pdf.

Name: Joon Hyuck Choi

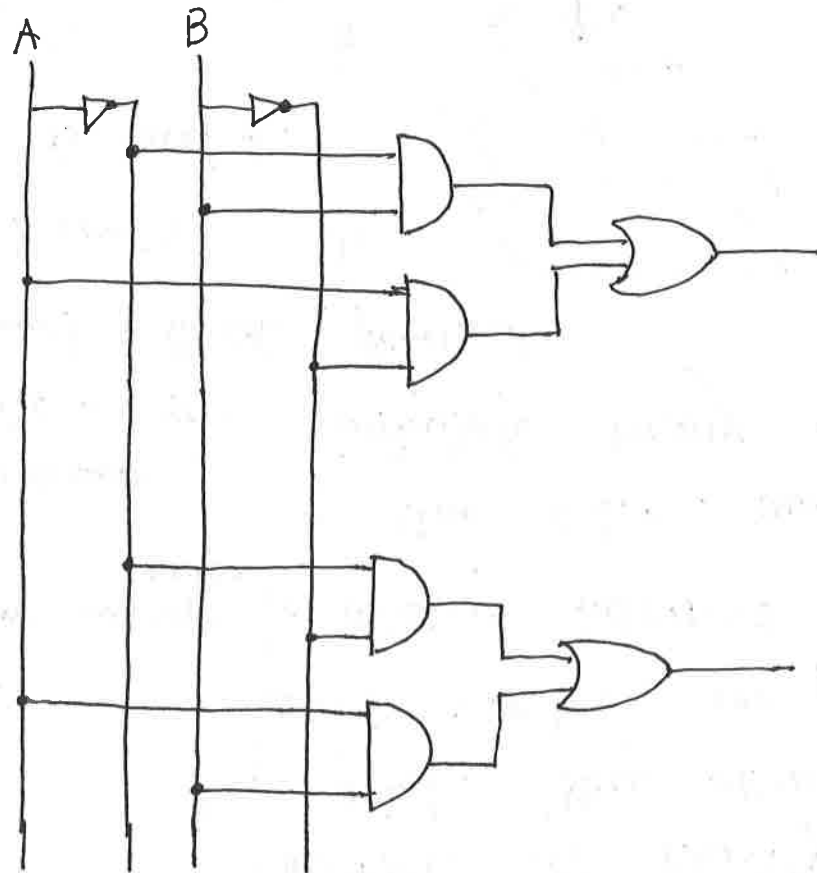
email: jchoi100@jhu.edu

JHE-D: jchoi100

## Problem 2

1) XOR:

2) XNOR:



# Problem 3

