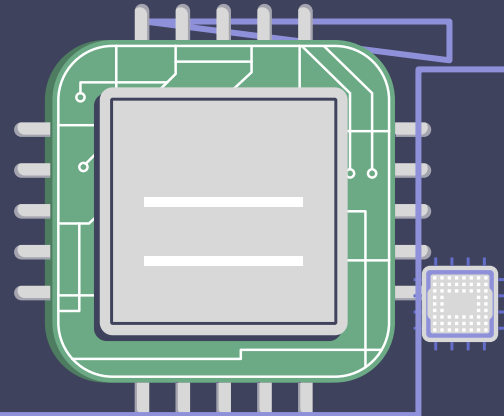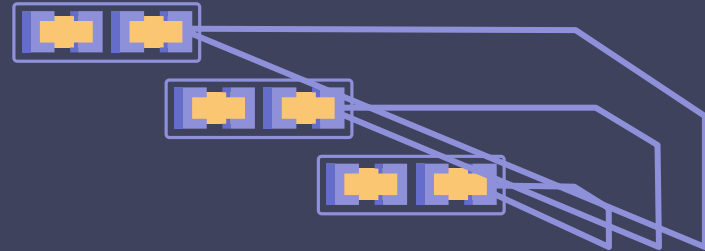# Milestone 3 Progress

John Collison,

# Current State of Processor

➢ **Memory Integration**

➢ Store queue, D-Cache, Victim cache complete

➢ I-Cache in progress

➢ Connecting modules to main CPU

➢ **Other**

➢ Replacing current branch predictor with GShare, written and tested