

### AVR Multi Motor Control

Facoltà di Ingegneria dell'Informazione, Informatica e Statistica Corso di Laurea in Ingegneria Informatica e Automatica

Candidate
Paolo Lucchesi
ID number 1765134

Thesis Advisors

Prof. Giorgio Grisetti

Drs. Barbara Bazzana

Academic Year 2021/2022

Thesis not yet defended

Reviewer:

Prof. Silvia Bonomi

#### AVR Multi Motor Control

Bachelor's thesis. Sapienza – University of Rome

 $\ensuremath{{\mathbb C}}$  2021 Paolo Lucchesi. All rights reserved

This thesis has been typeset by LATEX and the Sapthesis class.

Website: https://github.com/jcondor98/ammc

Author's email: lucchesi.1765134@studenti.uniroma1.it

Dedicated to my family, my granddad Pietro, my mom Anna Rosa, my dad Marco, my grandmom Pierina and my sister Valentina, which I thank everyday for everything I am.

To my dearest friends in Pitigliano, with whom I share some of the most beautiful  $memories\ I$  have.

To Nicola, with whom I have shared part of this path; he helped me in a dark period of my life and he is one of my dearest friends.

### Abstract

## Contents

1	Intr	roduction	1
<b>2</b>	Clie	ent-side user interaction	3
	2.1	User Interface	3
	2.2	Primitives offered	3
		2.2.1 Non-interactive mode	4
	2.3	Serial module	4
	2.4	Specification	4
		2.4.1 Software modules	4
		2.4.2 Modules dependency graph	4
	2.5	Man page	4
3	Mas	ster controller	5
	3.1	Hardware setup	5
	3.2	I2C setup	5
	3.3	Power management	5
	3.4	Specification	6
		3.4.1 Software modules	6
		3.4.2 Modules dependency graph	6
		3.4.3 Circuit schematics	6
		3.4.4 Wiring	6
4	Slav	ve controllers	7
	4.1	Hardware setup	7
	4.2	I2C setup	7
	4.3	Proportional-Integral-Derivative controller	8
	4.4	Power management	8
	4.5	Specification	8
		4.5.1 Software modules	8
		4.5.2 Modules dependency graph	8
		4.5.3 Circuit schematics	8
		4.5.4 Wiring	8
5	Clie	ent-Master communication	9
6	Mas	ster-Slave communication	11

viii	Contents
7 Conclusions	13
Bibliography	15

# Introduction

## Client-side user interaction

A client program has been realized to manipulate the dc motors directly from the PC. It can get and set the speed of individual motors, and to apply all the previously set speeds for all of them at once.

A brief list of the client's features is given below:

- Granular handling for getting and setting motors' speed
- Modular and extensible software architecture
- Terminal User Interface, implemented as a command shell
- Support for non-interactive use (i.e. scripting)
- Communication with master controller using the serial protocol
- Compatible with POSIX-compliant environments

The client is also documented with a man page, which can be found in section 2.5.

#### 2.1 User Interface

The end user interacts with the whole ammc ecosystem using a text-based client. It consists in a shell module, which I had written myself, offering some *internal commands* (hardcoded in the shell module itself) and is extended by *external commands* (found in a separated source code entity, and that can even be compiled in a detached transaction unit).

A particular focus was made on the software architecture: indeed, every external command can be realized standalone, and it is easy to add new commands just by altering the *client/source/shell commands.c* source file.

#### 2.2 Primitives offered

The commands that can be used to interface with the ammc ecosystem are the following:

- **connect** < device-path > Connect to a master controller, given the path to the block device representing it.
- **get-speed <motor-id>** Get the speed of a dc motor given its id. The motor id must be specified as a decimal number.
- **set-speed** <**motor-id**>=<**speed**> Set the speed of a dc motor given its id. The motor id must be specified as a decimal number and the speed must be specified in rpm.

apply Apply the previously set speed for all the dc motors.

#### 2.2.1 Non-interactive mode

The client shell is capable of running in non-interactive (i.e. scripting) mode with the -s option. If so, it will parse the input from a specified text file, or from stdin if not provided. A shell launched in non-interactive mode will not print shell prompts, and exit when end-of-file is encountered or on command failure.

#### 2.3 Serial module

The client's serial module has been realized using the POSIX termios interface. Unlike the master controller's counterpart, all its code is reentrant, therefore multiple instances of multiple serial devices can theoretically exist at the same time.

From the client's perspective, the master controller is seen as a file descriptor, and the end user just have to specify the path of the block device file representing the serial communication channel (e.g.  $/dev/ttyACM\theta$ ) using the connect command.

#### 2.4 Specification

- 2.4.1 Software modules
- 2.4.2 Modules dependency graph
- 2.5 Man page

### Master controller

The master controller handles all the slave controllers, dispaching arbitrary commands to them using the I2C protocol. It also communicates directly with the client application via serial port.

### 3.1 Hardware setup

The master controller itself is an AVR *ATMega2560* microcontroller unit[1]. This particular MCU has features convenient for this project, such as:

- I2C dedicated hardware subsystem
- Serial-over-USB bridge
- Relatively powerful specifications for future feature adding
- Plenty of timers and outgoing power pins

A 100pF capacitor is used to block the reset capabilities of the serial-over-usb controller and enhance the serial channel reliability. Two  $4.7k\Omega$  resistors are used as open-drain resistors for the I2C bus.

### 3.2 I2C setup

The I2C protocol is used to communicate with slave controllers. Transmission and reception are interrupt-based, and no busy-wait is used.

The I2C module has broadcasting capabilities, according to the informations found in the I2C standard I2C standard[2]; the broadcasting (i.e.  $general\ call$ ) address used is 0x00. As stated by the standard, the master controller has no knowledge on the number or identity of the slaves receiving a broadcast frame.

### 3.3 Power management

An own-written wrapper for the avr-gcc standard library's sleep functionalities is used for power management. By default, the master controller is in *idle* mode, and

6 3. Master controller

it is awakened by any raised interrupt, e.g. by incoming serial or I2C data; then, its main loop routine is executed and, if no other operations must be performed, it returns in idle mode.

The master controller is also put in idle when waiting for data inside serial or I2C routines; this is possible thanks to the interrupt-driven nature of the aforementioned modules.

### 3.4 Specification

- 3.4.1 Software modules
- 3.4.2 Modules dependency graph
- 3.4.3 Circuit schematics
- 3.4.4 Wiring

## Slave controllers

Each slave controller directly handles a single dc motor, receiving commands from the master controller via I2C. It must offer an interface to get and manipulate its motor's speed.

The slave controller produces a PWM wave having a duty cycle consistent with the speed (given in RPM) specified by the end user in order to control its motor.

Furthermore, it comes with a software Proportional-Integral-Derivative controller to correct the

#### 4.1 Hardware setup

The slave controller itself is an AVR *ATMega328P* microcontroller unit[3], which offers the following features of interest:

- CH340 Serial-over-USB controller for convenient firmware flashing
- Reduced size
- Relatively high clock and resources
- Dedicated I2C hardware subsystem
- PWM-capable 8-bit and 16-bit timers

Apart from the motor itself (along with its power supply and eventually its dedicated control board) no additional hardware is used.

### 4.2 I2C setup

A slave controller communicates with the master controller using the I2C protocol. As for the master controller, both transmission and reception are interrupt-based.

- 4.3 Proportional-Integral-Derivative controller
- 4.4 Power management
- 4.5 Specification
- 4.5.1 Software modules
- 4.5.2 Modules dependency graph
- 4.5.3 Circuit schematics
- 4.5.4 Wiring

# Client-Master communication

# Master-Slave communication

# Conclusions

# Bibliography

- $[1] \ \ Atmel, \ Atmel \ ATmega640/V-1280/V-1281/V-2560/V-2561/V \ \ data sheet, \ 2014.$
- $[2] \ \ Philips \ Semiconductors, \ \textit{The I2C-bus Specification}, \ 2000.$
- $[3] \ Atmel, \, Atmel \, ATmega 328P \, \, data sheet, \, 2015.$