Progress Report – 2/4/2022

Jackson Cornell

* Schematics finished for theremin, pre-amp, FPGA, codec, connectors, and power
* Simulations began for FPGA pitch detector algorithm
* Theremin constructed on breadboard

Obstacles that were overcame include getting the entire pitch detection algorithm working in simulation, finalizing board connections, and finalizing the power circuit design. There are currently problems getting the pitch detection working on the FPGA and getting the theremin working on the breadboard.

A picture containing graphical user interface

Description automatically generated

