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(/)

B.Tech. (EVD)

Program
Overview

Program
Structure

Bachelor of Technology (B. Tech.) in Electronics and VLSI Design (EVD)



DA-IICT launched the unique four-year B. Tech in Electronics and VLSI Design (EVD) from the Academic Year 2023-24 with an objective to create industry ready undergraduate manpower for VLSI industry. The main objective of the BTech (EVD) program is to equip students with necessary core competency to succeed long-term in engineering/ entrepreneurship careers and preparing them for higher studies and research as career options. The curriculum offers a strong foundation in the first two years and then provides the student opportunity to specialise in VLSI System Design and Electronics System Design that trains them both the technical and entrepreneurial skills needed to become a leader in this industry.

Students will learn to use industry ~ standard software such as Cadence, Synopsys, OrCad, MATLAB, Xilinx, etc. and hardware boards viz. FPGA, CPLD, etc. to design

Integrated Circuits. For some students, the student design project may culminate as a scientific paper that will help immensely in their career and will open up future study options in their particular field of specialization. Students will also gain direct industry experience through internships. The rural internship during their course will enable the students to appreciate the socio ~ cultural aspect of their course. The program includes compulsory Summer Research Internships to be taken up at various research organizations within and abroad.

The program brochure can be found **here**
(<https://www.daiict.ac.in/sites/default/files/other-files/BTech-EVD-Brochure.pdf>).

Program Outcomes (POs)

PO No.	Program Outcomes
PO1	Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
PO2	Problem analysis: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences
PO3	Design/development of solutions: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
PO4	Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO5	Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
PO6	The engineer and society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
PO7	Environment and sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
PO8	Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
PO9	Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
PO10	Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
PO11	Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
PO12	Life-long learning: Recognize the need for, and have the preparation and ability to


engage in independent and life-long learning in the broadest context of technological change.

The Program Specific Outcomes (PSOs) set the following goal:

After the successful completion of the BTech (EVD) program, students will have:

PSO No.	Program Specific Outcomes (PSOs)
PSO1	To apply the concepts of Electronics, VLSI Systems and Design industrial applications.
PSO2	Develop system solutions involving both hardware and software modules.
PSO3	To work as a socially responsible professional by applying EVD technology in real-world scenarios and applications.

Contact Us

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NAAC

- › [SSR & Evaluative Reports Addendum 2017 !\[\]\(b7e1c8bc060ab2af8bc42ce81bfcf3c4_img.jpg\) \(/sites/default/files/NAAC-Addendum-final.pdf\)](/sites/default/files/NAAC-Addendum-final.pdf)
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CoE, Government of Gujarat

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NIRF

- › [INDIA Ranking 2023 DCS Submitted \(/nirf-national-institutional-ranking-framework\)](/nirf-national-institutional-ranking-framework)

Other Links

- › [Prof. S.C. Sahasrabudhe - A Memoir \(https://www.daiict.ac.in/prof-sc-sahasrabudhe-memoir\)](https://www.daiict.ac.in/prof-sc-sahasrabudhe-memoir)
- › [Holidays 2023 !\[\]\(3d0946c14414af438def0008e8322b30_img.jpg\) \(/sites/default/files/other-files/Holidays2023.pdf\)](/sites/default/files/other-files/Holidays2023.pdf)
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- > **Committees**  (<https://www.daiict.ac.in/committees>)
- > **Anti-Ragging Committee**  (/sites/default/files/other-files/Anti-Ragging_Vigilance-Committee_Faculty-and-Staff_2022-23.pdf)
- > **DA-IICT Lecture Series** (/da-iiict-lecture-series-dls)
- > **Synapse**  (<https://instagram.com/synapsedaiict>)
- > **Concours**  (<http://concours.daiict.ac.in/>)
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Group Website



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