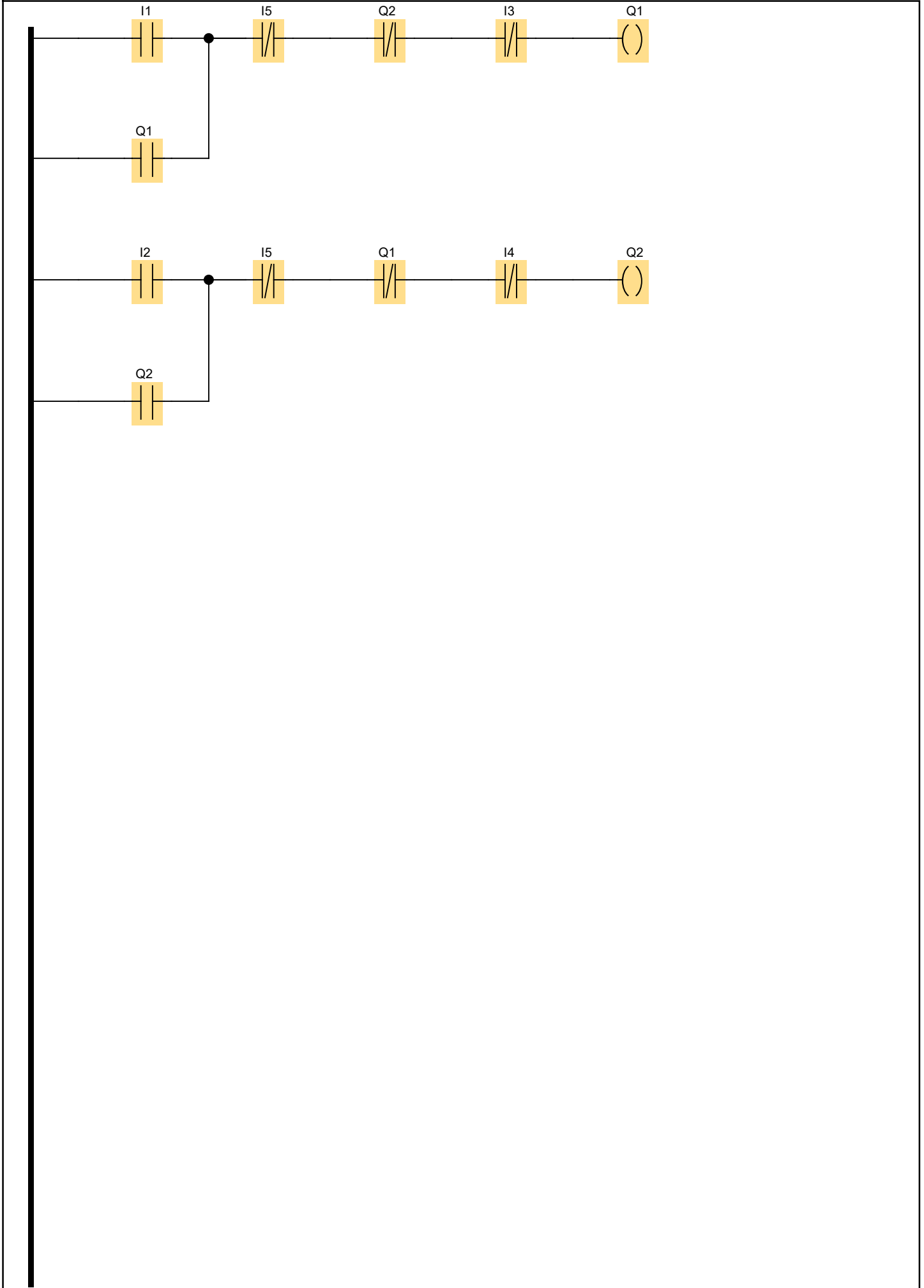


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Date:	10/15/24 11:48 AM/10/16/24 1:57 AM		File:	ejercicio 12.Ild	Page:	1 / 2

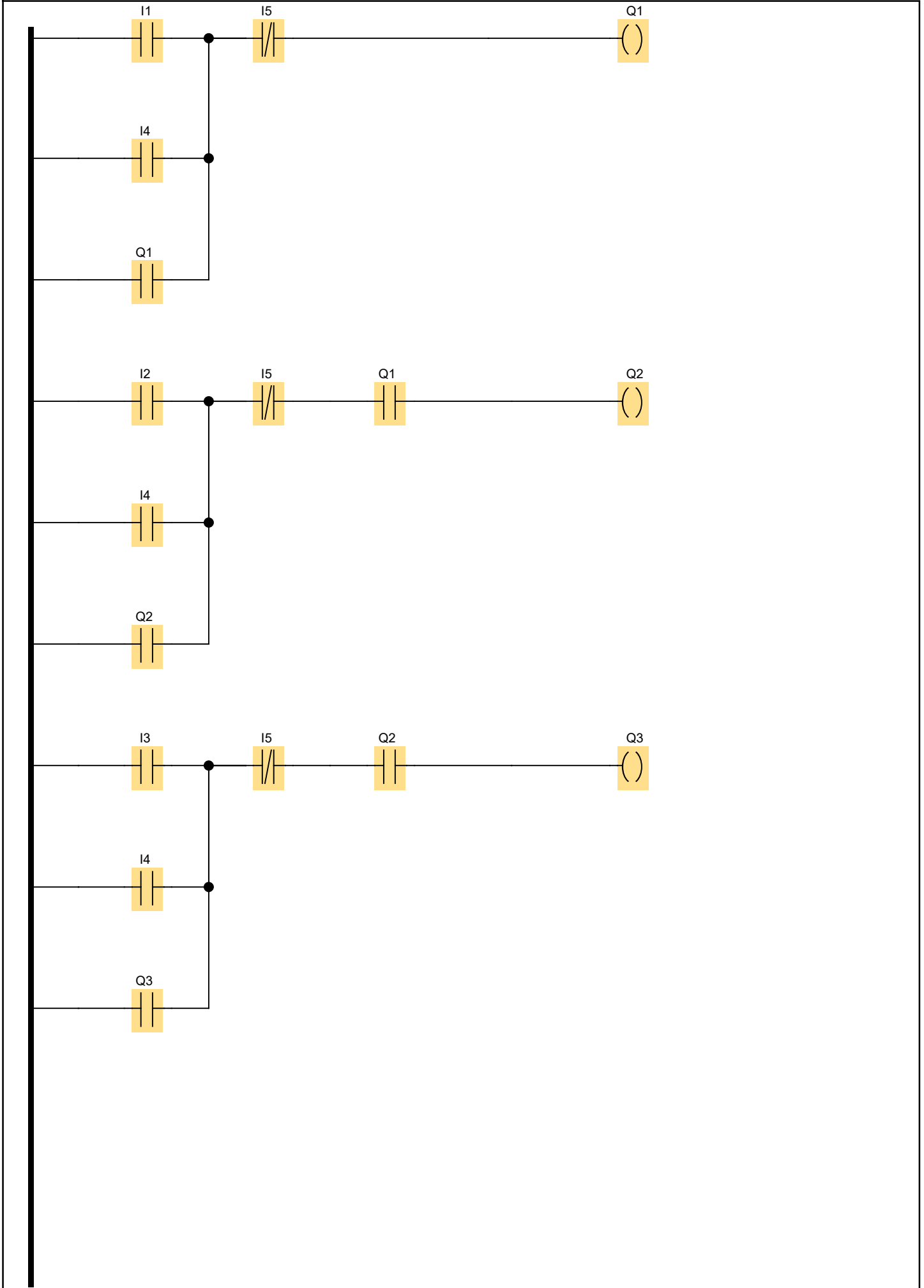




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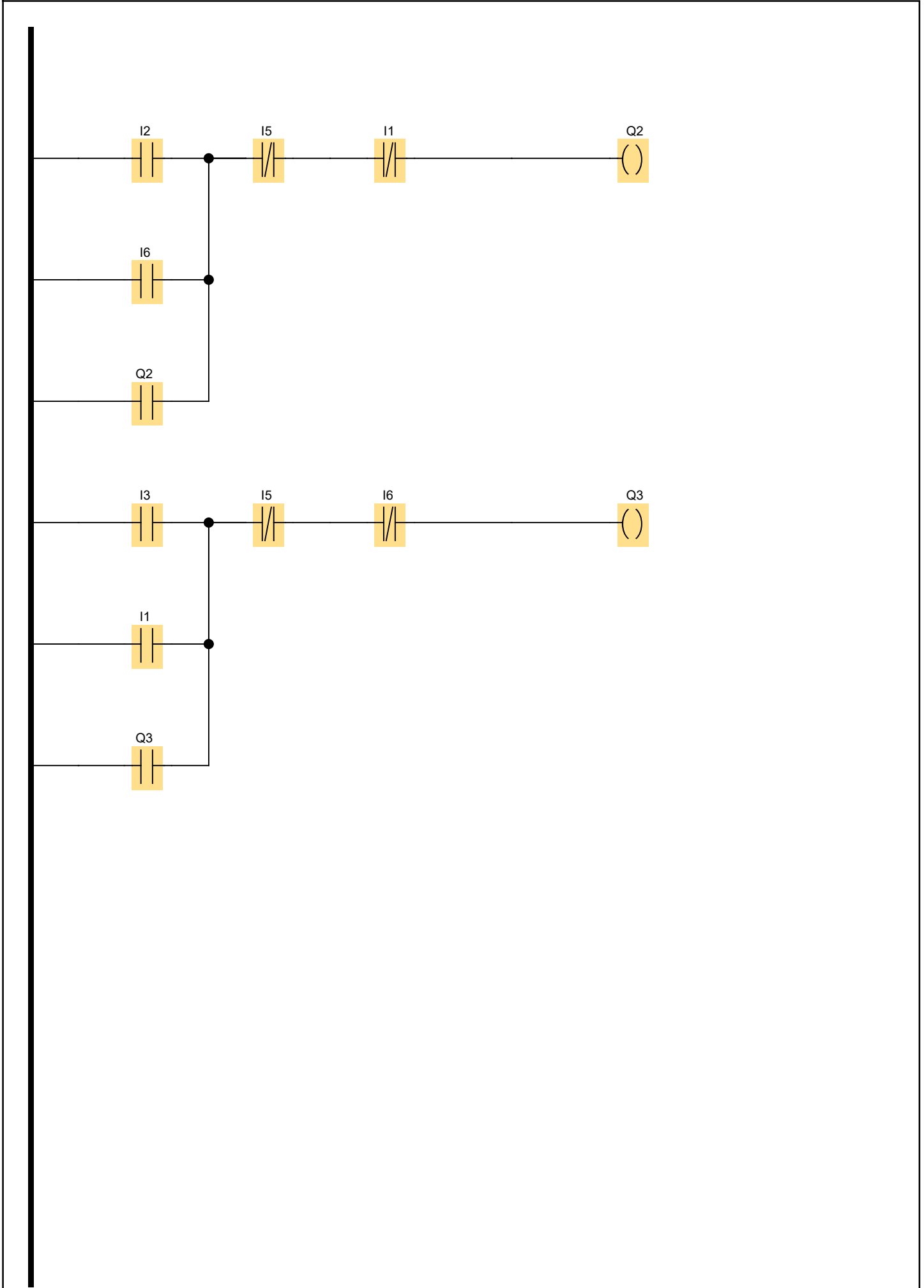






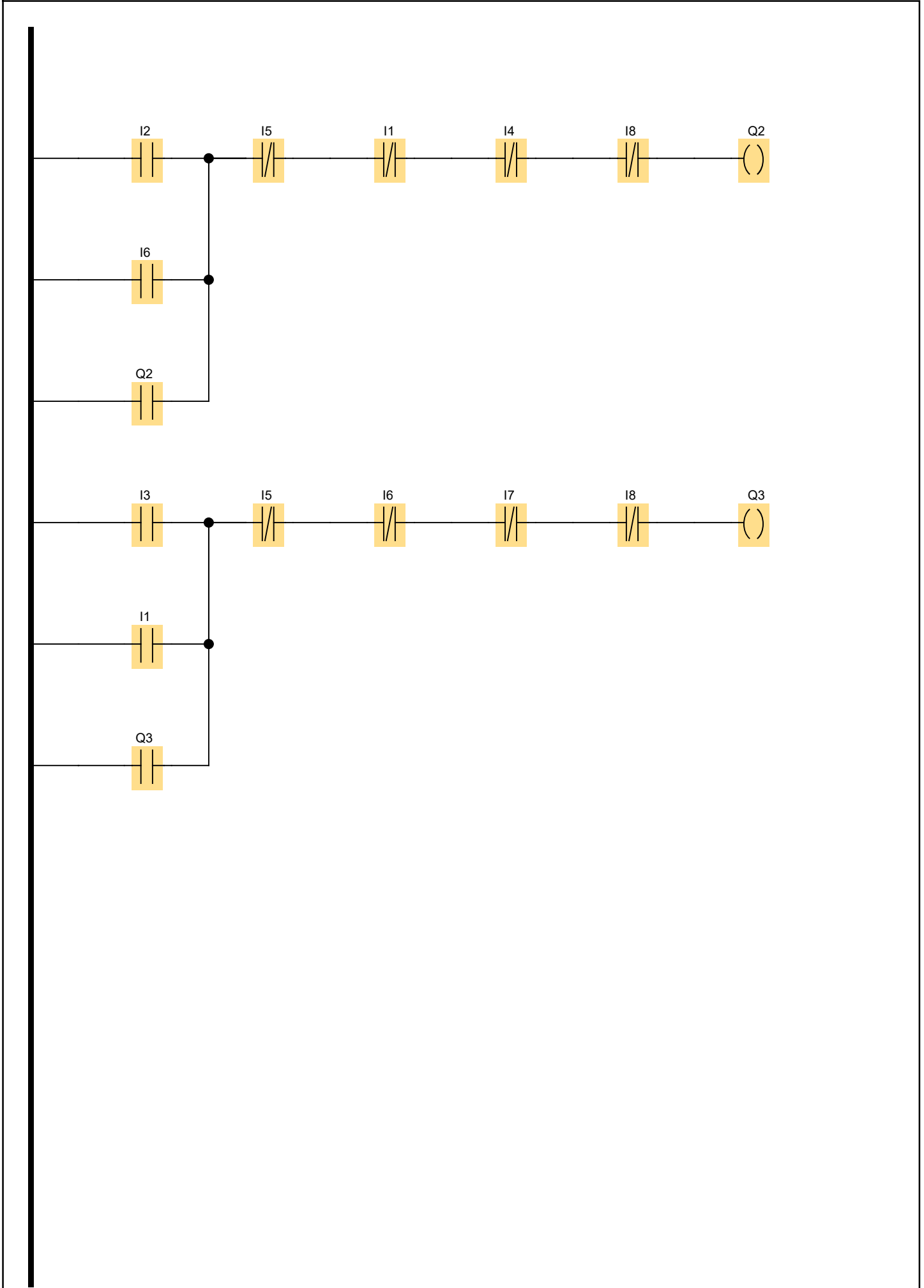
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Date:	10/15/24 11:48 AM/10/16/24 1:58 AM		File:	ejercicio 14.1ld	Page:	1 / 2



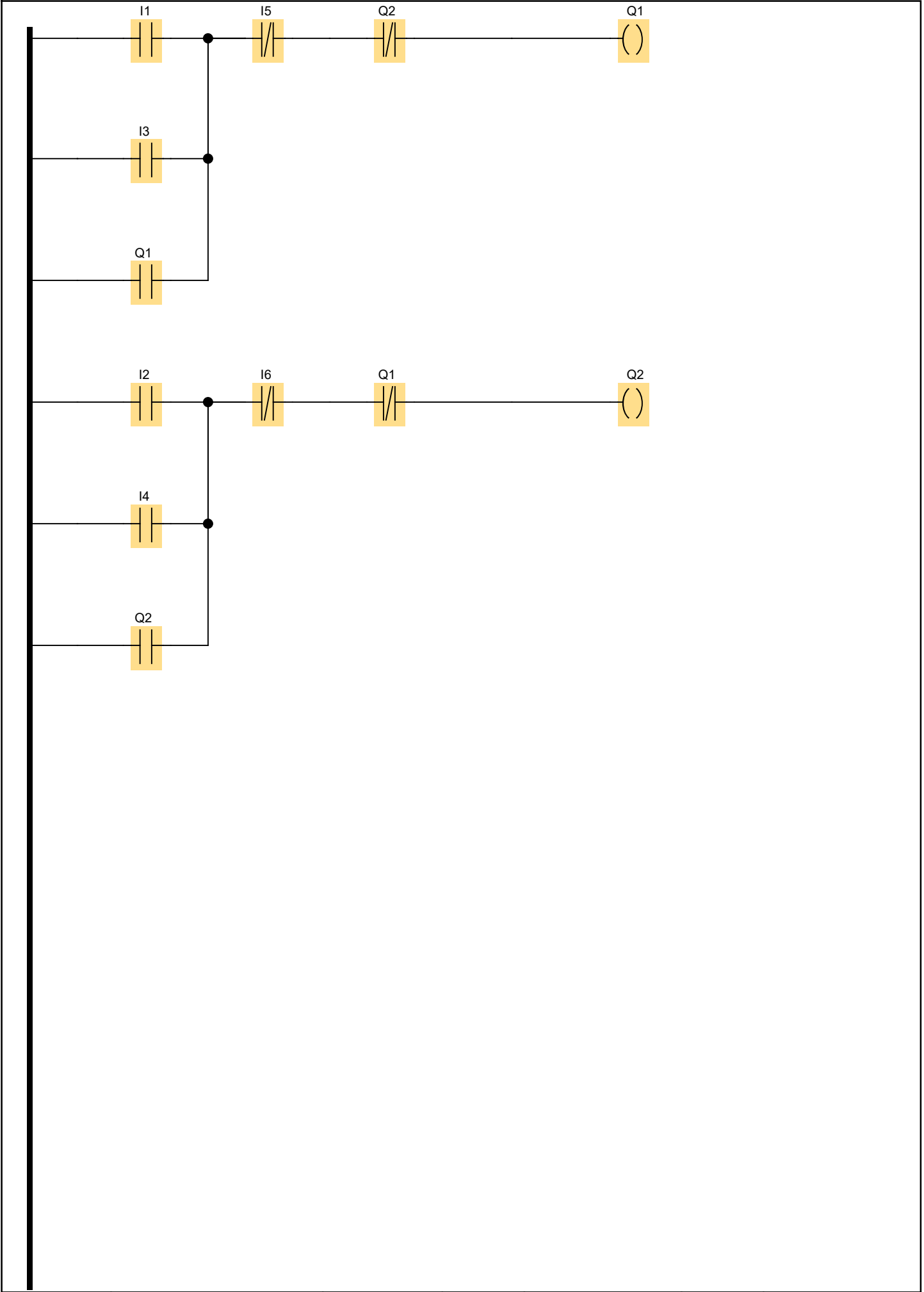


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Date:	10/15/24 11:48 AM/10/16/24 1:59 AM	File:	ejercicio 15.lld	Page:	1 / 2





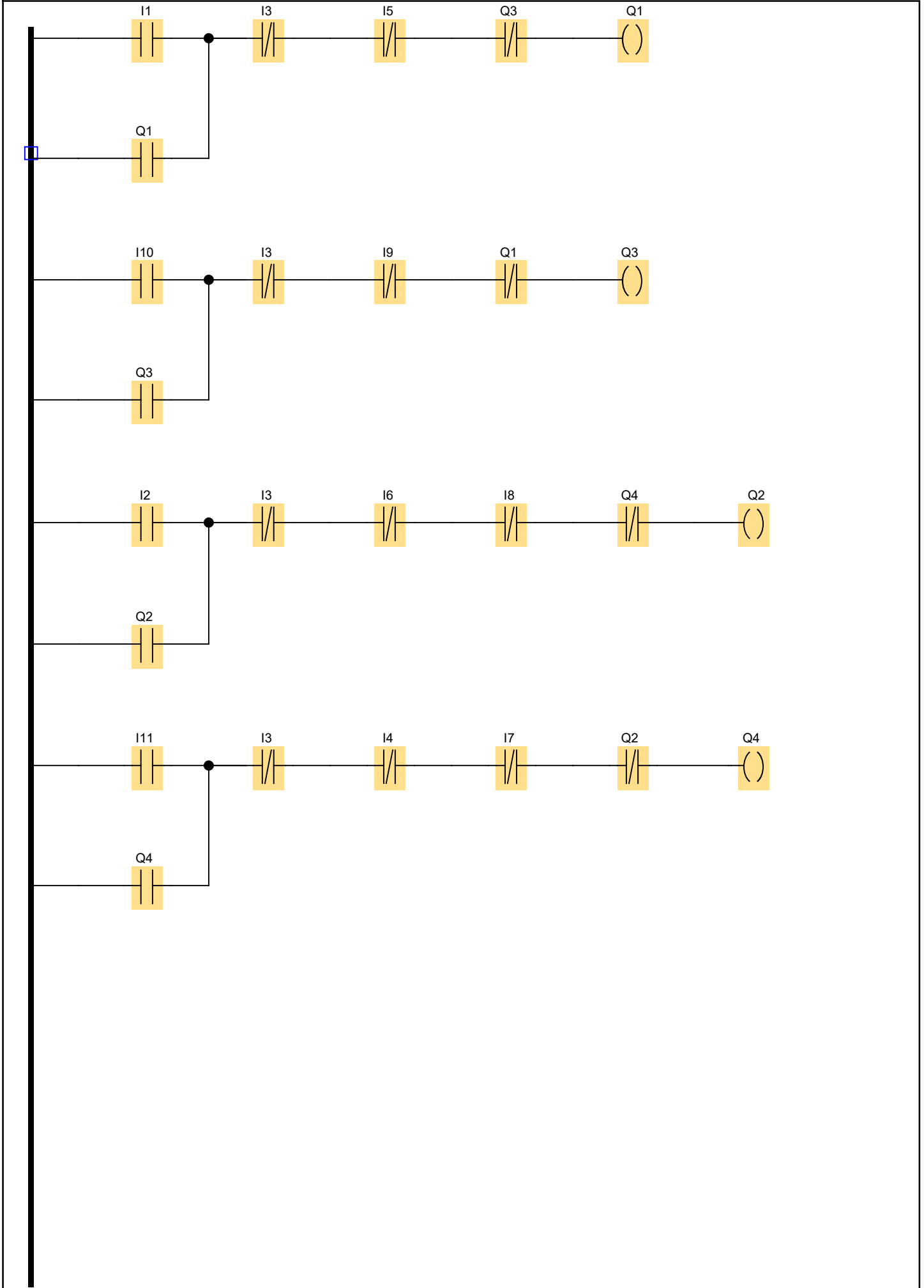
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I1											
I2											
I3											
I4											
I5											
I6											
I7											
I8											
Q2											
Q3											
Creator:		JCPElectrik		Project:				Customer:			
Checked:				Installation:				Diagram No.:			
Date:		10/15/24 11:48 AM/10/16/24 1:59 AM		File:		ejercicio 16.Ild		Page:		2 / 2	



Creator:	JCPElectrik		Project:		Customer:	
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Connection	Label
I1	
I2	
I3	
I4	
I5	
I6	
Q1	
Q2	





Creator:	JCPElectrik		Project:		Customer:	
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Date:	10/15/24 11:48 AM/10/16/24 1:29 AM		File:	ejercicio 18.lld	Page:	1 / 2

Connection	Label
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I2	
I3	
I4	
I5	
I6	
I7	
I8	
I9	
I10	
I11	
Q1	
Q2	
Q3	
Q4	



