BASIC CONTROLS

8 bit word size, Instructions fetch'd 4 bits at a time across 2 cycles

LOAD-PL
IJ — PL

FETCH11
IJ — IR

reset-n

INCR-PC
Id-temp=PC
ALU-OP: INCRPL

FETCH2
IJ — IR
Id-Decode

INCR-PC2
IJ-temp=PL
ALU-OP: INCRPC2

Nop

DECODE1
IJ — Decode

FIM, JUN
JCN, JMS,
ISE

FETCH21
IJ — IR

LDM

LDR

XCH

AOD

SUB

LDM
IJ — ALC = Decode

LDR
IJ — ALC = RRRR

XCH1
IJ-temp = ACC

XC43
IJ — Reg=temp

XCH2
IJ_ALC=RRRR

AOD
alu-op: Add
IJ — ACC
RRRR

SUB
alu-op: SUB
IJ — ACC
RRRR

FETCH22
IJ — IR
IJ — Decode

EXEC
Id-decode on
Load

DECODE2
IJ — Decode