

Jan Coleen Estilo																	
ST-7L - Exercise 11																	
Clock Cycle																	
Instruction Status																	
Instruction Status			j	k		Issue	Exec Comp	Write Result						Busy	Address		
	LD	F6	1+	R1									Load1	No			
	LD	F5	9+	R2									Load2	No			
	LD	F3	97+	R3									Load3	No			
	MUL	F3	F1	R2													
	ADD	F5	F4	R3													
	ADD	F6	F4	R1													
	ADD	F10	F5	R6													
Reservation Status					S1	S2	RS for j	RS for k									
Time	Name	Busy	Op	Vj	Vk	Qj	Qk										
	0 Add1																
	0 Add2																
	0 Add3																
	0 Mult1																
	0 Mult2																
	0 Mult3																
Register Result Status																	
Clock		F0	F1	F2	F3	F4	F5	F6	F7	F8	F9	F10	F11	F12	F13	F14	F15
	0 FU																
Clock Cycle 9																	
Instruction Status																	
Instruction Status			j	k		Issue	Exec Comp	Write Result						Busy	Address		
	LD	F6	1+	R1		1	2	3				Load1	No				
	LD	F5	9+	R2		2	3	4				Load2	No				
	LD	F3	97+	R3		3	4	5				Load3	No				
	MUL	F3	F1	R2		4 5-											
	ADD	F5	F4	R3		5	6-10										
	ADD	F6	F4	R1		6 7-											
	ADD	F10	F5	R6		7 8-											
Reservation Status					S1	S2	RS for j	RS for k									
Time	Name	Busy	Op	Vj	Vk	Qj	Qk										
	0 Add1	No															
	1 Add2	Yes	ADD	R(F4)	R1												
	2 Add3	Yes	ADD	R(F5)	R6												

[illegible]