

# JEEDIGI CHAITANYA VARDHAN

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**OBJECTIVE:** “seeking an internship opportunity in your esteemed organization to gain practical experience and contribute meaningfully to your ongoing success.”

## EDUCATION

**Bachelor of Technology in Electronics and Communication Engineering** (2nd Year accomplishing)

Sri Ramaswamy Memorial Institute of Science and Technology (SRM), Ramapuram, Chennai, Tamil Nadu | Expected 2027

CGPA: 9/10

**Senior Secondary (Class 12)** Percentage: 83%

Sri Chaitanya PU College, Bellary, Karnataka | 2023

**Secondary Education (Class 10)** Percentage: 83%

Sandur Residential School (ICSE), Sandur, Karnataka | 2021

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## TECHNICAL SKILLS

- **Data Analysis & Visualization Tools:** Tableau, Power BI
- **Programming:** Python, SQL, Java, C, COBOL, HTML and CSS
- **Data Engineering & Cloud Platforms:** SQL Server, AWS
- **Software Tools:** MS Office (Excel, PowerPoint, Word), MATLAB
- **Hardware Experiments** on Voltage Follower, Inverting and Non inverting amplifier, Summing amplifier, Integrator and Differentiator.
- **Multi Sim Software experiments** on Voltage Follower, Inverting and Non inverting amplifier.

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## ACADEMIC PROJECTS

- **AI Assistant for PC:** Developed a Python-based AI assistant integrated with API tools to enable seamless and efficient interaction with PCs and laptops.
- **Real-Time Weather Monitoring System:** Implemented an AI-powered weather monitoring system using Python and Arduino to provide accurate real-time weather information.
- **PC Control Using Hand Gestures:** Designed a touchless interaction system for PCs using Python, enabling users to control their devices through hand gestures.

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## ACADEMIC SUBJECTS

- Electrical and Electronics Engineering
- Electronic System and PCB Design
- Solid State Devices
- Devices and Digital Ic Laboratory
- Digital Logic Design
- Analog and Linear Electronic Circuits
- Analog and Linear Electronic Circuits Laboratory
- Programming for Problem Solving
- Artificial Intelligence
- Computer Organization and Architecture
- Signal Processing
- Control Systems: Theory and Applications
- Engineering Graphics and Design
- French Language
- Basic Civil and Mechanical Workshop

Declaration: - I hereby declare that all the particulars given above are true to the best of my knowledge.

Date: .....

Place: .....

[Jeedigi Chaitanya Vardhan]