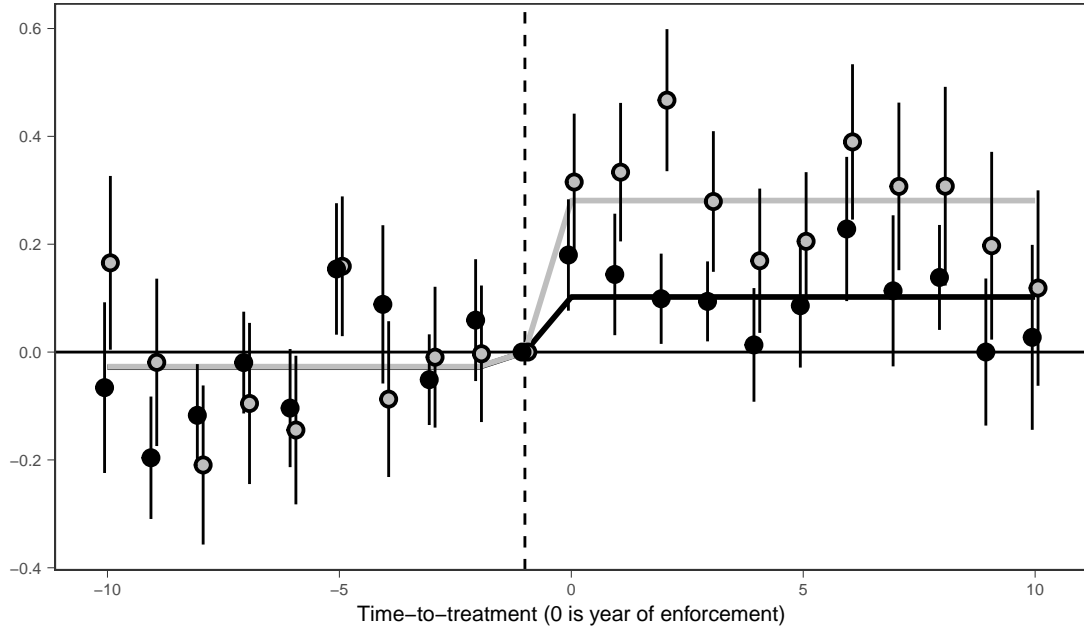


Effect on CPUE



Specification



With FEs



Without FEs