

Overview - Formal Specification

$Q = \{\text{Dormant}, \text{Init}, \text{Idle}, \text{Monitoring}, \text{Error Diagnosis}, \text{Safe Shutdown}\}$

$\Sigma_1 =$

$\{\text{start}, \text{init_ok}, \text{begin_monitoring}, \text{init_crash}, \text{idle_crash}, \text{monitor_crash}, \text{retry_init}, \text{idle}, \text{rescue}, \text{moni_rescue}, \text{shutdown}, \text{sleep}, \text{kill}\}$

$\Sigma_2 = \{\text{init_error_msg}, \text{idle_err_msg}, \text{moni_err_msg}, \text{retry}++, \text{load_drivers}, \text{confirm_drivers}, \text{begin_experiments}, \text{log_info}, \text{graceful_shutdown}, \text{moni_error_protocol}, \text{idle_error_protocol}, \text{turn_off}, \text{retry} = 0\}$

$q_0 = \text{Dormant}$

$V: \text{retry}: \mathbb{N}_0; \text{inlockdown} = \{\text{true}, \text{false}\}$

\wedge : Transitions specifications

1. $\rightarrow \text{Dormant}$

2. Dormant $\xrightarrow{\text{kill}}$ Exit

3. Dormant $\xrightarrow{\text{start} / \text{load_drivers}}$ Init

4. Init $\xrightarrow{\text{init_ok} / \text{confirm_drivers}}$ Idle

5. Init $\xrightarrow{\text{init_crash} / (\text{log_info} ; \text{init_err_msg})}$ Error Diagnosis

6. Idle $\xrightarrow{\text{begin_monitoring} / \text{begin_experiments}}$ Monitoring

7. Idle $\xrightarrow{\text{idle_crash} / \text{idle_err_msg}}$ Error Diagnosis

8. Monitoring $\xrightarrow{\text{monitor_crash} [\text{inlockdown}=\text{false}] / \text{moni_err_msg}}$ Error Diagnosis

9. Error Diagnosis $\xrightarrow{\text{retry_init} [\text{retry}<3] / \text{retry}++}$ Init

10. Error Diagnosis $\xrightarrow{\text{idle_rescue} / \text{idle_error_protocol}}$ Idle

11. Error Diagnosis $\xrightarrow{\text{moni_rescue} / \text{moni_error_protocol}}$ Monitoring

12. Error Diagnosis $\xrightarrow{\text{shutdown} [\text{retry} \geq 3] / \text{graceful_shutdown}}$ Safe Shutdown

13. Safe Shutdown $\xrightarrow{\text{sleep}}$ Dormant

Init - Formal Specification

$Q = \{\text{boot_hw}, \text{senchk}, \text{tchk}, \text{psychk}, \text{ready}\}$

$\Sigma_1 = \{\text{hw_ok}, \text{senok}, \text{t_ok}, \text{psy_ok}\}$

$\Sigma_2 = \{\}$

$q_0 = \text{boot_hw}$

$V: \{\}$

\wedge : Transitions specifications

1. $\rightarrow \text{boot_hw}$

2. $\text{boot_hw} \xrightarrow{\text{hw_ok}} \text{senchk}$

3. $\text{senchk} \xrightarrow{\text{senok}} \text{tchk}$

4. $\text{tchk} \xrightarrow{\text{t_ok}} \text{psychk}$

5. $\text{psychk} \xrightarrow{\text{psi_ok}} \text{ready}$

Monitoring - Formal Specification

$Q = \{\text{monidle}, \text{regulate_environment}, \text{lockdown}\}$

$\Sigma_1 = \{\text{no_contagion}, \text{after_100ms}, \text{contagion_alert}, \text{purge_succ}\}$

$\Sigma_2 = \{\text{FACILITY_CRIT_MMSG}, \text{inlockdown} = \text{false}, \text{inlockdown} = \text{true}\}$

$q_0 = \text{monidle}$

$V: \text{inlockdown} = \{\text{true}, \text{false}\}$

\wedge : Transitions specifications

1. \rightarrow monidle
2. monidle $\xrightarrow{\text{no_contagion}}$ regulate_environment
3. regulate_environment $\xrightarrow{\text{after_100ms}}$ monidle
4. regulate_environment $\xrightarrow{\text{contagion_alert / (FACILITY_CRIT_MESG ; inlockdown = true)}}$ lockdown
5. lockdown $\xrightarrow{\text{purge_succ / inlockdown = false}}$ monidle

Error Diagnosis - Formal Specification

$Q = \{ \text{error_rcv}, \text{reset_module_data}, \text{applicable_rescue} \}$

$\Sigma_1 = \{ \text{reset_to_stable}, \text{apply_protocol_rescues} \}$

$\Sigma_2 = \{ \}$

$q_0 = \text{error_rcv}$

$V: \text{error_protocol_def} = \{ \text{true}, \text{false} \}$

\wedge : Transitions specifications

1. \rightarrow error_rcv
2. error_rcv $\xrightarrow{[\text{error_protocol_def} = \text{true}]}$ applicable_rescue
3. error_rcv $\xrightarrow{[\text{error_protocol_def} = \text{false}]}$ reset_module_data
4. applicable_rescue $\xrightarrow{\text{apply_protocol_rescues}}$ exit
5. reset_module_data $\xrightarrow{\text{reset_to_stable}}$ exit

Lockdown - Formal Specification

$Q = \{ \text{prep_vpurge}, \text{alt_temp}, \text{alt_psy}, \text{risk_assess}, \text{safe_status} \}$

$\Sigma_1 = \{ \text{initiate_purge}, \text{tcyc_comp}, \text{psicyc_comp} \}$

$\Sigma_2 = \{ \text{lock_doors}, \text{unlock_doors} \}$

$q_0 = \text{prep_vpurge}$

$V: \text{risk}: \mathbb{N}_0$

\wedge : Transitions specifications

6. $\longrightarrow \text{prep_vpurge}$

7. $\text{prep_vpurge} \xrightarrow{\text{initiate_purge} / \text{lock_doors}} \text{alt_temp}$

8. $\text{alt_temp} \xrightarrow{\text{tcyc_comp}} \text{risk_assess}$

9. $\text{prep_vpurge} \xrightarrow{\text{initiate_purge} / \text{lock_doors}} \text{alt_psy}$

10. $\text{alt_psy} \xrightarrow{\text{psicyc_comp}} \text{risk_assess}$

11. $\text{risk_assess} \xrightarrow{[\text{risk} > 1\%]} \text{prep_vpurge}$

12. $\text{risk_assess} \xrightarrow{[\text{risk} < 1\%] / \text{unlock_doors}} \text{safe_status}$

13. $\text{safe_status} \longrightarrow \text{exit}$