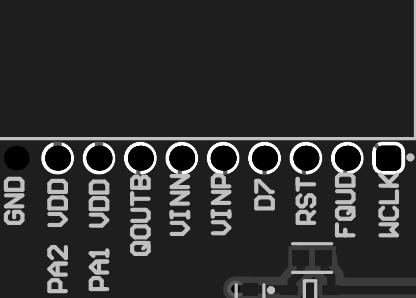


GND

+3.3V



DDS OUT

PA1 OUT

PA2 OUT

3.7V 12.0V

BYP

GATE
GND

RF OUT+

RF OUT-

GND

GND
GND

UART TX

RST

SWCLK

SDIO

+12V

+5V

+3.3V

+3.7V

UART TX

RST

SWCLK

SDIO

+12V

+5V

+3.3V

+3.7V

UART TX

RST

SWCLK

SDIO

+12V

+5V

+3.3V

+3.7V

+12V