

result1



HP2 —

HP23 —

CortexToDDR —

HP2 —

HP23

CortexToDDR —

SPW_Acquisition1 —

FFT2 —

I2Cstorage1

SPWAcquisition2

12Cstorage2

FFT1

Supervisor +

100

100

1
 2
 3
 4

140000 1