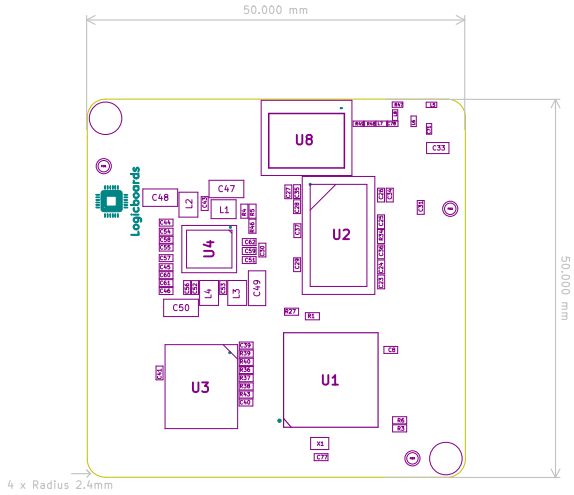

PCB SPECIFICATIONS:

A. MATERIAL : FR-4(TG-150)
B. SOLDERMASK COLOR : MATT BLACK
C. SILKSCREEN COLOR : WHITE
D. SURFACE FINISH : ENIG (GOLD)
E. IMPEDANCE CONTROL: NA
F. VIAS : SEE DRILL TABLE
G. STACKUP : SEE STACKUP TABLE

MINIMUM PARAMETERS:

DEFAULT:
TRACE WIDTH : 3.5 MILS
CLEARANCE : 3 MILS
VIA SIZE : 15.75 MILS
VIA DRILL : 7.9 MILS
uVIA SIZE : 10.85 MILS
uVIA DRILL : 3.95 MILS
dPAIR WIDTH : 5 MILS
dPAIR GAP : 4 MILS

LAYER STACKUP:

LAYER	NAME	MATERIAL	THICKNESS	CONSTANT
1	Top Overlay	–	–	NA
2	Top Solder	Solder Resist	0.020 mm	3.5
3	Top Layer	Copper	0.035 mm	–
4	Dielectric 1	FR-4	0.11 mm	4.29
5	Signal Layer 1	Copper	0.035 mm	–
6	Dielectric 2	FR-4	0.6 mm	4.29
7	Signal Layer 2	Copper	0.035 mm	–
8	Dielectric 3	FR-4	0.11 mm	4.29
9	Signal Layer 3	Copper	0.035 mm	–
10	Dielectric 4	FR-4	0.11 mm	4.29
11	Signal Layer 4	Copper	0.035 mm	–
12	Dielectric 5	FR-4	0.6 mm	4.29
13	Bottom Layer	Copper	0.035 mm	–
14	Bottom Solder	Solder Resist	0.020 mm	3.5
15	Bottom Overlay	–	–	NA

Total Board Thickness – 1.6mm
Tolerance – 10%
Note – Manufacturer follow their own standard 1.6mm thickness stackup.

DRILL TABLE:

COUNT	HOLE SIZE	PLATED	DRILL LAYER	HOLE TYPE
547	0.201 mm	PTH	TopL – BottomL	Round
2	2.200 mm	PTH	TopL – BottomL	Slot
100	0.100 mm	PTH	TopL – Signal11	Round
25	0.100 mm	PTH	BottomL – Signal14	Round

Refer Drill_Report.txt

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Logic Boards
Sheet:
File: LB1A_DCA7M4_R512MB_F4GB.kicad_pcb

Title: LOGIC BOARD 1A (LB1A_DCA7M4_R512MB_F4GB)

Size: A4	Date: 2021-06-28	**Rev: 1.0.0**
KiCad E.D.A. kicad 5.1.10-88a1d61d5889ubuntu20.04.1		Id: 1/1