

STM32MP157 EVAL

MB1263

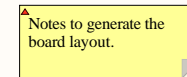
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Legend

New in RevC : OSC X6, R100/R900/R901 pull-up, CN3 3x2

Text to be added to silkscreen.
Warning text.



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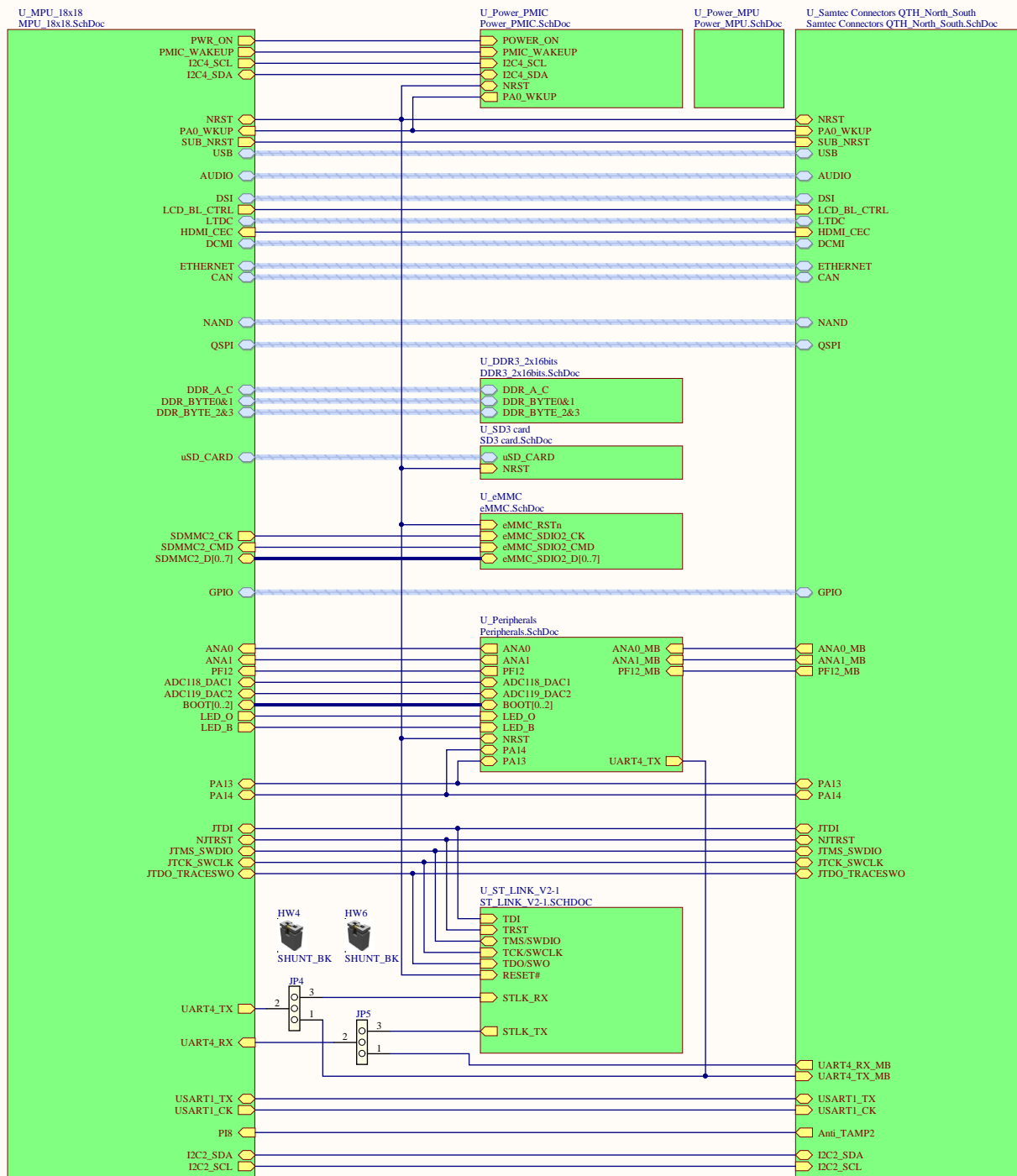
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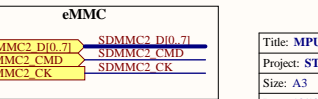
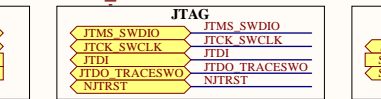
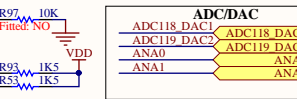
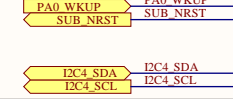
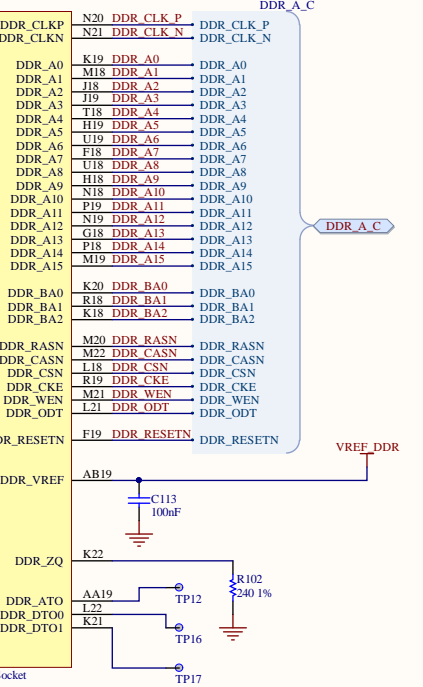
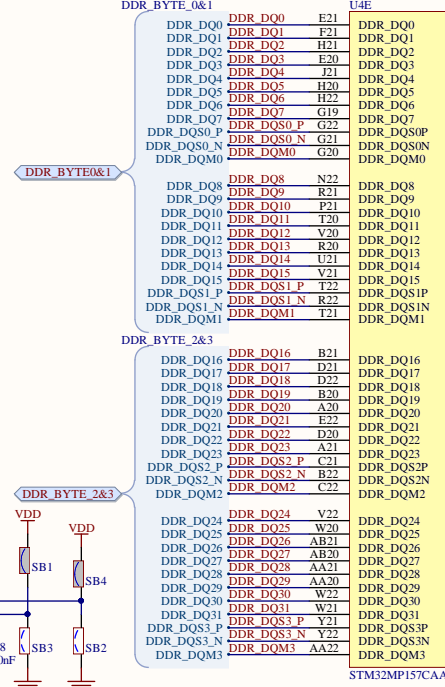
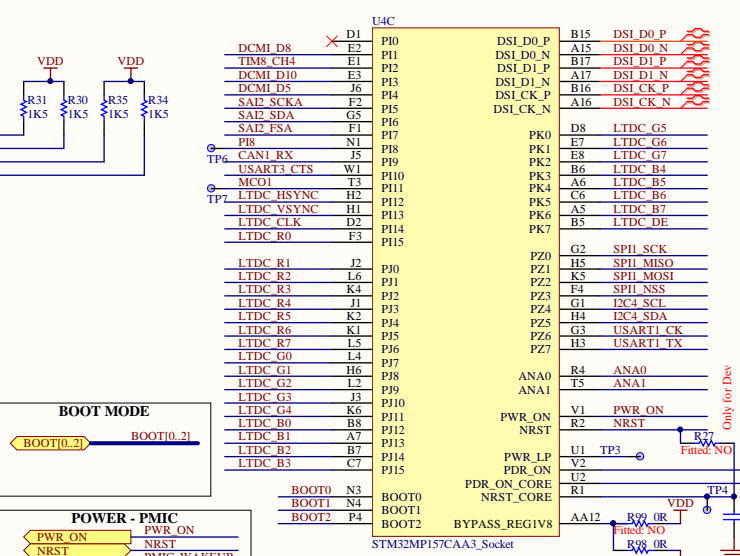
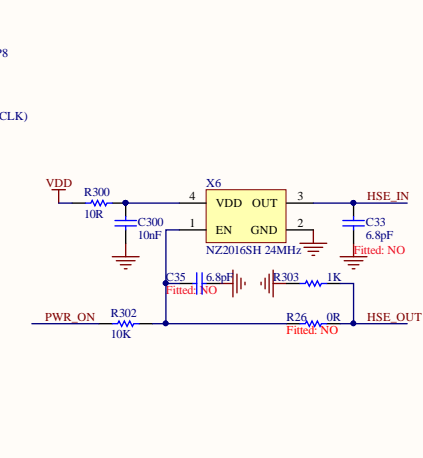
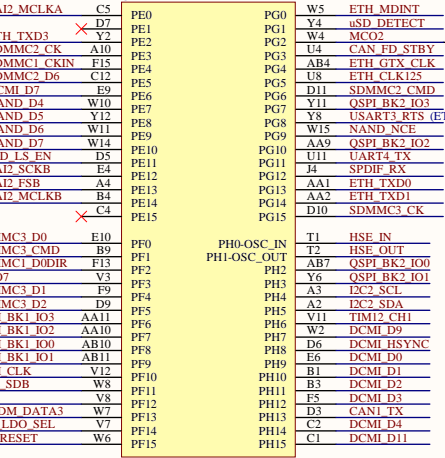
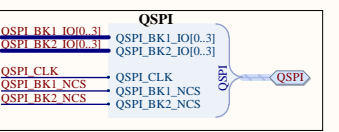
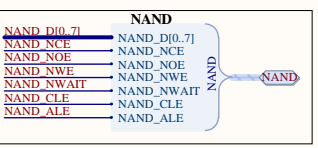
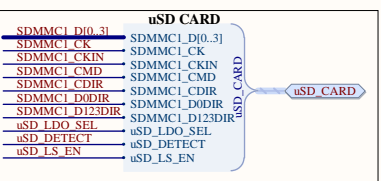
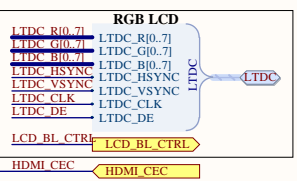
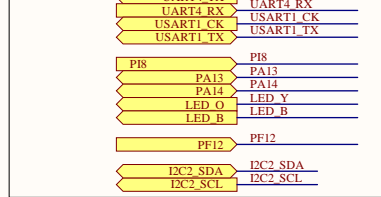
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MB1263-TOP.SchDoc

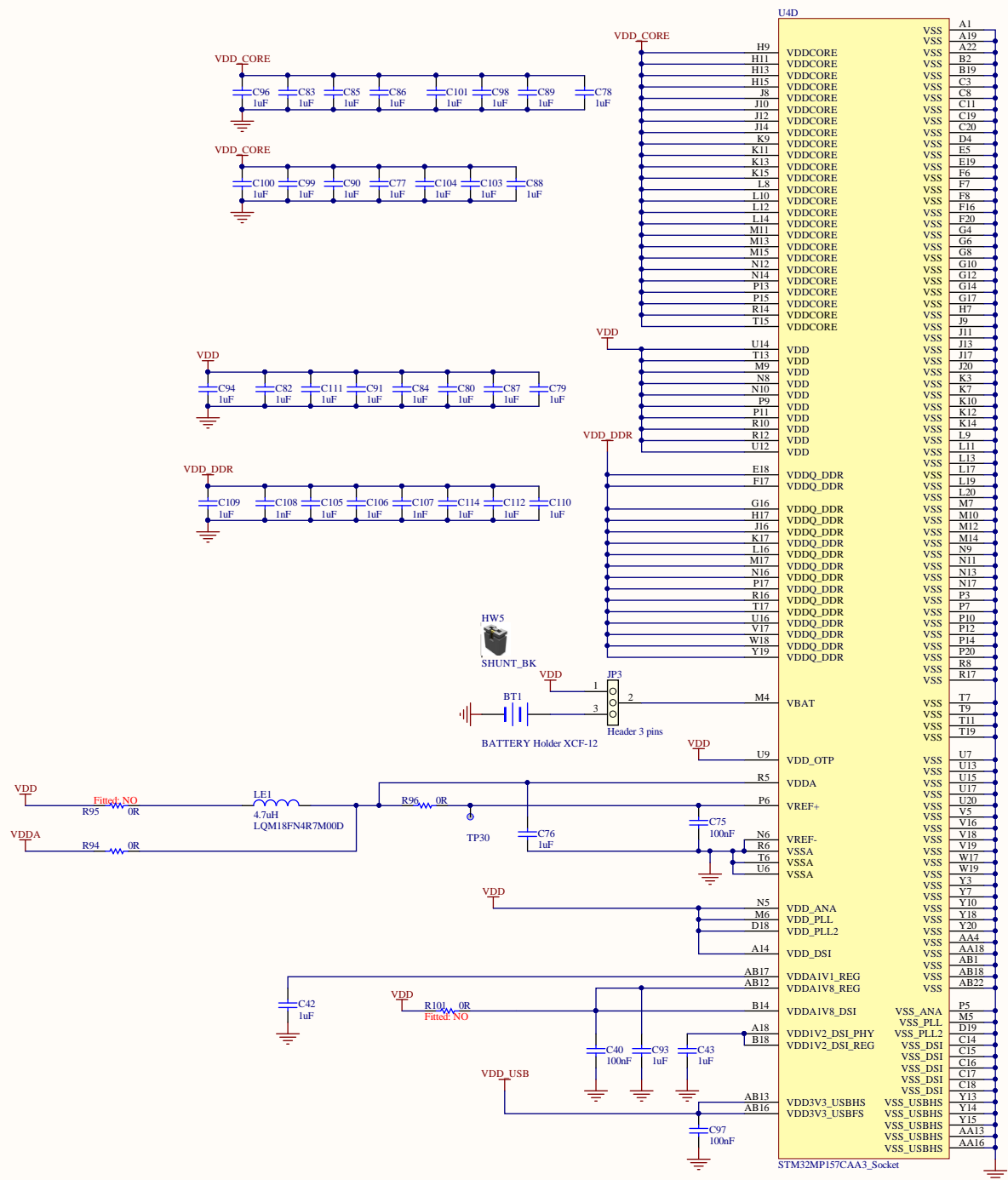


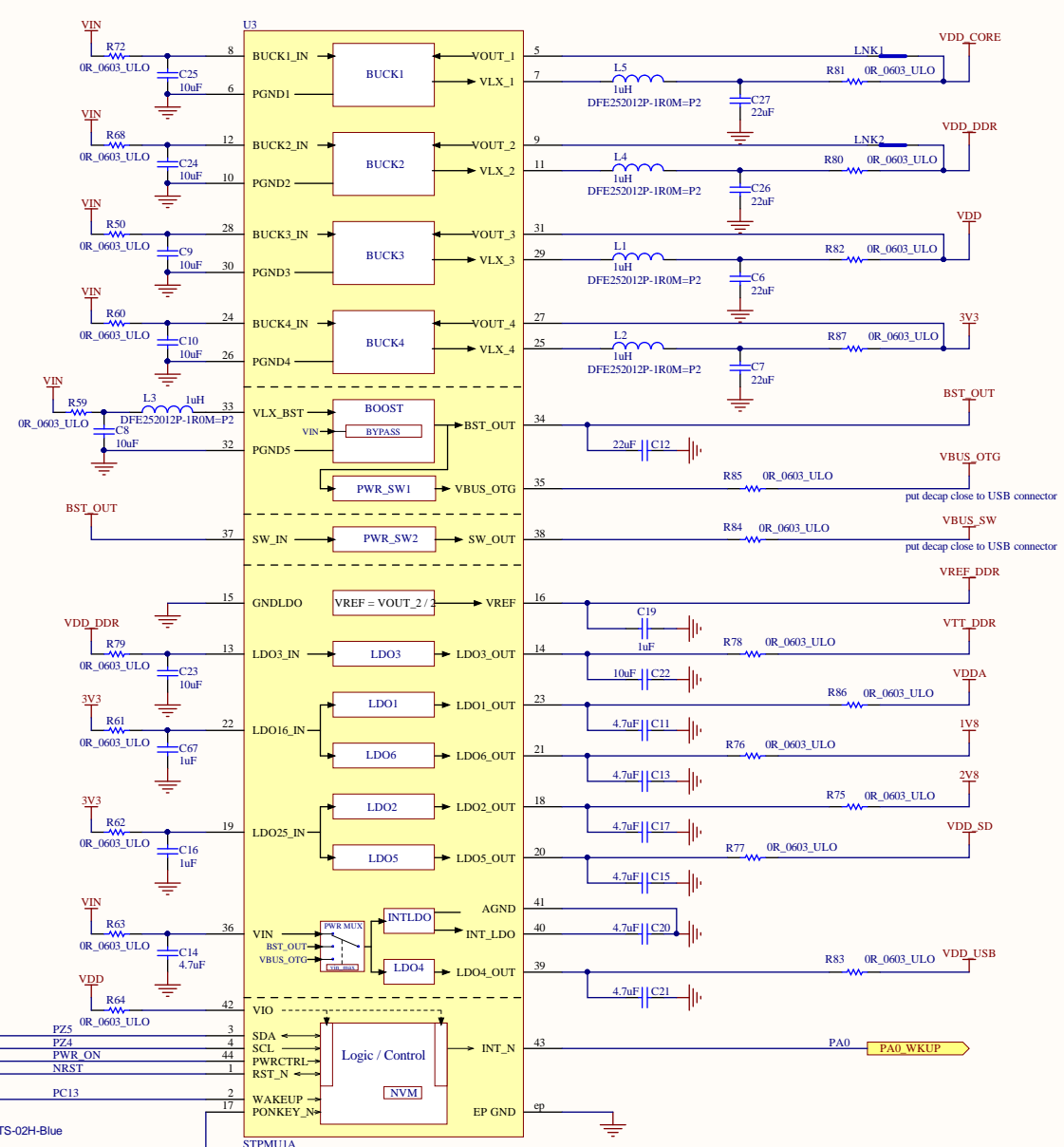
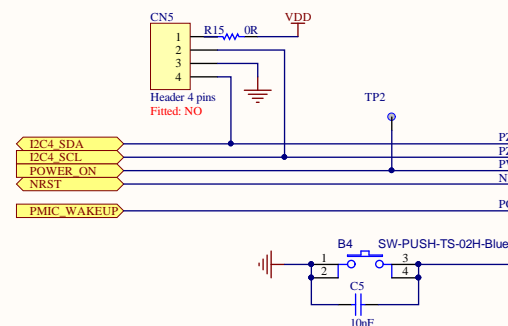
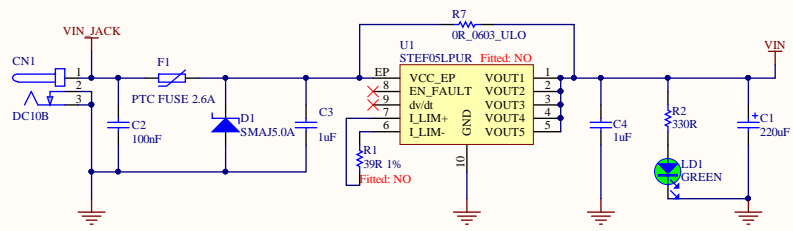
Title: MB1263	
Project: STM32MP157 EVAL	
Variant: [No Variations]	
Revision: C-01	Reference: MB1263
Size: A4	Date: 14-SEP-18 Sheet: 1 of 11

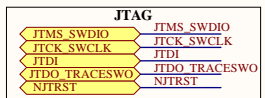
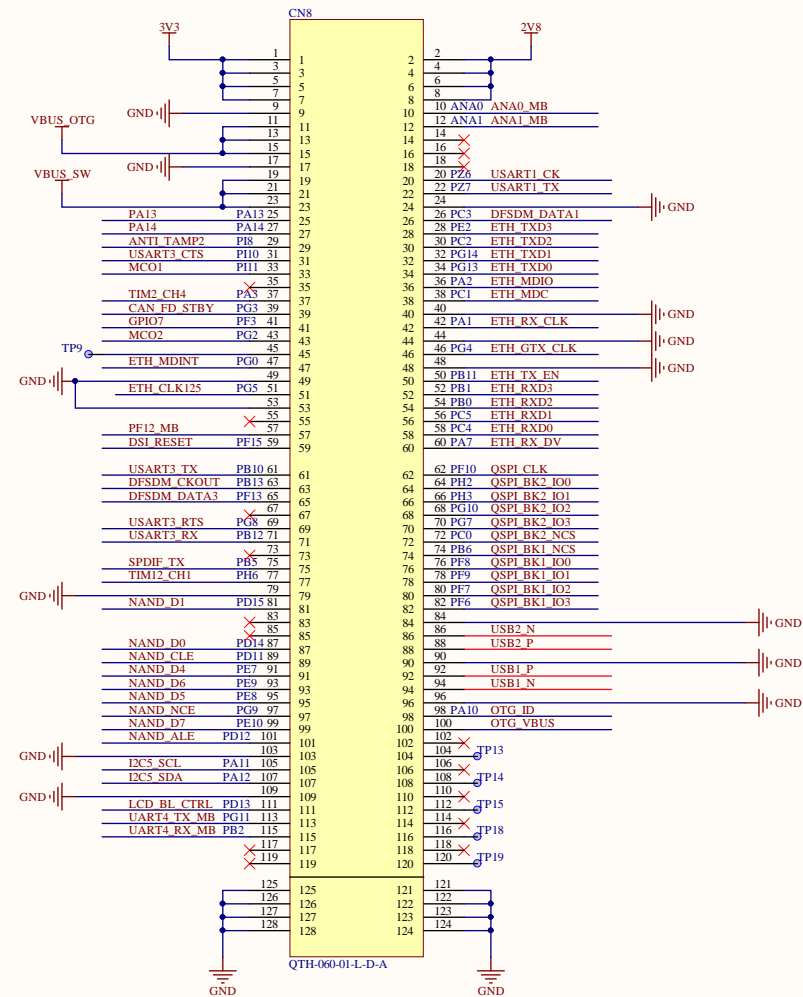
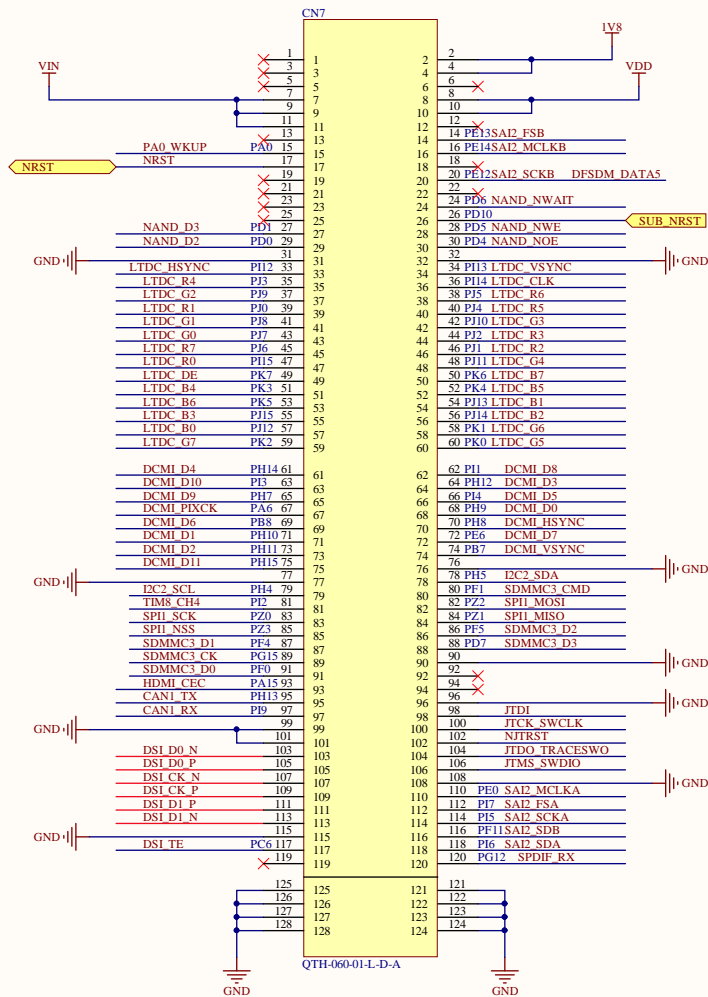
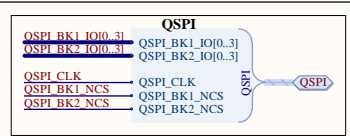
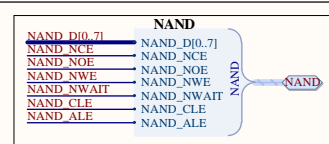
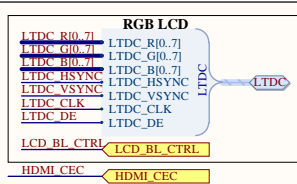
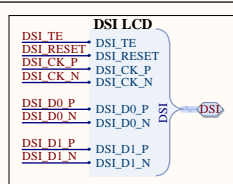
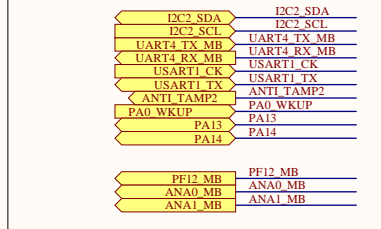
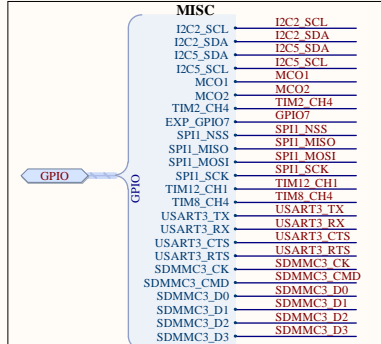
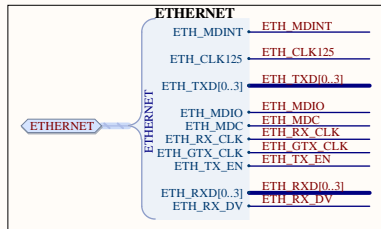
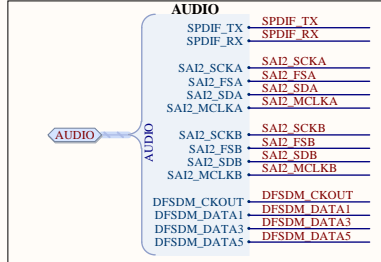
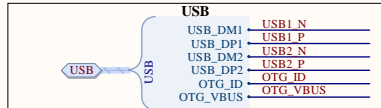
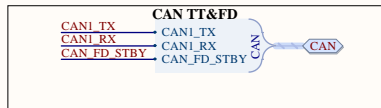
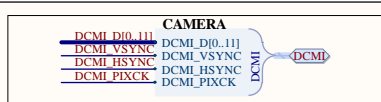




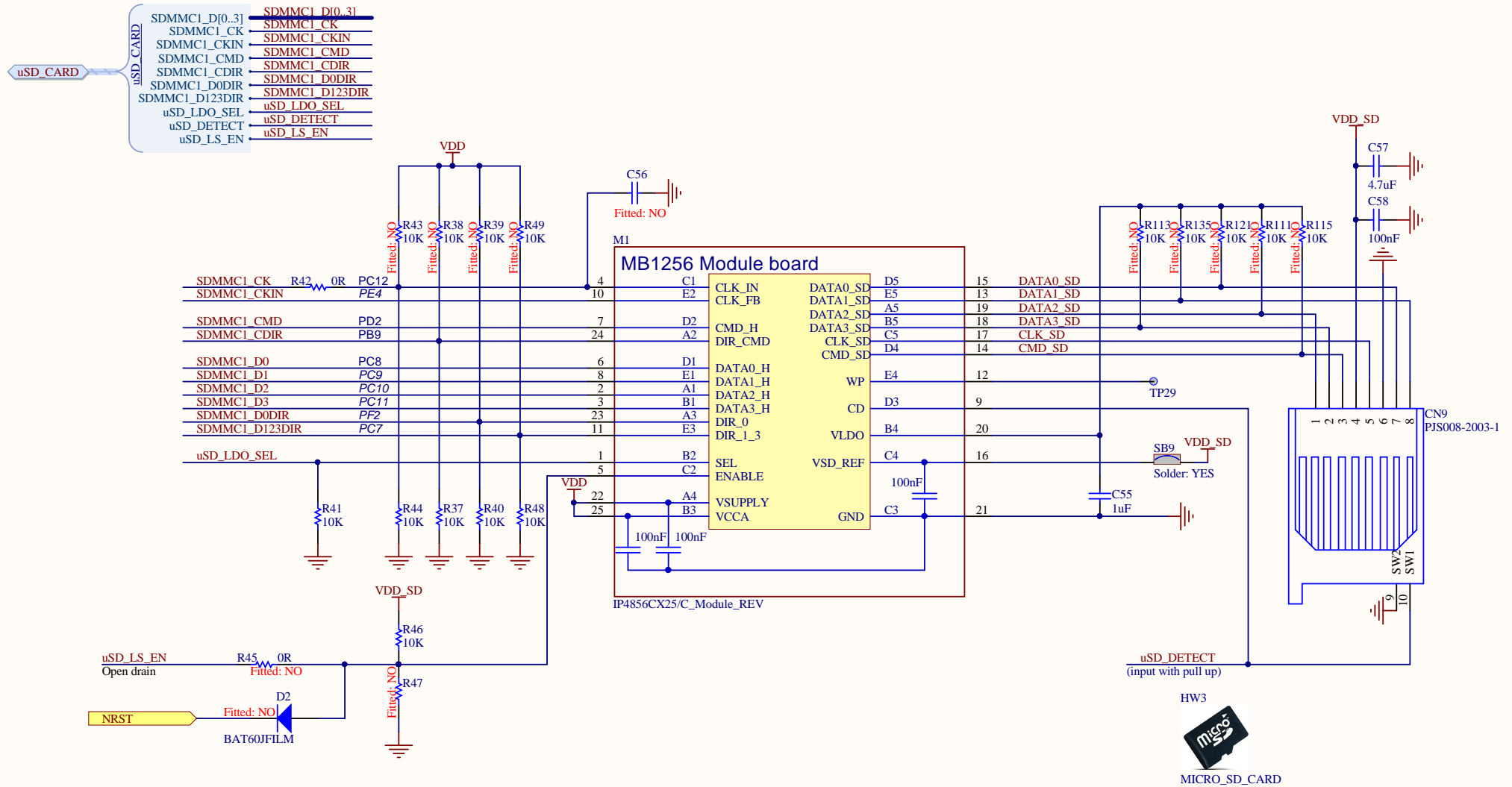


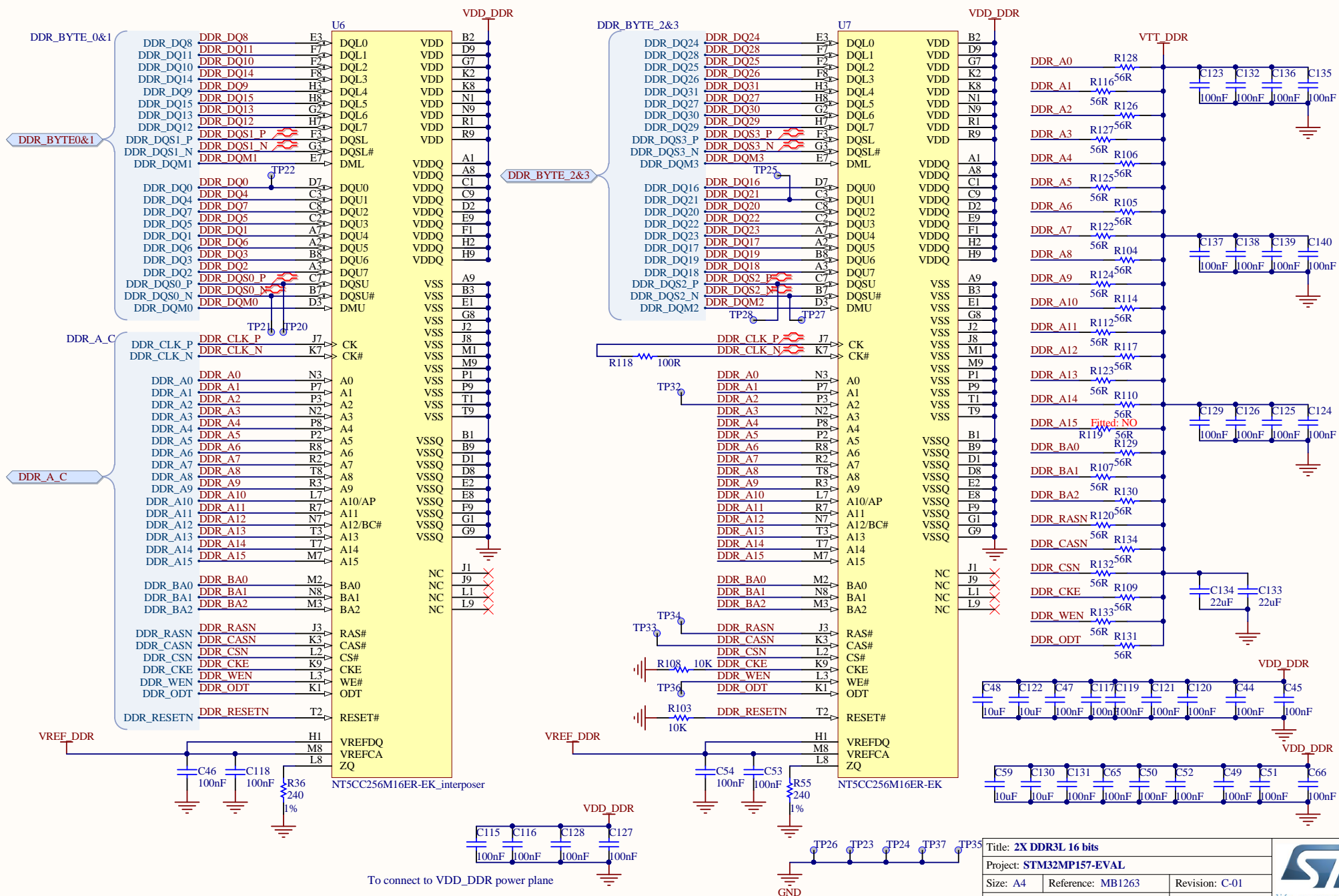




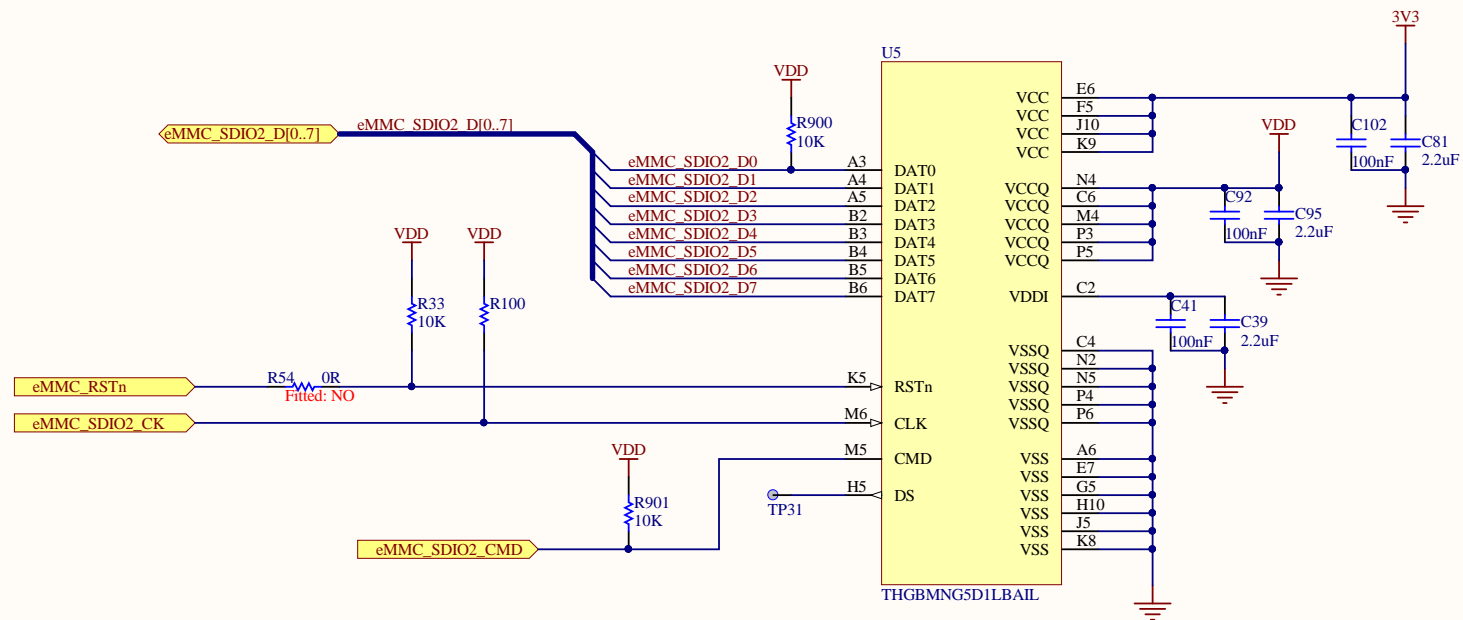



MICRO SD 3.0 Card



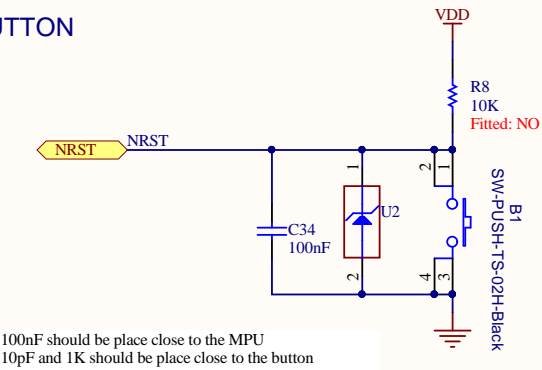


eMMC

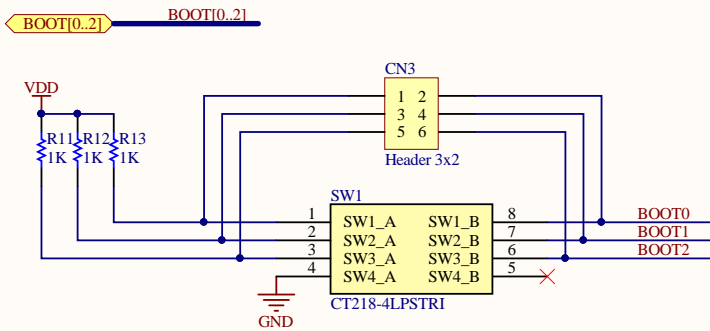


Title: eMMC			 life.augmented
Project: STM32MP157-EVAL			
Size: A4	Reference: MB1263	Revision: C-01	
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RESET BUTTON



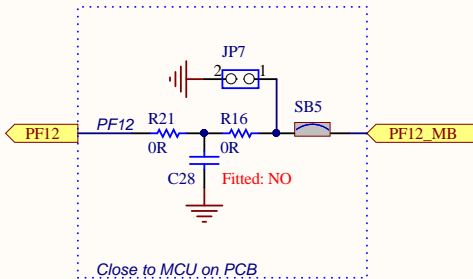
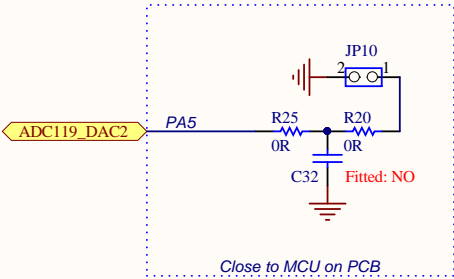
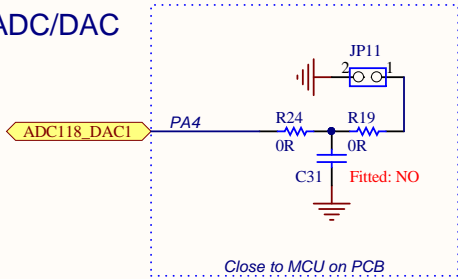
BOOT MODE



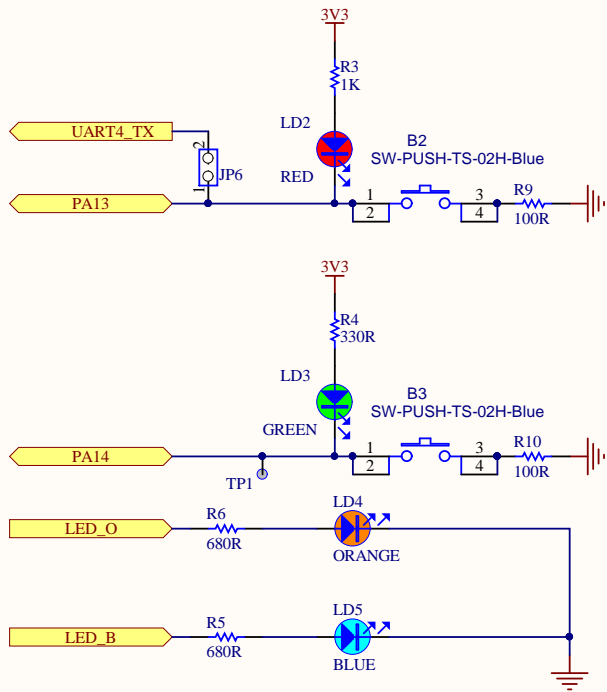
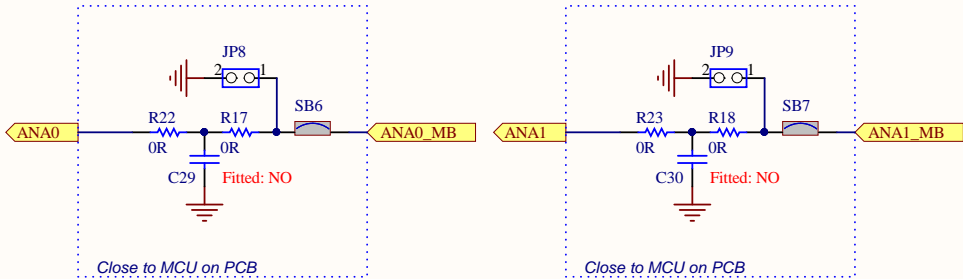
Boot Mode	BOOT2	BOOT1	BOOT0
Reserved	1	0	0
NOR	0	0	1
NAND	1	1	1
eMMC	0	1	0
SD-Card	1	0	1
Forced UART/USB boot	1	1	0

"0" is floating signal or grounded
"1" is signal with pullup or tied to VDD

ADC/DAC



Fast ADC



HW2

PCB

mb1263 PCB 6L

Title: Peripherals		
Project: STM32MP157-EVAL		
Size: A4	Reference: MB1263	Revision: C-01
Date: 10/19/2018	Sheet: 11 of 11	

