JAYANTH (JAY) DEVANATHAN

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EDUCATION

Georgia Institute of Technology, Atlanta, Georgia

August 2014 - May 2018

Candidate for Bachelor of Science in Computer Science, GPA: 4.0

- Taken courses in Object-Oriented Programming(CS 1331), Calculus, and Linear Algebra
- Currently taking courses in Data Structures and Algorithms(CS 1332) and Multivariable Calculus

Thomas Jefferson High School for Science and Technology, Alexandria, Virginia September 2010 - June 2014

- Taken courses in AP Computer Science, Artificial Intelligence, Parallel Computing, Computer Systems Research
- Solid background in software development techniques such as implementing sorting algorithms (heap, quick, merge sort) and data structures (trees, sets, maps, recursion, arrays, linked lists, stacks, queues), Big-O analysis, Image Processing, Heuristics, Neural Networks
- National Merit Scholar Finalist
- Weighted GPA: 4.45/5.0

TECHNICAL SKILLS

Experienced in Java and HTML

Familiar with C, C++, Python, MATLAB, Verilog, Android Studio, and Xcode

RESEARCH EXPERIENCE

FPGA Security Research

January 2015 - May 2015

Research Intern

- Designed and implemented information control flow policies on field gate programmable arrays.
- Implemented policies in Verilog for hardware oriented information secure solutions.

Aspiring Scientists Summer Internship Program, Fairfax, Virginia Research Intern

June 2012 - August 2014

- Co-authored a paper published in IEEE Scientific Journal with Dr. Siddhartha Sikdar
- Developed an arm prosthetic software in MATLAB that used image processing to analyze muscle ultrasound movement with a 90% accuracy
- Coded a virtual hand in C to emulate prostheses software output
- Designed and submitted ultrasound data collection methods for IRB approval on 20 subjects

LEADERSHIP, HONORS, AND EXTRACURRICULAR ACTIVITIES

Freshman Servant Leadership Organization (FSLO) Treasurer (2014 – 2015), Financial Director (2015)

September 2014 - Present

Manage a \$2,000 budget for service projects and events in the Atlanta community.

GTMUN Conference, Director of Plenary General Assembly

August 2014 – October 2014

Directed a 100-person committee at Georgia Tech's annual Model UN Conference.

Georgia Tech Model United Nations (GTMUN) Competitive Team, Member

September 2014 - Present

Selected to represent Georgia Tech in Model UN conferences across the nation.

Science Fair: Digit Detection Using Ultrasound Imaging of Muscle Activity

September 2012 - April 2013

- 1st Place- Fairfax Regional Science Fair(2013)
- Grand Prize Nominee Fairfax Regional Science Fair (2013)
- 1st Place TJHSST Science Fair (2013)

Growth and Inspiration through Volunteering and Education (GIVE) - Tutor, Web Designer, Manager of Centreville Branch

September 2012 - June 2014

- Managed a tutoring center in Centreville for children in elementary and middle schools.
- Met 2 hours a week during the summer for setting up an automatic emailing system and designing the GIVE website.

Latin Honor Society, Member (2012-2013), Webmaster (2013-2014)

September 2012 - June 2014

- 2nd in Country NJCL Website Contest
- 3rd in Country NCJL Dramatic Interpretation Advanced Prose