

Seat No.: \_\_\_\_\_

Enrolment No. \_\_\_\_\_

**GUJARAT TECHNOLOGICAL UNIVERSITY**

**BE - SEMESTER-V (NEW) - EXAMINATION – SUMMER 2018**

**Subject Code:2150707**

**Date:30/04/2018**

**Subject Name:Microprocessor and Interfacing**

**Time:02:30 PM to 05:00 PM**

**Total Marks: 70**

**Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

		MARKS
<b>Q.1</b>	(a) Answer the following questions:	<b>03</b>
	(1) How many machine cycles are executed by 8085 microprocessor? List down it.	
	(2) How many flags are available in 8085 microprocessor? List down each.	
	(3) List down various segment registers of 8086 microprocessor.	
	(b) Differentiate: (1) higher level language and low level language (2) hardware and software interrupt	<b>04</b>
	(c) Draw and explain the internal block diagram of 8085 microprocessor.	<b>07</b>
<b>Q.2</b>	(a) What are the addressing capacity of 8085 microprocessor and 8086 microprocessor?	<b>03</b>
	(b) Write an 8085 program to add two 16-bit nos stored in memory locations 2100H and 2200H respectively.	<b>04</b>
	(c) Explain 8085 data transfer instructions with suitable examples.	<b>07</b>
	<b>OR</b>	
	(c) Explain 8085 branch instructions with suitable examples.	<b>07</b>
<b>Q.3</b>	(a) How many flags are available in an 8086 microprocessor? How the parity flag (PF) is used by 8086 microprocessor?	<b>03</b>
	(b) Explain stack and subroutine with suitable example.	<b>04</b>
	(c) Explain various addressing modes of 8085 microprocessor.	<b>07</b>
	<b>OR</b>	
<b>Q.3</b>	(a) How many I/O modes are available in 8255A programmable peripheral interface? List and explain them in short.	<b>03</b>
	(b) Differentiate 8085 microprocessor with 8086 microprocessor.	<b>04</b>
	(c) Explain programmable interrupt controller 8259A in detail.	<b>07</b>
<b>Q.4</b>	(a) Draw timing diagram for an arithmetic instruction: MOVE A, B.	<b>03</b>
	(b) Write an assembly language program to find the factorial of a number from 0 to 10.	<b>04</b>
	(c) Draw and explain the architecture of Pentium processor.	<b>07</b>
	<b>OR</b>	
<b>Q.4</b>	(a) What are the main features of ARM processor? List down each.	<b>03</b>
	(b) How the physical addresses are calculated from segment register in 8086 microprocessor?	<b>04</b>
	(c) Draw and explain internal architecture diagram of 8086 microprocessor.	<b>07</b>
<b>Q.5</b>	(a) What is page table? How it works?	<b>03</b>
	(b) Explain arithmetic instruction of SUN SPARC with example.	<b>04</b>
	(c) Differentiate 80286 with 80386 microprocessor.	<b>07</b>

**OR**

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|------------|---|-----------|
| <b>Q.5</b> | (a) What is a descriptor table? What is its main usage?               | <b>03</b> |
|            | (b) Explain branch instruction of SUN SPARC with example.             | <b>04</b> |
|            | (c) Draw and explain SUN SPARC microprocessor architecture in detail. | <b>07</b> |

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