GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-V (NEW) - EXAMINATION - SUMMER 2016

Subject Code:2150707 Date:09/05/201			6
•	e:02	Name: Microprocessor and Interfacing 2:30 PM to 05:00 PM Total Marks: 7 ns: Attempt all questions. Make suitable assumptions wherever necessary.	0
	3.	Figures to the right indicate full marks.	
Q.1	a)	Draw the functional block diagram of internal architecture of IC 8085 and Explain its working.	07
	b)	Write an 8085 program to copy block of five numbers starting from location 2001h to locations starting from 3001h.	07
Q.2	a)b)	Explain the Architecture of Pentium Processor. List and Explain categories of 8085 instructions that deal with data transfer. OR	07 07
	b)	Explain the working of rotate instructions of 8085 with proper example in each case.	07
Q.3	a)	Draw and explain block diagram of 8255A programmable peripheral interface device. What do you mean by BSR mode?	07
	b)	How many machine cycles are required to execute MVI A,32H instruction? Draw compete timing diagram with each machine cycle and find execution time for instruction with assume clock frequency f=2 MHz OR	07
Q.3	a)b)	Draw and explain programmable interrupt controller 8259A. How many machine cycles are required to execute MOV r,M instruction? Draw compete timing diagram with each machine cycle and briefly explain it.	07 07
Q.4	a)b)	Write ALP to calculate the factorial of a number between 0 to 8. Explain RIM and SIM instructions with pseudo code example. OR	07 07
Q.4	a)	Write ALP for displaying binary up counter. Counter should count numbers from 00H to FFH and it should increment after every 0.5 sec. (use 8085 operating frequency =2MHz)	07
	b)	Explain subroutine with proper example.	07
Q.5	a)b)	Explain the architecture of the 80386 with a neat block diagram. Discuss the features of ARM Processor. OR	07 07
Q.5	a) b)	Describe the architecture of the 80286 with a neat block diagram. Draw and explain architecture of SUN SPARC.	07 07
