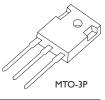
## **HIGH VOLTAGE • HIGH SPEED SWITCHING TRANSISTORS**



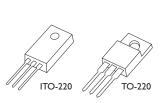


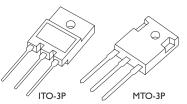


#### **HDT** series

Bipolar transistors (NPN)

			Abso	lute Max	imum R	atings													
Туре No.	VRM	VCEO	VEBO	lc	lв	Рт	Tstg	Tj	VCEO (sus)	hFE	VCE (sat)	VBE (sat)	θјс	fī	ton	ts	tf	Outli	ne
	[V]	[V]	[V]	[A]	[A]	[W]	[°C]	[°C]	(min) [V]	(min)	(max) [V]	(max) [V]	[°C/W]	(typ) [MHz]	(max) [µs]	(max) [µs]	(max) [µs]	Package	Figure
	[1]	L*J	[,]	[/-]	[/]	[,,]	[ ~]	[ ]	[,]		[,]	[1]	[ C/ 11]	[1 11 12]	[µ3]	[իս]	[µ3]		
2SC4310						50							2.5					TO-220	80-3
4311				6	3	40	-55						3.1					ITO-220	82-4
4312	900	800	7			80	≀	150	800	7	1.5	2.5	1.56	7	0.5	3	0.5		
4313				10	4	100	150						1.25					MTO-3P	86-2
4314				15	6	130							0.96						

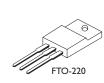




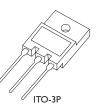
#### **HFX** series

Bipolar transistors (NPN)

			Abso	lute Max	imum R	atings						Elec	trical Cha	aracterist	ics				
Туре No.	VRM	VCEO	VEBO	lc	lв	Рт	Tstg	Tj	VCEO (sus)	hFE	VCE (sat)	VBE (sat)	θјс	fT	ton	ts	tf	Outli	ne
	[V]	[V]	[V]	[A]	[A]	[W]	[°C]	[°C]	(min) [V]	(min)	(max) [V]	(max) [V]	(max) [°C/W]	(typ) [MHz]	(max) [µs]	(max) [µs]	(max) [µs]	Package	Figure
2SC4230						50							2.5					TO-220	80-3
4231	]			2		30							4.16					ITO-220	82-4
4232	]				١.	70							1.7					MTO-3P	86-2
4233	]					60							2.08					TO-220	80-3
4234				3		45	-55						2.77					ITO-220	82-4
4235	1200	800	7			80	₹	150	800	8	I	1.5	1.56	8	0.5	3.5	0.3		
4236				6	3	100	150						1.25					MTO-3P	86-2
4237				10	4	150							0.83						
4583				3	I	50							2.5						
4584				6	3	65							1.92					ITO-3P	88-3
4585	1			10	4	85							1.47						







# Switching Transistors for Lighting Equipment Bipolar transistors (NPN)

			Abso	lute Max	imum R	atings			Electrical Characteristics											
Туре No.	VRM	VCEO	VEBO	lc	Ів	Рт	Tstg	Tj	VCEO (sus)	hFE	VCE (sat)	VBE (sat)	θјс	fT	ton	ts	tf	Outli	ne	
									(min)	(min)	(max)	(max)	(max)	(typ)	(max)	(max)	(max)	Package	Figure	
	[V]	[V]	[V]	[A]	[A]	[W]	[°C]	[°C]	[V]		[V]	[V]	[°C/W]	[MHz]	[µs]	[µs]	[µs]	1 ackage	rigure	
2SC4940	1200	550	_	4	2	30	-55		550	10	I		4.16	10	0.8	3.0		ITO-220	82-4	
4941	1500	800	/		_	65	≀	150	800	15	0.5	1.5	1.92	8	0.5	3.5	0.3	ITO-3P	88-3	
5382	1200	550	9	6	3	40	150		550	10	I	1	3.13	_	1.3	4.0		FTO-220	84-3	

### LOW SATURATION VOLTAGE SWITCHING TRANSISTORS







#### LSV series

Bipolar transistors

			Abso	lute Max	imum R	atings						Elec	trical Cha	aracterist	ics				
Type No.	Vсво	VCEO	VEBO	lc	lв	Рт	Tstg	Tj	VCEO (sus) (min)	hFE (min)	VCE (sat) (max)	VBE (sat) (max)	θjc (max)	fT (typ)	ton (max)	ts (max)	tf (max)	Outli	ne
	[V]	[٧]	[V]	[A]	[A]	[W]	[°C]	[°C]	[V]	()	[V]	[V]	[°C/W]	[MHz]	[µs]	[µs]	[µs]	Package	Figure
2SA1795				-5		- 10							12.5						*
1796				-7		10							12.5					E-pack	'
1679				-5	-1.5		-55						5						
1598	-60	<del>-4</del> 0	-7	-7		25	≀	150	-40	70	-0.3	-1.2	5	50	0.3	1.5	0.5		
1599				-10			150						5					ITO-220	82-4
1600				-12		30							4.16						
1601				-15	-2	45							2.77						
2SC4668				7		- 10							12.5						*
4669				10		10							12.5					E-pack	~ I
4148				7	1.5		<b>–55</b>						5						
4149	60	40	7	10		25	≀	150	40	70	0.3	1.2	5	50	0.3	1.5	0.5		
4150	1			12	_		150						5					ITO-220	82-4
4151	1			15	2	30							4.16						
4876				30	4	45				100			2.78						

<sup>\*</sup>I: Leaded package - Fig. 78-3; SMD package - Fig. 77-3







#### **HSV** series

Bipolar transistors

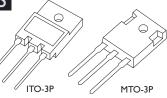
			Abso	lute Max	imum R	atings						Elec	trical Cha	racterist	ics				
Type No.	Vсво	VCEO	VEBO	lc	lв	Рт	Tstg	Tj	VCEO (sus) (min)	hFE	VCE (sat)	VBE (sat)	θјс	fT (true)	ton	ts	tf	Outli	ne
	[V]	[V]	[V]	[A]	[A]	[W]	[°C]	[°C]	(min) [V]	(min)	(max) [V]	(max) [V]	(max) [°C/W]	(typ) [MHz]	(max) [µs]	(max) [µs]	(max) [µs]	Package	Figure
2SA 1876				-3	-1	10							12.5						*
1877				_		10	-55						12.5					E-pack	'
1878	-80	-80	-7	<u>–</u> 5			} ≀	150	-80	70	-0.3	-1.2		50	0.3	1.5	0.2		
1879				-7	-1.5	25	150						5					ITO-220	82-4
1880				-10															
2SC4978				3	I	10							12.5					FI-	*
4979	1			-		10	-55						12.5					E-pack	'
4980	100	80	7	5			≀ ≀	150	80	70	0.3	1.2		50	0.3	1.5	0.2		
4981				7	1.5	25	150						5					ITO-220	82-4
4982				10															

<sup>\*</sup>I: Leaded package - Fig. 78-3; SMD package - Fig. 77-3

## DARLINGTON TRANSISTORS & TRANSISTOR ARRAYS







# **Darlington Power Transistors**Bipolar transistors

			Abso	lute Max	imum Ra	atings						Elec	trical Cha	aracterist	ics				
Type No.	Vсво	VCEO	VEBO	lc	lB	Рт	Tstg	Тј	VCEO (sus) (min)	hFE (min)	VCE (sat) (max)	VBE (sat) (max)	<i>θ</i> jc (max)	fT (typ)	ton (max)	ts (max)	tf (max)	Outli	ne
	[V]	[V]	[V]	[A]	[A]	[W]	[°C]	[°C]	[V]	(111111)	[V]	[V]	[°C/W]	[MHz]	[µs]	[µs]	[µs]	Package	Figure
2SD1022	100	100	7	5	0.5	30		150		1500	1.5	2	4.17	20	_	5	3		
1023	200	200	′	)	0.5	30		150	_	1500	1.5	2	4.17	20	2	8	5	TO-220	80-3
1024	100	100	7	8	0.5	50	-55	150		1500	1.5	2	2.5	20	2	5	3	10-220	00-3
1025	200	200	,	0	0.5	30	} ≀	130		1300	1.5		2.5	20		8	5		
1026	100	100				100	150						1.25			5	3	MTO-3P	86-2
1027	200	200	7	15	I			150	_	1500	1.5	2	1.23	20	2	8	5		
2196	200					65							1.92			12	, ,	ITO-3P	88-3
1788	100	100																	
1789	200	200	7	±4	0.3	25		150	_	1500	1.5	2	5.0	20	2	12	5		
1790	60 <sup>±10</sup>	60 <sup>±10</sup>					<b>–55</b>												
1791	100	100	7	7	0.5	30		150	_	1500	1.5	2	4.17	20	2	12	5	ITO-220	82-4
1792	200	200	,		0.5	30	150	130		1300	1.5		7.17	20		12		110-220	02-4
1793	100	100	7	10	0.5	50	130	150	_	1500	1.5	2	2.5	20	2	12	5		
1794	200	200																	
1795	500	400	12	10	0.5	50		150	400	150	1.5	2	2.5	10	2	15	15		
2SB1282				±4	-0.3	25							5						
1283				<b>–7</b>	-0.5	30	-55						4.16					ITO-220	82-4
1284	-100	-100	-7	-10	-0.8	35	} ≀	150	_	1500	-1.5	-2	3.57	20	I	4	2		
1285				-15	-1	100	150						1.25					MTO-3P	86-2
1448				_13		65							1.92					ITO-3P	88-3

#### **Transistor Arrays**

Bipolar transistors Arrays





			Abs	olute N	1aximur	n Ratin	gs				Electric	al Charac	teristics					
Туре No.	Vсво	VCEO	VEBO	lc	lв	Рт	Tstg	Tj	hFE (min)	VCE (sat)	VBE (sat)	θја	ton	ts	tf	Remarks	Out	line
	[V]	[V]	[٧]	[A]	[A]	[W]	[°C]	[°C]	(min)	(max) [V]	(max) [V]	(max) [°C/W]	(max) [ $\mu$ s]	(max) [µs]	(max) [µs]		Package	Figure
TH3L10	100	100	7								_					N IDN IV/4		
3L20	200	200	/		0.3					1.5	2					NPNX4		
3C10	±100	±100	±7	±3	±0.3					±1.5	±2					NPNX2, PNPX2		
3110	-100	-100	-7		-0.3	3.5	-55~150	150	1500	-1.5	-2	35	2	8	3	PNPX4	TH	65
3L10Z*	60 <sup>±10</sup>	60 <sup>±10</sup>			0.3													
5L10	100	100	7	_	٥.					1.5	2					NPNX4		
5L20	200	200		5	0.5										5			
TK3LI0	100	100	7		0.3					1.5	_					NIDNIVA		
3L20	200	200	/	±3	0.3					1.5	2					NPNX3		
3J10	-100	-100	-7		-0.3	2.5	FF 1F0	150	1500	-1.5	-2	50	,		3	PNPX3	TK	66
3L10Z*	60 <sup>±10</sup>	60 <sup>±10</sup>			0.3	2.5	<b>-55∼150</b>	150	1500			50	2 8	8	3		IK	66
5L10	100	100	7	5	٥.					1.5	2					NPNX3		
5L20	200	200		5	0.5										5			
TH5P4	±60	±40	±7	±5	±1.5	3.5			70	±0.3	±1.2	35.7	0.3	2	0.5	NPNX2, PNPX2	TH	65

<sup>\*:</sup> With zener diode between collector and base